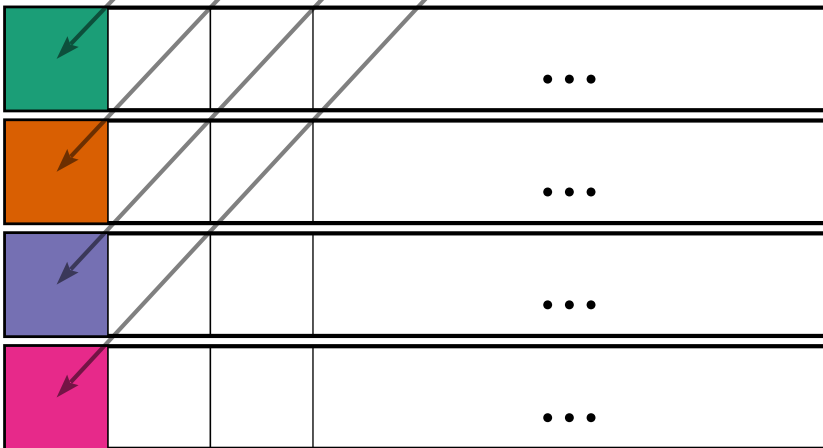
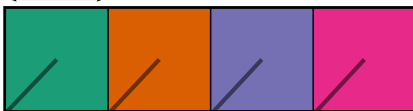


64 bits



16 bits



4
vars

SSE register
8 x 16 bits