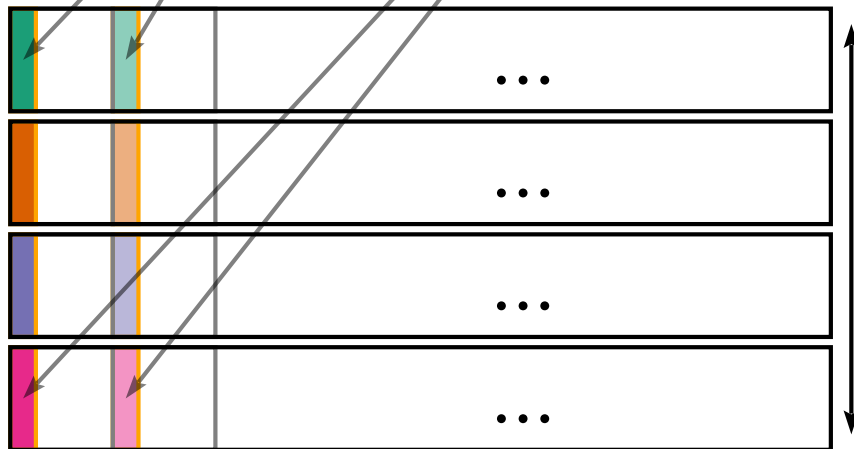


64 bits



16 bits



4  
vars

SSE register  
16 x 8 bits