

# **CSC1104 – Computer Organisation & Architecture**

# Laboratory/Tutorial 1: Components and Performance of Computers

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## Objectives

- Evaluate the program and CPU performance.
- Identify the major components and ports of Raspberry Pi 4 and their function.

# Questions:

1. A program written in Java runs 15 seconds on a processor. A new Java compiler is released that requires only 0.6 as many instructions as the old compiler. Unfortunately, it increases the CPI by 1.1. How fast can this program run with the new compiler?

#### Solution:

lacktriangleq Execution time for old compiler = Instruction count<sub>old</sub> imes CPI<sub>old</sub> imes Clock cycle time.

Hence, 15 seconds = Instruction  $count_{old} \times CPI_{old} \times Clock$  cycle time.

- $\square$  Execution time for new compiler = Instruction count<sub>new</sub> × CPI<sub>new</sub> × Clock cycle time.
- □ Instruction count<sub>new</sub> = 0.6 \* Instruction count<sub>old</sub>

Execution time for old compiler Instruction Count<sub>old</sub>×CPI<sub>old</sub>× Clock cycle time

**CPU** execution time for new compiler =  $15 \times 0.6 \times 1.1 = 9.9$  seconds.

2. Please derive the detailed size of these terms one by one.

$$8 \text{ GHz} = \underline{8,000 (8 \times 10^3)} \text{ MHz} = \underline{8,000,000 (8 \times 10^6)} \text{ KHz} = \underline{8,000,000,000 (8 \times 10^9)} \text{ Hz}$$

$$16 \text{ GHz} = \underline{16 \times 10^3} \text{ MHz} = \underline{16 \times 10^6} \text{ KHz} = \underline{16 \times 10^9} \text{ Hz}$$

$$40 \text{ MHz} = \underline{0.04} \qquad \text{GHz} = \underline{40 \times 10^3} \qquad \text{KHz} = \underline{40 \times 10^6} \qquad \text{Hz}$$

$$600 \text{ MHz} = \underline{0.6} \qquad \text{GHz} = \underline{600 \times 10^3} \qquad \text{KHz} = \underline{600 \times 10^6} \qquad \text{Hz}$$

$$300 \text{ KHz} = \underline{0.0003} \text{ GHz} = \underline{0.3} \text{ MHz} = \underline{300 \times 10^3} \text{ Hz}$$

$$5000 \text{ KHz} = \underline{0.005} \text{ GHz} = \underline{5} \text{ MHz} = \underline{5 \times 10^6} \text{ Hz}$$

$$4 \text{ GiByte} = \underline{4 \times 1024} \text{ MiByte} = \underline{4 \times 1024^2} \text{ KiByte} = \underline{4 \times 1024 \times 1024 \times 1024 \times 1024 \times 1024^3} \text{ Bytes}$$

20 GiByte = 
$$\underline{20 \times 1024}$$
 (20×2<sup>10</sup>) MiByte =  $\underline{20 \times 1024^2}$  (20×2<sup>20</sup>) KiByte =  $\underline{20 \times 1024^3}$  (20×2<sup>30</sup>) Bytes

100 MiByte = 
$$\frac{100}{1024} \frac{(100 \times 2^{-10})}{1024} \frac{\text{GiByte}}{100 \times 2^{-10}} = \frac{100 \times 1024 (100 \times 2^{10})}{100 \times 2^{-10}} \text{ KiByte} = \frac{100 \times 1024^2 (100 \times 2^{-10})}{100 \times 2^{-10}} \text{ Bytes}$$

90 KiByte = 
$$\frac{90}{1024 \times 1024} \frac{(90 \times 2^{-20})}{1024 \times 1024} \frac{(90 \times 2^{-20})}{1024} \frac{(90 \times 2^{-10})}{1024} \frac{(90 \times 2^{-10$$

$$1 \ second = \underline{1000} \quad ms = \underline{1 \times 10^6} \quad \mu s = \underline{1 \times 10^9} \quad ns = \underline{1 \times 10^{12}} \quad ps$$

$$200 \text{ ms} = 0.2 \quad \text{s} = 200 \times 10^3 \quad \text{µs} = 200 \times 10^6 \quad \text{ns} = 200 \times 10^9 \quad \text{ps}$$

$$150 \ \mu s = 150 \times 10^{-6} \ s = 150 \times 10^{-3} \ ms = 150 \times 10^{3} \ ns = 150 \times 10^{6} \ ps$$

600 ns = 
$$\underline{600 \times 10^{-9}}$$
 s =  $\underline{600 \times 10^{-6}}$  ms =  $\underline{600 \times 10^{-3}}$   $\mu$ s =  $\underline{600 \times 10^{3}}$  ps

$$800 \text{ ps} = 800 \times 10^{-12}$$
  $\text{s} = 800 \times 10^{-9}$   $\text{ms} = 800 \times 10^{-6}$   $\text{\mu s} = 800 \times 10^{-3}$   $\text{ns}$ 

3. Using a typical benchmark program running on 2 computers, the following machine characteristics result:

Computer	Clock Rate (MHz)	Performance (MIPS)	CPU Execution Time (secs)
A	5	1	12x
В	25	18	x

The final column shows that the Computer A required 12 times longer than the Computer B measured in CPU execution time.

- a. What is the relative size of the instruction count of the machine code for this benchmark program running on the two machines?
- b. What are the CPI values for the two machines?

### Solution:

According to the MIPS rate equation as follows:

$$\frac{\text{MIPS rate}}{\text{Execution time} \times 10^6} = \frac{1}{\text{CPI} \times \text{Clock Cycle time} \times 10^6} = \frac{\frac{\text{Clock Rate}}{\text{CPI} \times 10^6}}{\text{CPI} \times 10^6}$$

So, we can calculate instruction count and CPI on these 2 computers.

Instruction count<sub>A</sub> = MIPS rate<sub>A</sub>× Execution time<sub>A</sub> ×  $10^6$  =  $1 \times 12x \times 10^6$  =  $12x \times 10^6$ .

Instruction count<sub>B</sub> = MIPS rate<sub>B</sub>× Execution time<sub>B</sub> ×10<sup>6</sup> = 18×x ×10<sup>6</sup> = 18x ×10<sup>6</sup>.

Instruction count<sub>B</sub> / Instruction count<sub>A</sub> =  $18x \times 10^6 / 12x \times 10^6 = 1.5$ .

$$CPI_A = Clock \ Rate_A / (MIPS \ rate_A \times 10^6) = 5 \times 10^6 / (1 \times 10^6) = 5.$$

 $CPI_B = Clock \ Rate_B / (MIPS \ rate_B \times 10^6) = 25 \times 10^6 / (18 \times 10^6) = 1.389.$ 

4. Consider two different computers, with two different instruction sets, both of which have a clock rate of 200 MHz. The following measurements are recorded on the two machines running a given set of benchmark programs:

Instruction Type	Instruction Count (millions)	Cycles per Instruction		
Computer A				
Arithmetic and logic	8	1		
Load and store	4	3		
Branch	2	4		
Others	4	3		
Computer B				
Arithmetic and logic	10	1		
Load and store	8	2		
Branch	2	4		
Others	4	3		

Determine and compare the average CPI, MIPS rate, and execution time for each computer.

#### Solution:

### For computer A:

- Total No. of instructions executed by Computer A are: 8+4+2+4=18 (million).
- Total number of CPU clock cycles needed by Computer A are:  $\sum$  (instruction count<sub>i</sub> \* CPI<sub>i</sub> of each instruction = 8\*1+4\*3+2\*4+4\*3 = 40 (million-cycles).
- Average CPI of Computer A is: 40 / 18 = 2.22 (Cycles/instruction).
- MIPS rate of Computer A is  $\frac{\text{Clock Rate}}{\text{CPI} \times 10^6}$ :  $200 * 10^6 / (2.22 * 10^6) = 90$ .
- CPU execution time of Computer A is: *instruction count* \* *CPI* / *Clock Rate* = 40 million /  $(200 * 10^6) = 0.2$  seconds.

## For computer B:

- Total No. of instructions executed by Computer B are: 10+8+2+4=24 (million).
- Total number of CPU clock cycles needed by Computer B are: sum(instruction count<sub>i</sub> \* CPI<sub>i</sub> of each instruction = 10\*1+8\*2+2\*4+4\*3 = 46 (million-cycles).
- Average CPI of Computer B is: 46 / 24 = 1.92 (Cycles/instruction).
- MIPS rate of Computer B is  $\frac{\text{Clock Rate}}{\text{CPI} \times 10^6}$ :  $200 * 10^6 / (1.92 * 10^6) = 104$ .
- CPU execution time of Computer B is: *instruction count* \* *CPI* / *Clock Rate* = 46 million /  $(200 * 10^6) = 0.23$  seconds.

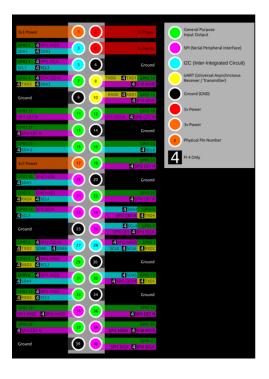
5. Please perform a literature study on the hardware structure of a Raspberry Pi 4 Model B+ platform and the sensor HAT board. Please identify its major components, ports, types of sensors and their functions.

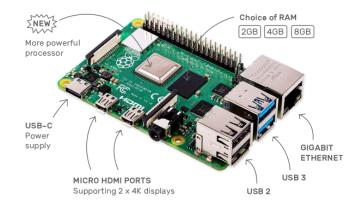


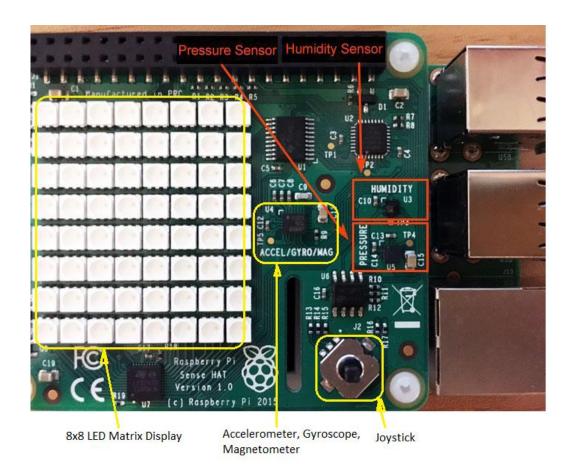


# Solution:

- The major components on Raspberry Pi 4 Model B+ are shown below.
- The major components of Raspberry Pi Sense HAT are shown next.







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