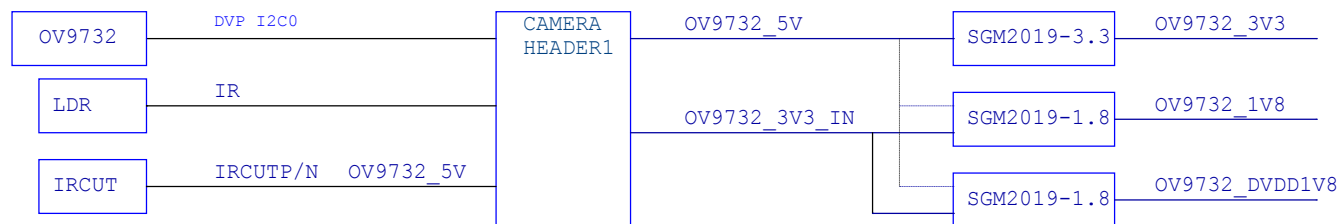
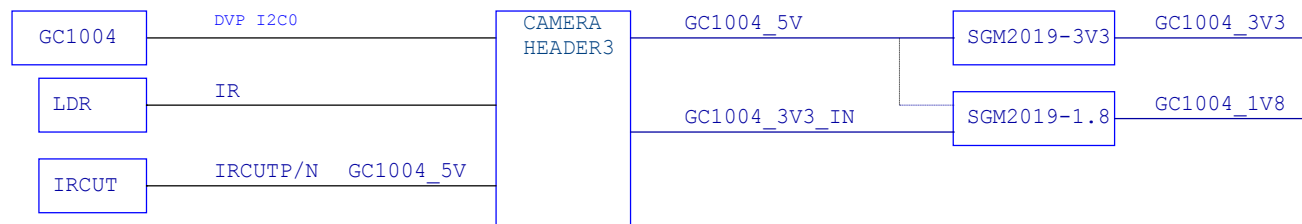
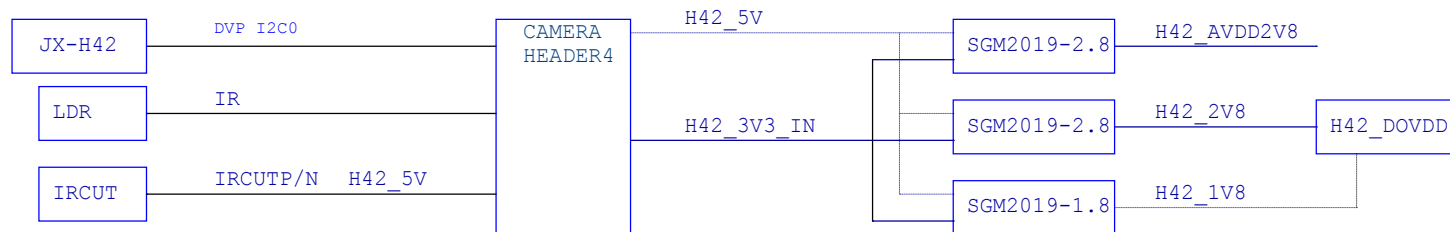
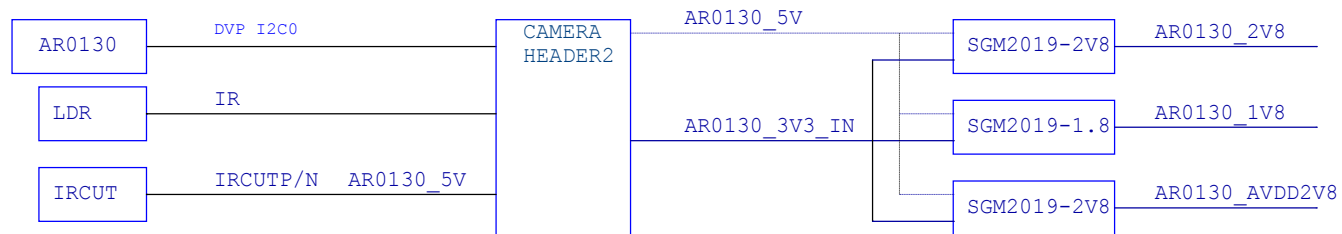


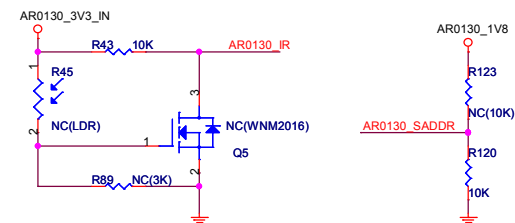
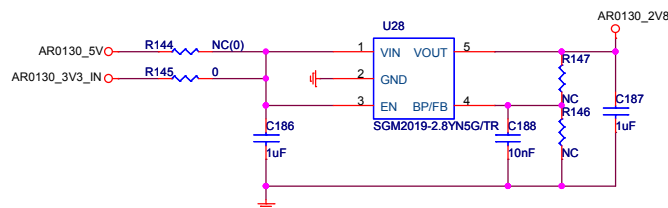
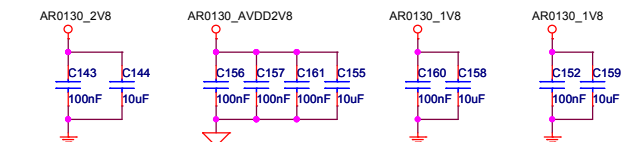


## RD\_HFJZ\_CAMERA

### Schematic Revision 3.0

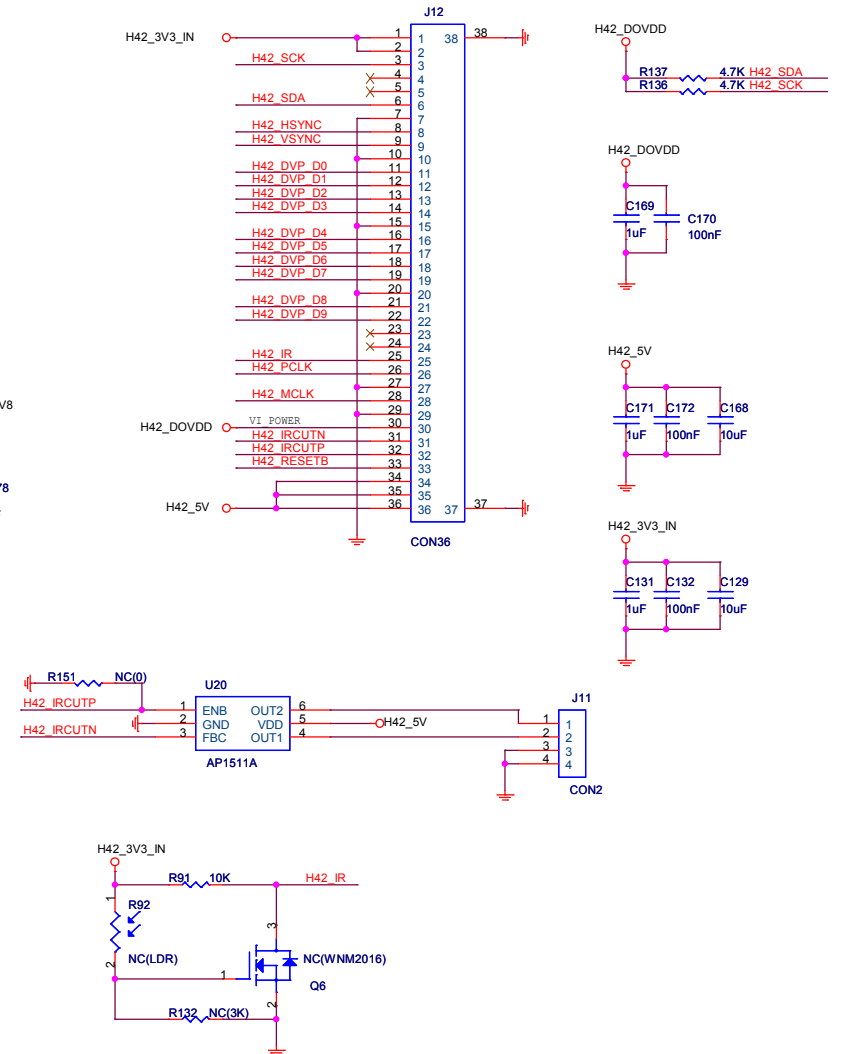
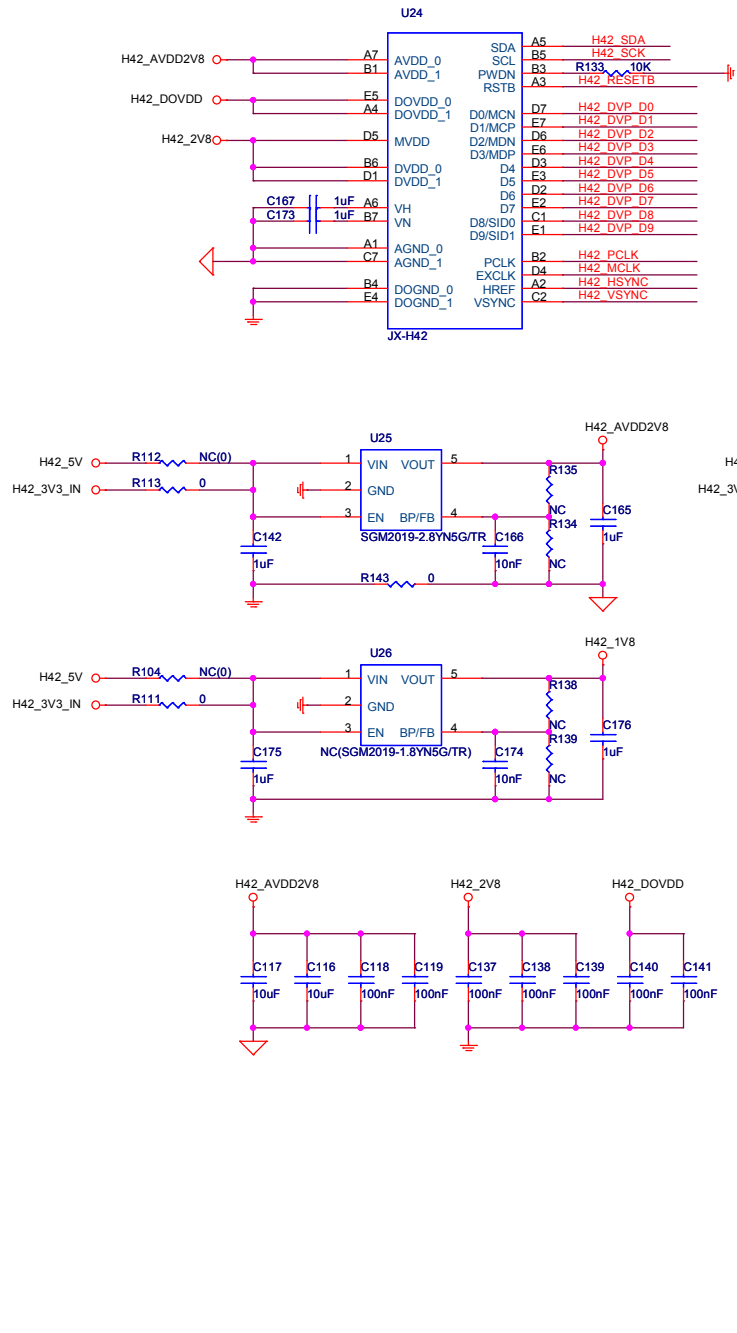
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COVER SHEET	1
SYSTEM ARCHITECTURE	2
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REVISION HISTORY	7





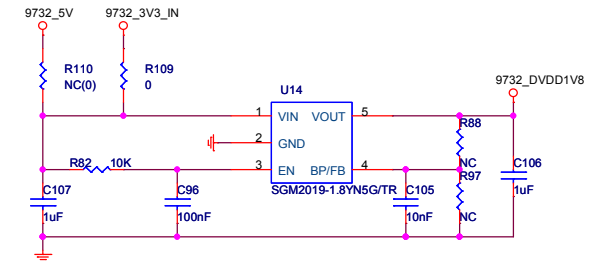
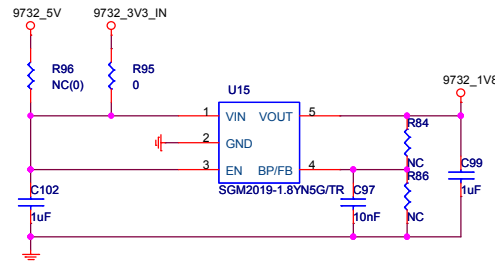
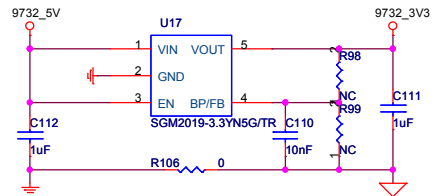
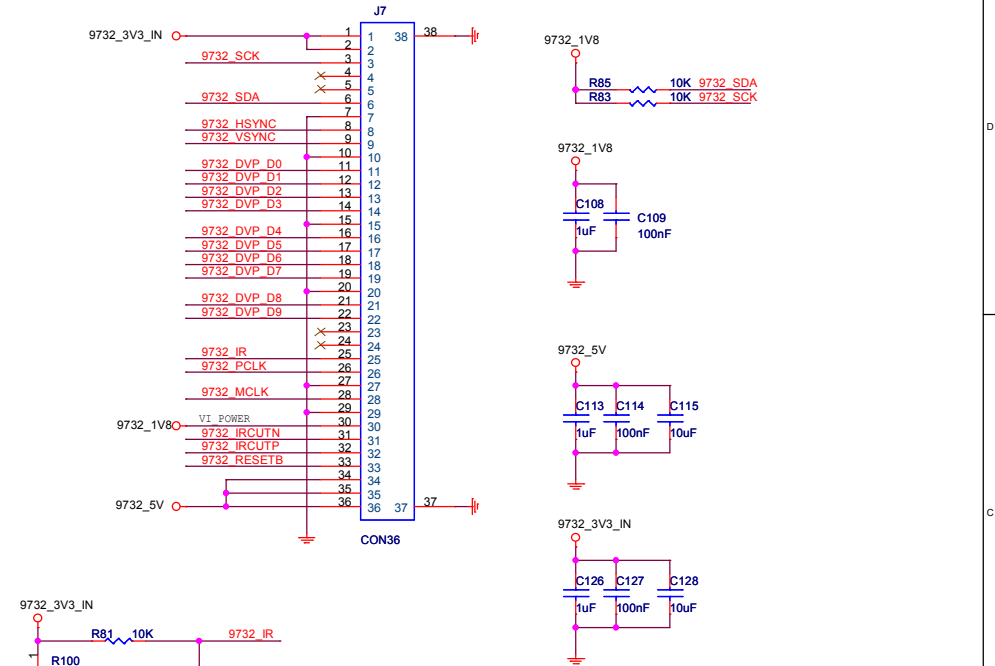
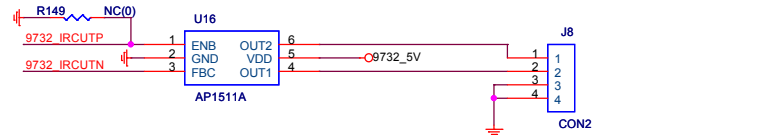
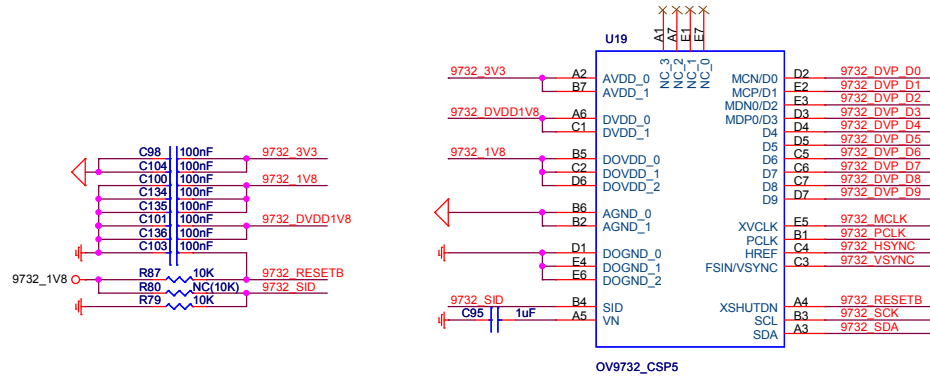
<b>INGENIC SEMICONDUCTOR CO.,LTD</b>				
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Size A3	Document Number AR0130			Rev 3.0
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# JX-H42

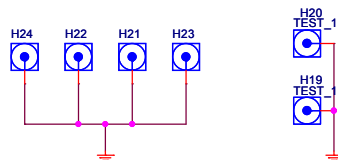




# OV9732



Power-Up Sequence : 9732\_3V3/9732\_1V8 -> 9732\_DVDD1V8



INGENIC SEMICONDUCTOR CO.,LTD			
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Data	Revision	Change
May 26 2015	Rev1.0	1. First Revision
Oct 13 2015	Rev2.0	1. Change 32pin DVP Connector to 36pin and use new signal definition 2. ADD OV9732 and OV9750 sensor , Delete GC1004 and AR0130 sensor 3. ADD LDR signal detection 4. Change MOSFET IRCUT circuits to AP1511A buffer
Oct 13 2015	Rev3.0	1. ADD AR0130、JX-H42 and GC1004 sensor , Delete OV9750、OV9712 and AR0141 sensor