

Node Name	Customize Columns ...	Direction	Location	I/O Bank	VREF Group	I/O Standard	Reserved	Current Strength	Slew Rate	Differential Pair	Strict Preservation
A[7]		Input	PIN_AC24	5	B5_N2	2.5 V (default)		8mA (default)			
A[6]		Input	PIN_AB24	5	B5_N2	2.5 V (default)		8mA (default)			
A[5]		Input	PIN_AB23	5	B5_N2	2.5 V (default)		8mA (default)			
A[4]		Input	PIN_AA24	5	B5_N2	2.5 V (default)		8mA (default)			
A[3]		Input	PIN_AA23	5	B5_N2	2.5 V (default)		8mA (default)			
A[2]		Input	PIN_AA22	5	B5_N2	2.5 V (default)		8mA (default)			
A[1]		Input	PIN_Y24	5	B5_N2	2.5 V (default)		8mA (default)			
A[0]		Input	PIN_Y23	5	B5_N2	2.5 V (default)		8mA (default)			
B[7]		Input	PIN_AC25	5	B5_N2	2.5 V (default)		8mA (default)			
B[6]		Input	PIN_AB26	5	B5_N1	2.5 V (default)		8mA (default)			
B[5]		Input	PIN_AD26	5	B5_N2	2.5 V (default)		8mA (default)			
B[4]		Input	PIN_AC26	5	B5_N2	2.5 V (default)		8mA (default)			
B[3]		Input	PIN_AB27	5	B5_N1	2.5 V (default)		8mA (default)			
B[2]		Input	PIN_AD27	5	B5_N2	2.5 V (default)		8mA (default)			
B[1]		Input	PIN_AC27	5	B5_N2	2.5 V (default)		8mA (default)			
B[0]		Input	PIN_AC28	5	B5_N2	2.5 V (default)		8mA (default)			
Borrow		Output	PIN_AB28	5	B5_N1	2.5 V (default)		8mA (default)	2 (default)		
Result[7]		Output				2.5 V (default)		8mA (default)	2 (default)		
Result[6]		Output	PIN_AH18	4	B4_N2	2.5 V (default)		8mA (default)	2 (default)		

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Result[5]		Output	PIN_AF18	4	B4_N1	2.5 V (default)		8mA (default)	2 (default)		
Result[4]		Output	PIN_AG19	4	B4_N2	2.5 V (default)		8mA (default)	2 (default)		
Result[3]		Output	PIN_AH19	4	B4_N2	2.5 V (default)		8mA (default)	2 (default)		
Result[2]		Output	PIN_AB18	4	B4_N0	2.5 V (default)		8mA (default)	2 (default)		
Result[1]		Output	PIN_AC18	4	B4_N1	2.5 V (default)		8mA (default)	2 (default)		
Result[0]		Output	PIN_AD18	4	B4_N1	2.5 V (default)		8mA (default)	2 (default)		
Sub		Input	PIN_AB25	5	B5_N1	2.5 V (default)		8mA (default)			
<<new node>>											