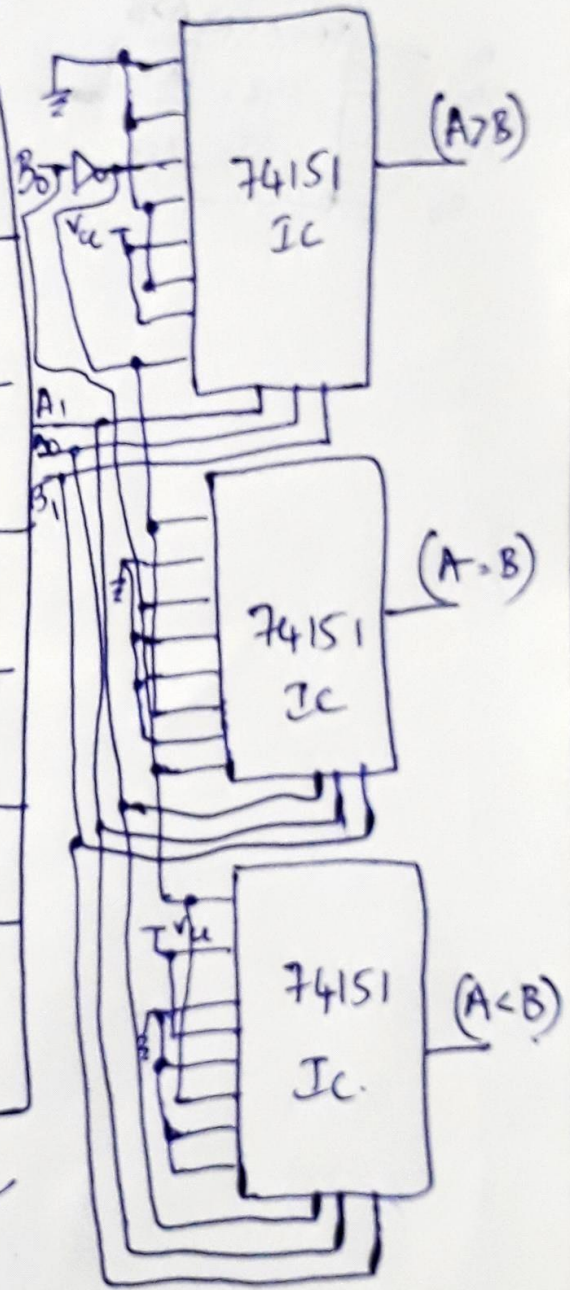


Set-1

Q3) Design a 2-bit Magnitude Comparator using 74151 ICs & minimum external gates.

A_1, A_0	B_1, B_0	$A > B$	$A = B$	$A < B$
00	00	0	1	0
00	01	0	0	1
00	10	0	0	1
00	11	0	0	1
01	00	1	0	0
01	01	0	1	0
01	10	0	0	1
01	11	0	0	1
10	00	1	0	0
10	01	1	0	0
10	10	0	1	0
10	11	0	0	1
11	00	1	0	0
11	01	1	0	0
11	10	1	0	0
11	11	0	1	0

1 Mark.



1 Mark. 1 Mark.