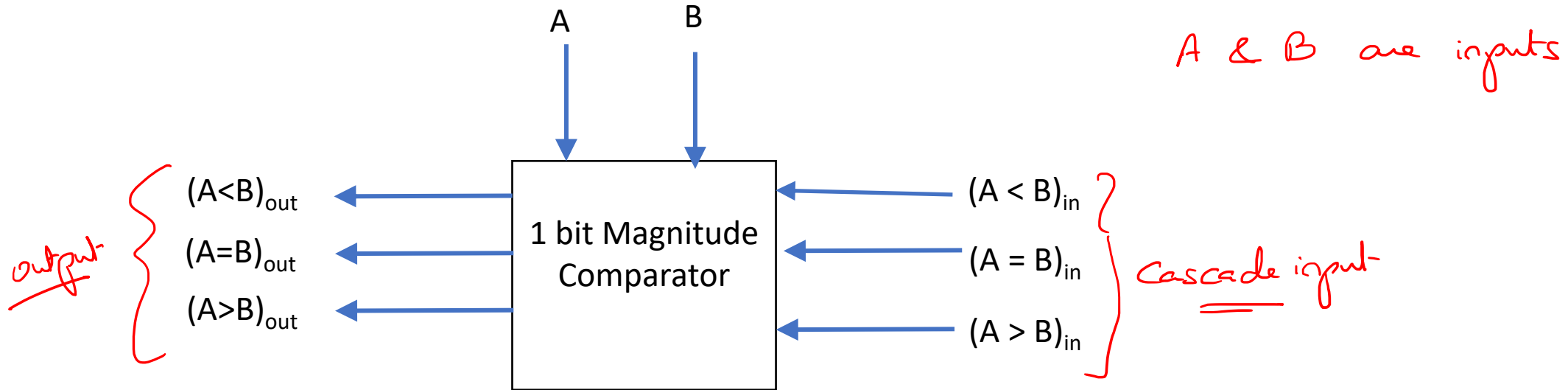


Multipliers and Magnitude comparators

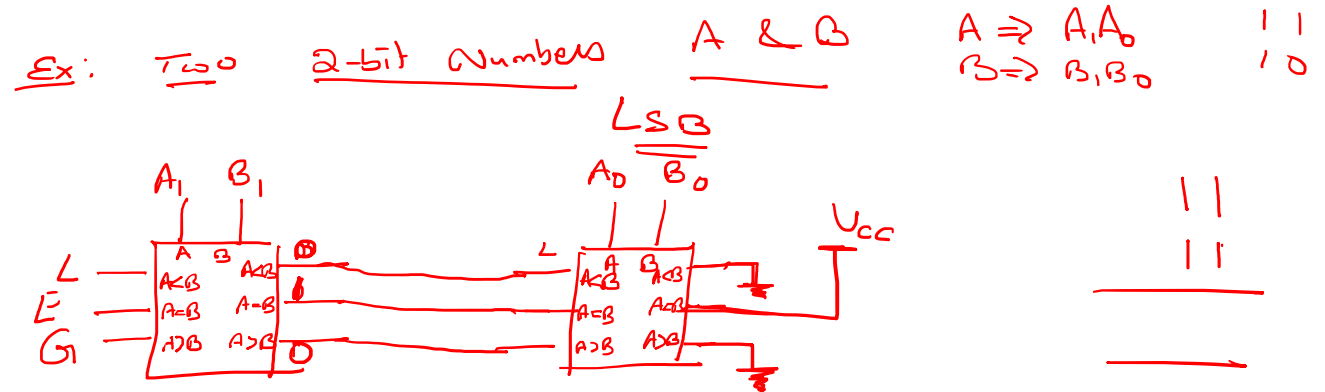
- 1-bit magnitude comparator with cascading input:



G $(A > B)_{out} = (A > B) + (A = B)(A > B)_{in}$

L $(A < B)_{out} = (A < B) + (A = B)(A < B)_{in}$

E $(A = B)_{out} = (A = B)(A = B)_{in}$



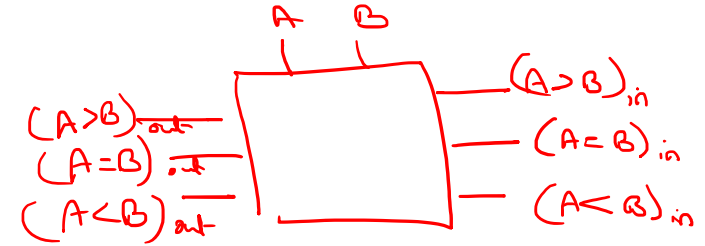
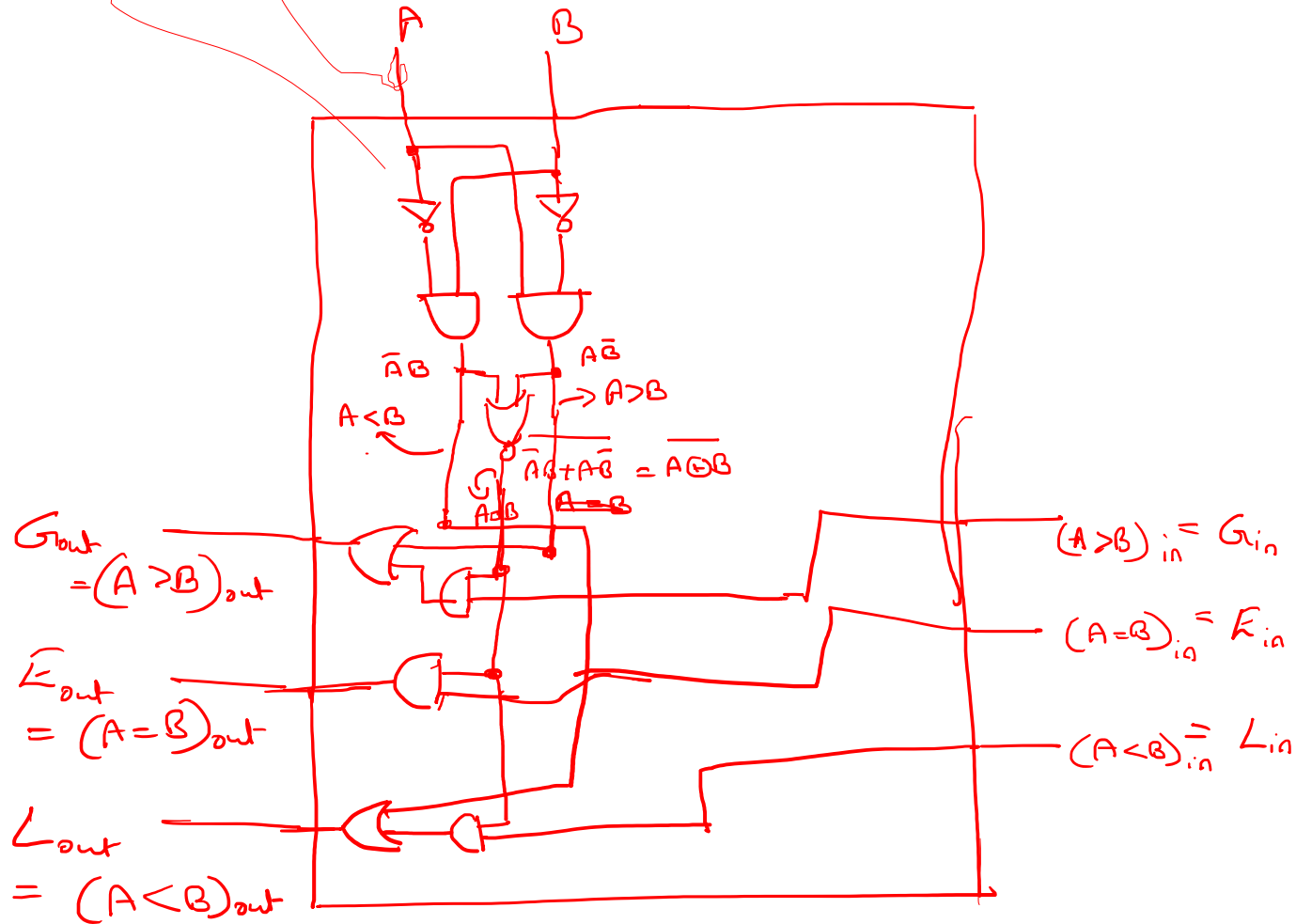
G $A > B = (A_1 > B_1) + (A_1 = B_1) \cdot (A_0 > B_0) \Rightarrow G = G_1 + E_1 G_0$

L $A < B = (A_1 < B_1) + (A_1 = B_1) \cdot (A_0 < B_0) \Rightarrow L = L_1 + E_1 L_0$

E $(A = B) = (A_1 = B_1) \cdot (A_0 = B_0) \Rightarrow E = E_1 \cdot E_0$

- Design of 1-bit magnitude comparator with cascading input:

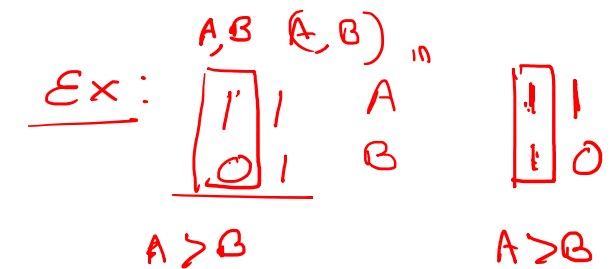
$$(A > B)_{out} = (A > B) + (A = B)(A > B)_{in}$$



$$(A > B)_{out} = (A > B) + (A = B)(A > B)_{in}$$

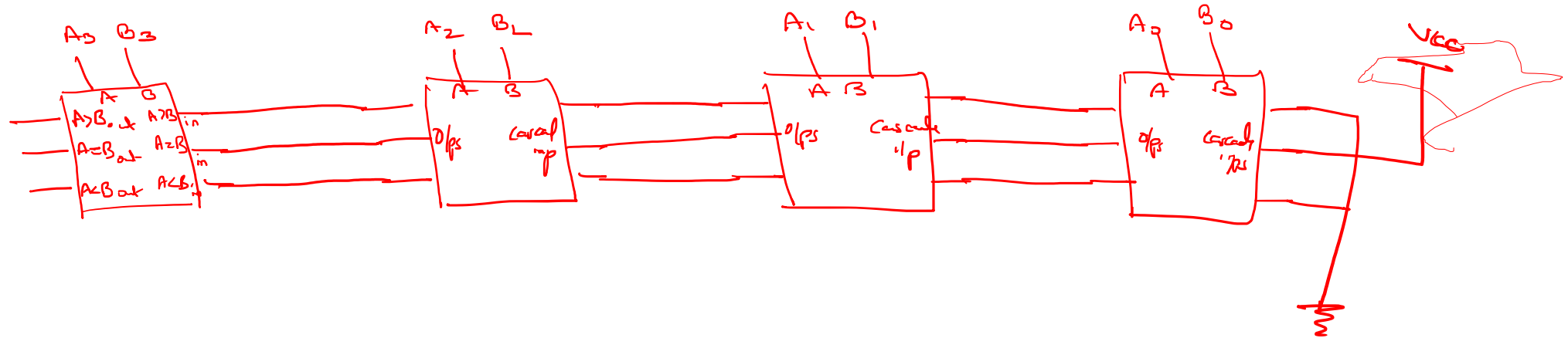
$$(A = B)_{out} = (A = B) \cdot (A = B)_{in}$$

$$(A < B)_{out} = (A < B) + (A = B)(A < B)_{in}$$



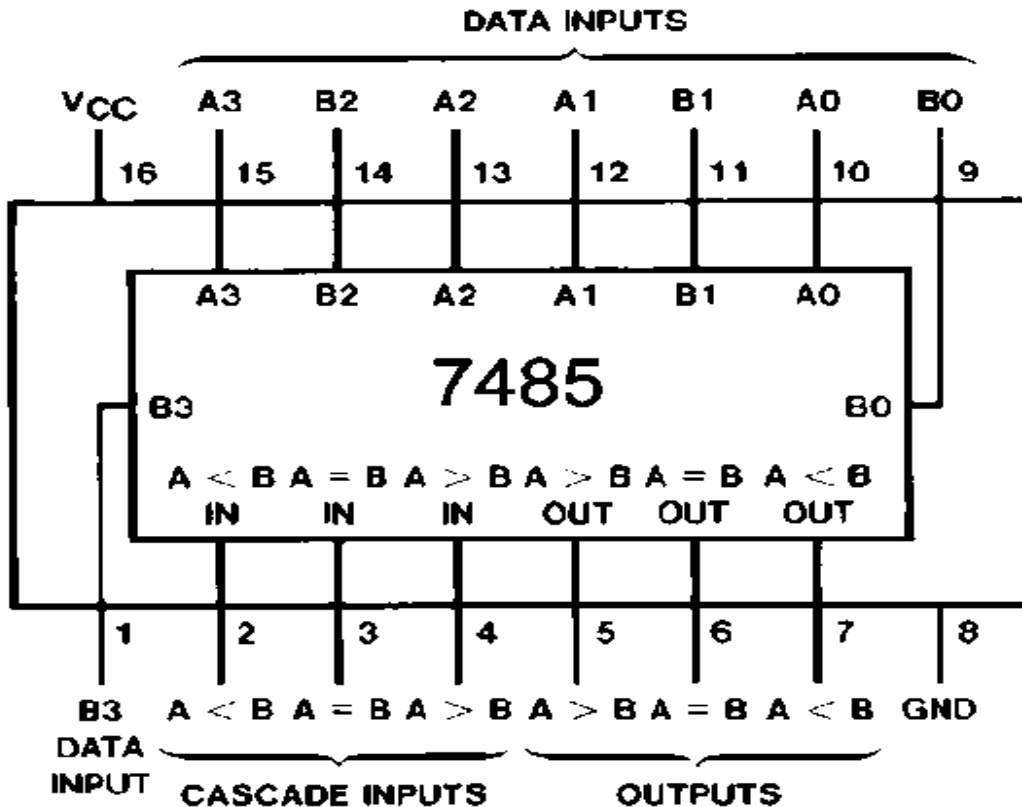
- Design a 4-bit magnitude comparator using 1-bit magnitude comparator with cascading inputs.

$A > B = G$
 $A = B = E$
 $A < B = L$

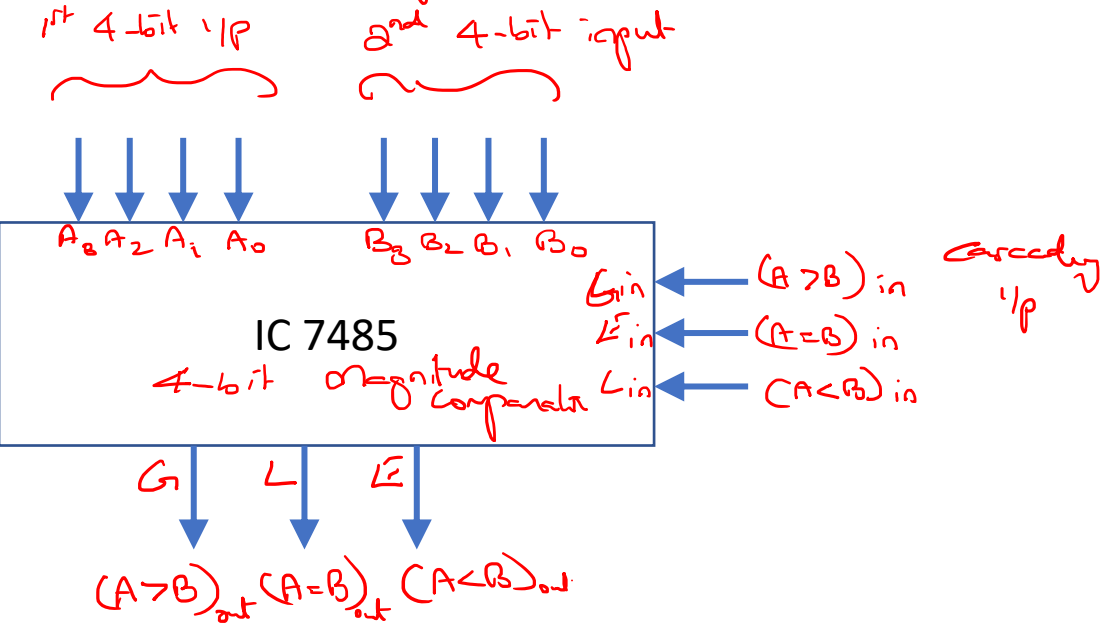


- 7485 IC (4-bit magnitude comparator with cascading inputs)

IC - Pin diagram



Schematic Diagram

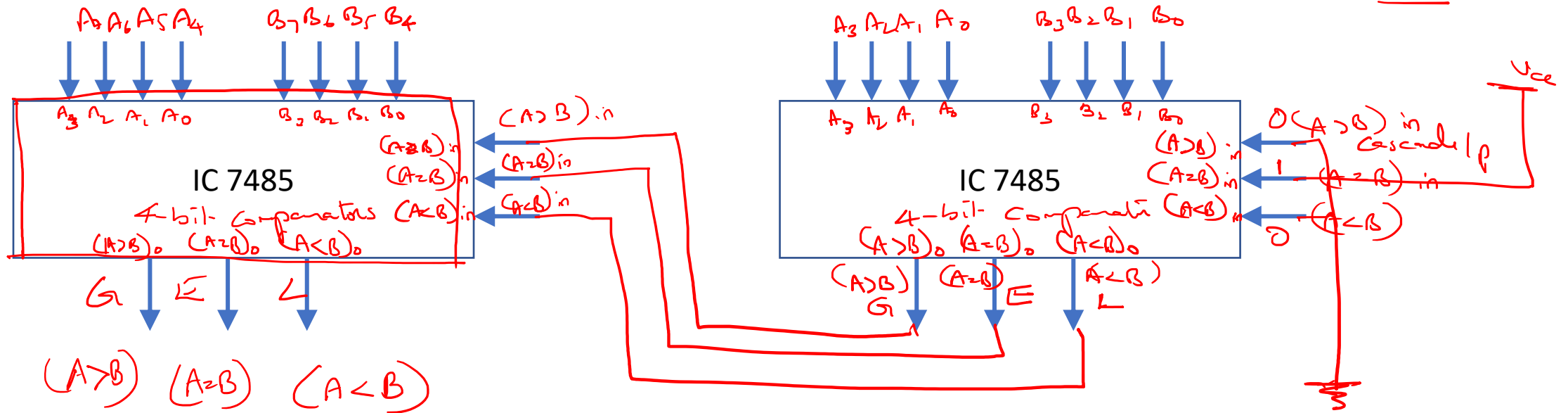


- Design 8-bit magnitude comparator using 7485 ICs

$$A = A_7 A_6 A_5 A_4 A_3 A_2 A_1 A_0$$

$$B = B_7 B_6 B_5 B_4 B_3 B_2 B_1 B_0$$

To check $A > B$
 $A = B$
 $A < B$



Don't forget

- Design a combinational circuit using 7483 IC and external gate to perform the arithmetic operations as shown below

4-bit Adder

→ ONLY one gate

$$F = 2X + Y \text{ when } m=0, \quad \& \quad F = X + 2Y \text{ when } m=1$$

Where X, Y are two bit numbers and output F is a 4 bit number.

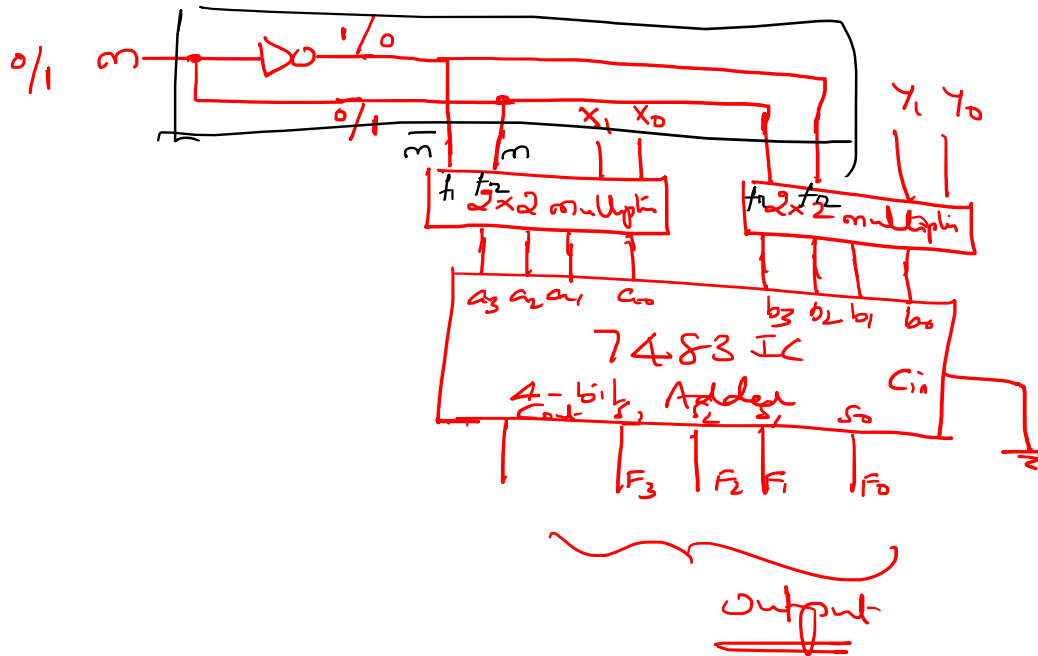
Control pin: m

$$\begin{array}{l} m=0 \\ m=1 \end{array} \quad \begin{array}{l} F = 2X + Y \\ F = X + 2Y \end{array}$$

$$\begin{array}{l} 10 \times X + 01 \times Y \\ 01 \times X + 10 \times Y \end{array}$$

X & Y are two bit numbers

$$\begin{array}{l} 11 (\text{max}) \Rightarrow 3_{10} \\ 3 \times 3 = 9_{10} \\ \underline{1001} \end{array}$$



$$\begin{array}{l} m=0 \\ m=1 \end{array} \quad \begin{array}{l} 10 \quad 2\text{-bit} \\ 2 \times X + 1 \times Y \\ 01 \quad 2\text{-bit} \\ 1 \times X + 2 \times Y \end{array}$$

m	f_1	f_2	m	f_1	f_2
0	1	0	0	0	1
1	0	1	1	1	0
	$f_1 = 3$	$f_2 = 3$		$f_1 = 3$	$f_2 = 3$

- Questions?