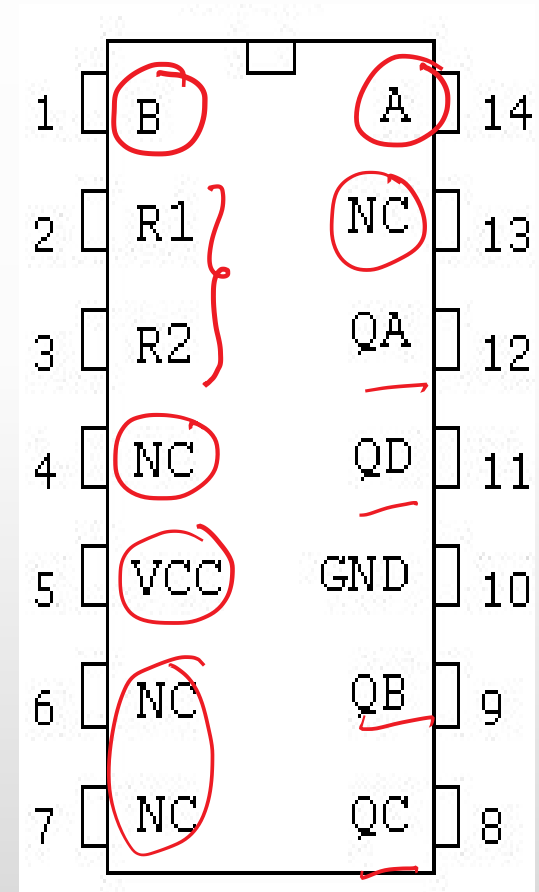


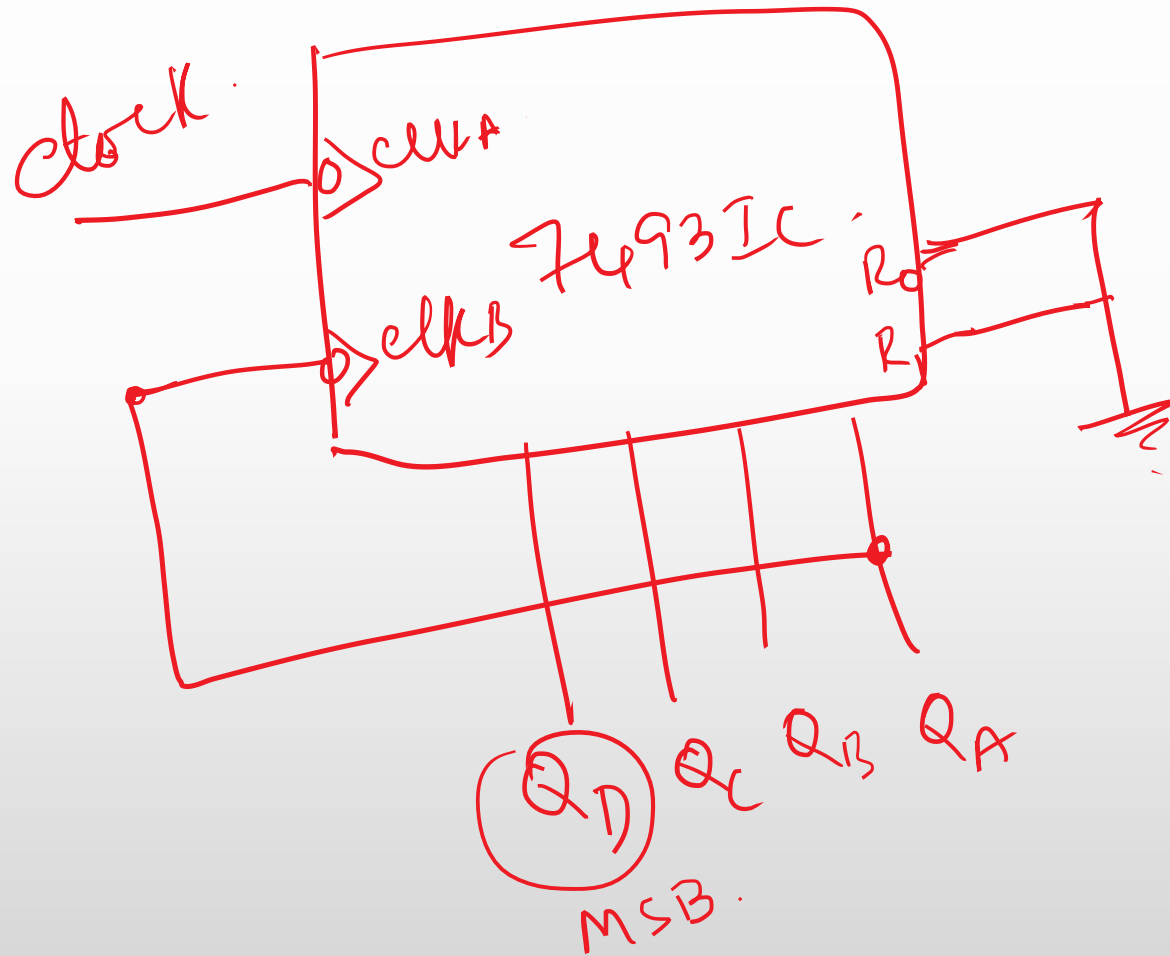


ASYNCHRONOUS COUNTER(RIPPLE COUNTER)

OK FH



- Design MOD 16 counter using 7493IC



Q2 Q1 Q0 Q_A

0000

0001

0010

0011

0100

0101

0110

0111

1000

1001

1010

1011

1100

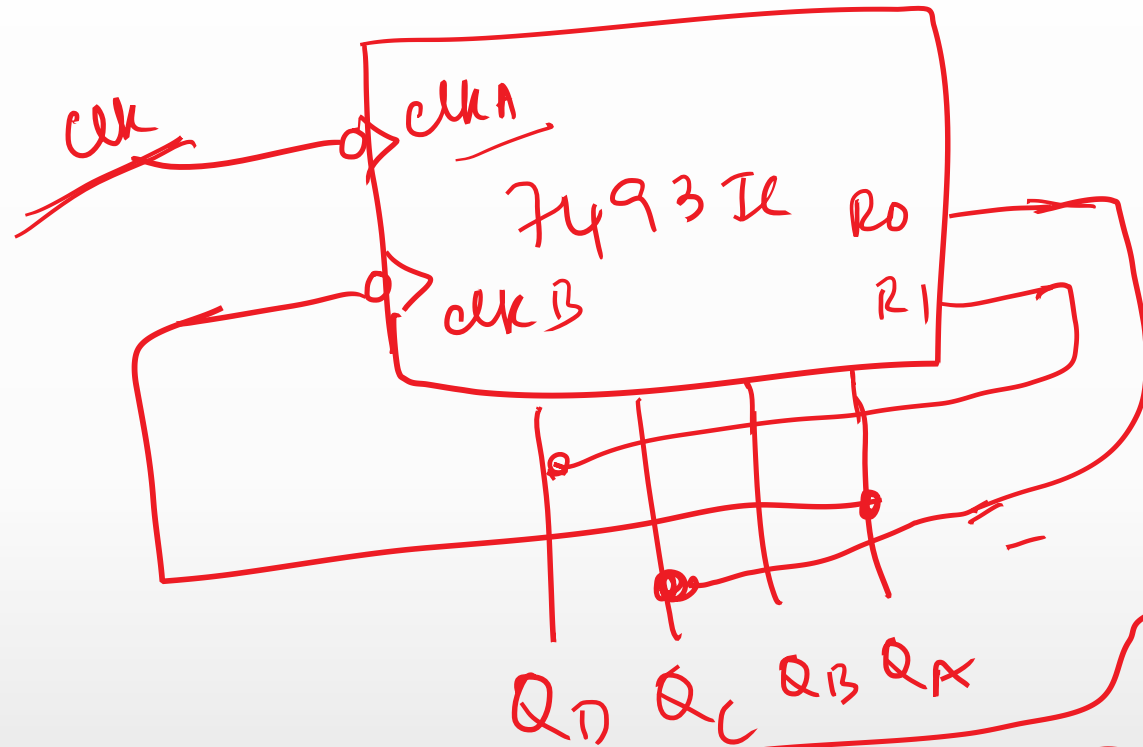
1101

1110

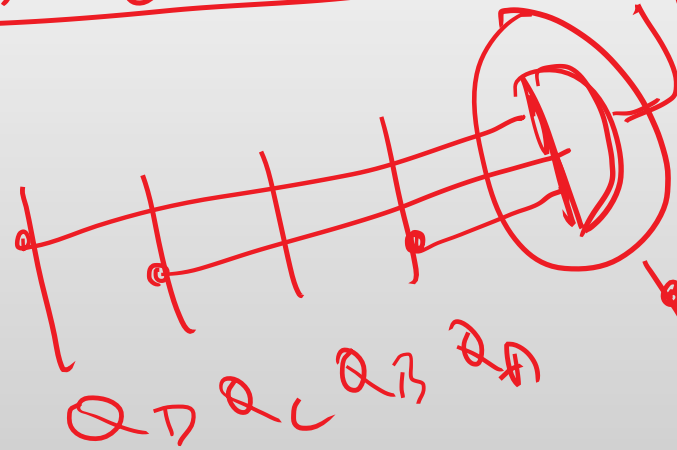
1111

- Design MOD 12 counter using 7493 IC and external gate if required

~~11~~ 11



MOD 12



$Q_D Q_C Q_B Q_A$

0000

0001

0010

0011

0100

0101

0110

0111

1000

1001

1010

1011

1100

1101

- Design a counter that performs UP count from 00H to FFH using 7493ICs

