



MANIPAL INSTITUTE OF TECHNOLOGY

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# Basic Electrical Technology

## Node Voltage Analysis

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# Objective

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- Application of KCL for analysis of DC circuits



# Introduction

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## Node

- A point in the circuit where three or more than three elements are joined

## Kirchhoff's Current Law (KCL)

- The algebraic sum of currents at a node is zero



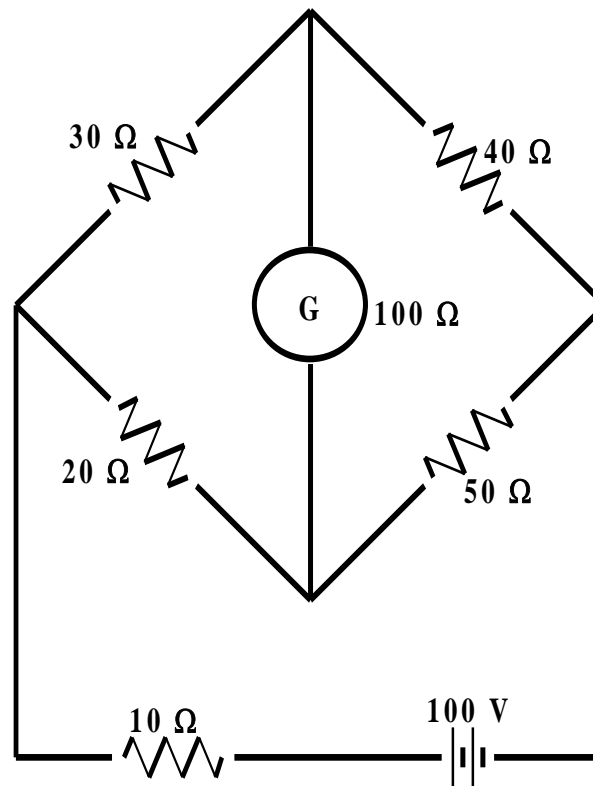
# Node Voltage Analysis Method

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- Convert all the voltage sources in the circuit to current sources
- Identify nodes in the circuit and assign a voltage for each node
- One of the nodes is the reference node
- Write KCL equations for all the nodes
- Solve for voltages

# Illustration 1

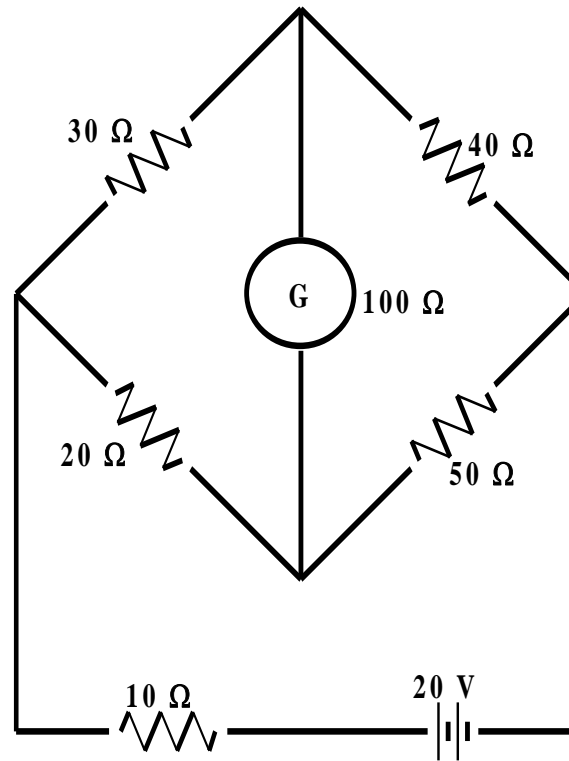
Determine the current through the galvanometer “G”



**Ans: 84 mA**

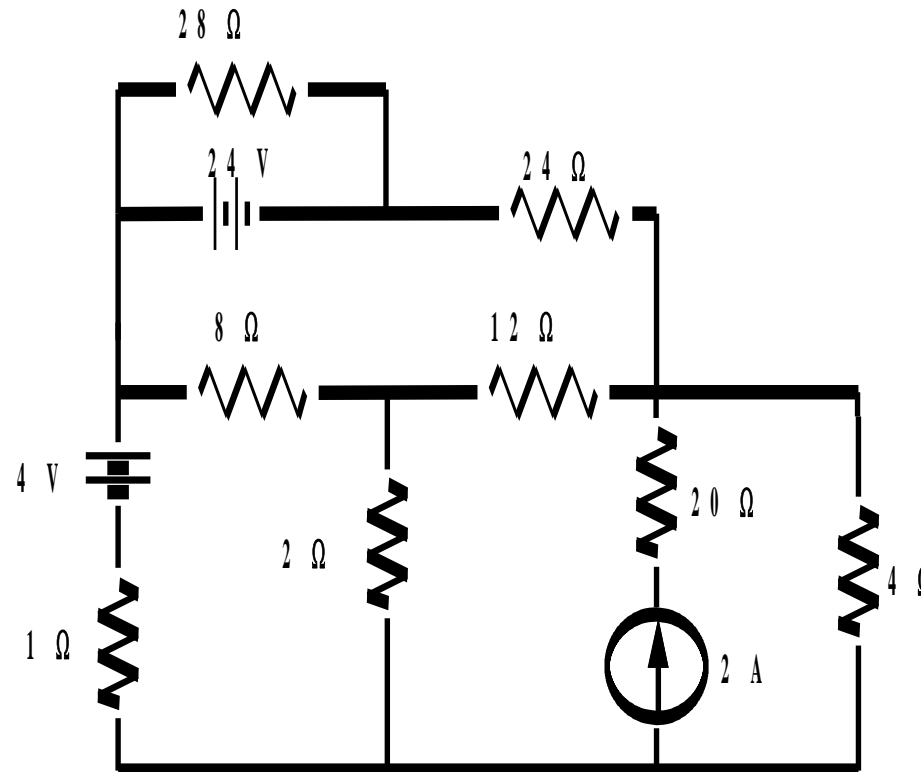
# Illustration 1 contd...

How to write the network equations by inspection?



# Illustration 2

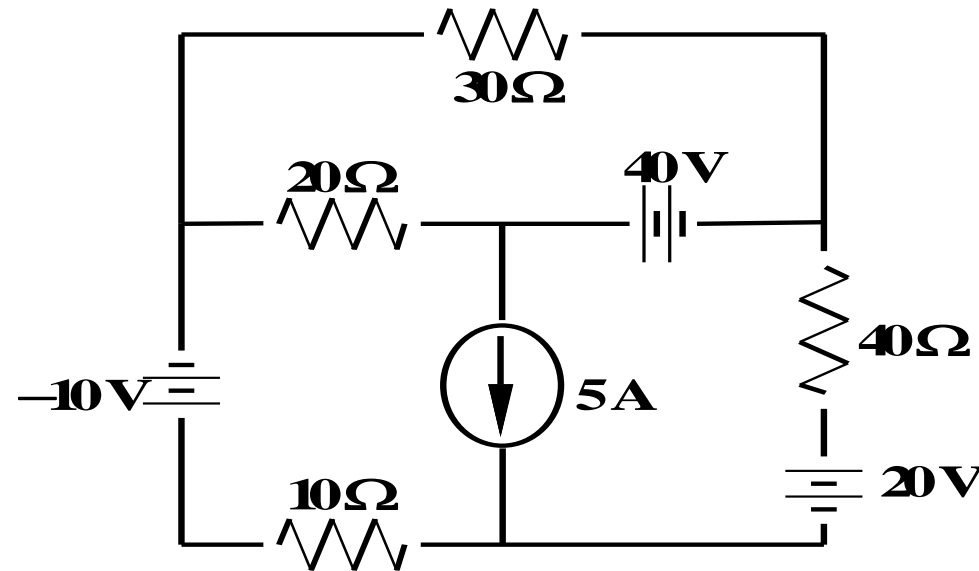
Determine the power dissipated in  $8\ \Omega$  resistor. Is the  $4\text{ V}$  source charging or discharging?



**Ans:  $P_{8\Omega} = 1.386\text{ W}$   
Charging**

# Illustration 3

Find the current through 40 V battery. Is the battery charging or discharging?



**Ans: 4.19 A,  
Discharging**





# Summary

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- Node voltages are determined
- Other operating conditions can be determined using the node voltages
- Concept of super-node:- If there is a voltage source between two nodes