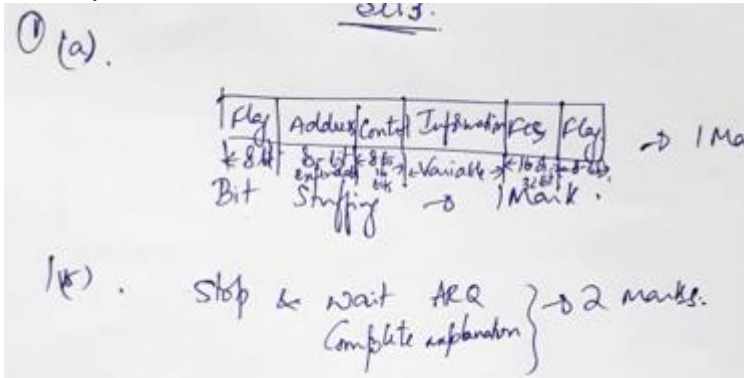
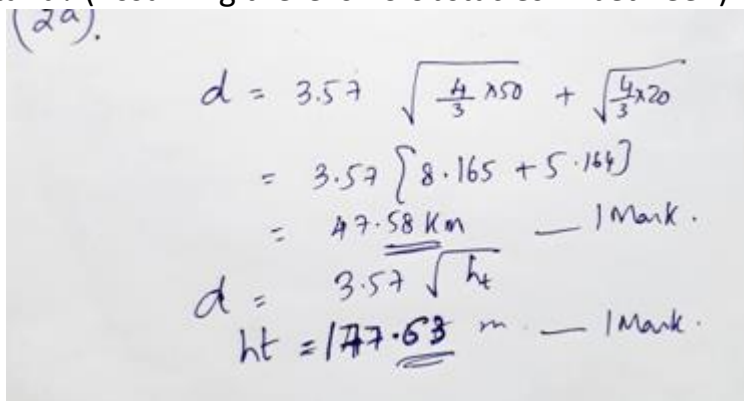
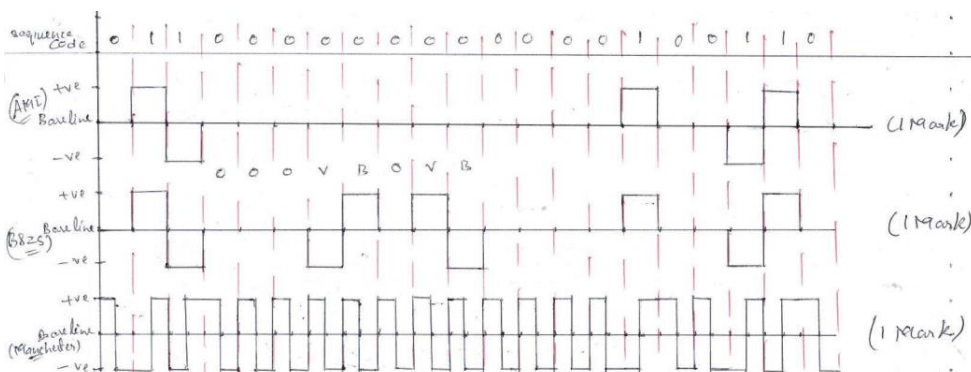


III SEMESTER B.TECH.
(INFORMATION TECHNOLOGY/COMPUTER & COMMUNICATION ENGINEERING)
IN-SEMESTER EXAMINATIONS, DECEMBER 2021
SUBJECT: PRINCIPLES OF DATA COMMUNICATION [ICT 2156]
SCHEME OF EVALUATION
TOTAL MARKS: 20 M

1	<p>a. With respect to the standard HDLC frame format, give the structure of the frame format indicating the individual field size in bits and explain bit stuffing with an example.</p> <p>b. With respect to the error control mechanism, explain the concept of stop and wait ARQ.</p>  <p>The diagram shows an HDLC frame structure: Flag (8 bits), Address (8 bits), Control (8 bits), Information (Variable), Flag (8 bits). Below it, it says 'Bit Stuffing' and '→ 1 Mark'. Below the diagram, it says '(b). Stop & wait ARQ' and 'Complete explanation' with a bracket indicating '→ 2 marks'.</p>	<p>1+1</p> <p>2</p>
2	<p>a. Given that the transmitter and receiver antenna height is 50 meters and 20 meters respectively, if a man would like to see the receiving antenna which is on the ground level what should be the height of the building on which he should stand? (Assuming there is no obstacles in between).</p>  <p>The calculation shows: $d = 3.57 \sqrt{\frac{4}{3} \times 50} + \sqrt{\frac{4}{3} \times 20}$ $= 3.57 [8.165 + 5.164]$ $= 47.58 \text{ Km} \quad \text{--- 1 Mark.}$ $d = 3.57 \sqrt{h_t}$ $h_t = \underline{177.63 \text{ m}} \quad \text{--- 1 Mark.}$</p> <p>b. What is the channel capacity for a telephone channel with a 2.4 kHz bandwidth and a signal-to-noise ratio of 20 dB, where the noise is white thermal noise?</p>	<p>1+1</p> <p>0.5+0.5</p>

	<div>2/2</div> <div>Given: BW=2.4kHz SNR_{dB} = 20dB</div> <div>To find: Channel Capacity (C)</div> <div>Solⁿ: $C = B \log_2(1 + \text{SNR})$ $C = 2400 \log_2(1 + 10^2)$ $C = 2400 * 6.658 \text{ bps}$ $\Rightarrow C = 15979.2 \text{ bps}$ <div>15.98 kbps [0.5M]</div></div> <div><div>SNR_{dB} = 10 log₁₀ SNR $\Rightarrow \text{SNR} = 10^{\text{SNR}_{\text{dB}}/10}$ $\Rightarrow \text{SNR} = 6.658$ [0.5M]</div></div>																
3	<p>For the bit stream 01100000000000100110, sketch the waveforms for Manchester, bipolar AMI and B8ZS. Assume that the signal level for the preceding bit for NRZI was high; the most recent preceding 1 bit (AMI) has a negative voltage, and the most recent preceding 0 bit (pseudo ternary) has a negative voltage.</p> <div></div>	1+1+1															
4	<p>Explain the working of Statistical time-division multiplexing (STDM) and compare with synchronous time division multiplexing by drawing the possible frame format</p> <p>Block Diagram carries 1 marks, Detailed Explanation carries 1 marks and Comparison between Statistical and synchronous time-division multiplexing (TDM) 1 marks</p> <div><div><table><tr><td>Flag</td><td>Address</td><td>Control</td><td>Statistical TDM subframe</td><td>FCS</td><td>Flag</td></tr></table><p>(a) Overall frame</p><table><tr><td>Address</td><td>Data</td></tr></table><p>(b) Subframe with one source per frame</p><table><tr><td>Address</td><td>Length</td><td>Data</td><td>...</td><td>Address</td><td>Length</td><td>Data</td></tr></table><p>(c) Subframe with multiple sources per frame</p></div><div>Figure 8.13 Statistical TDM Frame Formats</div></div>	Flag	Address	Control	Statistical TDM subframe	FCS	Flag	Address	Data	Address	Length	Data	...	Address	Length	Data	1+1+1
Flag	Address	Control	Statistical TDM subframe	FCS	Flag												
Address	Data																
Address	Length	Data	...	Address	Length	Data											

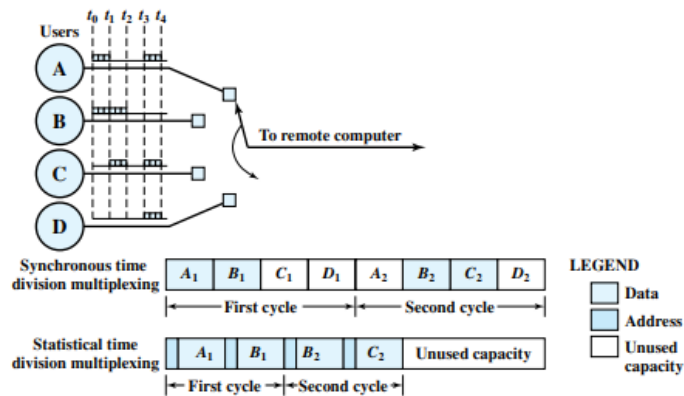


Figure 8.12 Synchronous TDM Compared with Statistical TDM

- 5) What will be the checksum that Alice sends Bob for the following message? Show the steps of working. Verify at the receiver's end as well. Assume $n = 8$ and the equivalent Hexadecimal value of A = 0x41. Message: EVERYTHING

2+1

5) Given: Message = EVERYTHING
 $n = 8$

To find:
 a) Checksum
 b) Verify

Solⁿ:

E:	0100	0101	45
V:	0101	0110	56
E:	0100	0101	45
R:	0101	0010	52
Y:	0101	1001	57
T:	0101	0100	54
H:	0100	1000	48
I:	0100	1001	49
N:	0100	1110	4E
G:	0100	0111	47

1100	00	0101
1100	00	0101
0000	1000	

Sum: 0000 1000

Checksum: 1111 0111 [2M]

Receiver:

1100	00	0101
1111	0111	
1111	1100	
1111	1100	

Sum: 1111 1111

Checksum: 0000 0000 [1M]

6

A CRC is constructed to generate FCS for a 12-bit message. The generator polynomial is $P(X) = X^4 + X^2 + X + 1$. Draw the shift-register circuit and encode the data bit sequence 1100 1110 0111 using the generator polynomial and generate the codeword. Show the detailed steps of working.

1+2

Given: Data bit sequence: 1100 1110 0111
 Pattern, $P(X) = X^4 + X^2 + X + 1$ (10111)

Asked: a) Shift Register Circuit
 b) Codeword

Solution:

Shift Register Circuit

$n-k = 5$ Shift registers

Codeword = 1100 1110 0111 1010

I = Input	C ₃	C ₂	C ₁	C ₀	C ₃ ← C ₂ ⊕ C ₀	C ₂ ← C ₁ ⊕ C ₀	C ₁ ← C ₀ ⊕ I
1	0	0	0	0	1	1	1
1	0	1	1	1	0	0	1
0	1	0	0	1	1	0	1
0	0	1	0	1	0	1	0
1	1	0	1	0	1	0	0
1	0	1	0	0	1	1	1
1	1	1	1	1	1	1	0
0	1	1	1	0	0	1	1
0	1	0	1	1	0	0	1
1	0	0	0	1	1	0	1
1	0	1	0	1	1	0	1
1	1	1	0	1	1	0	1
1	1	0	0	1	1	0	1
1	1	0	0	1	1	0	1
1	1	0	0	1	1	0	1

OK FCS