



Dispatch width: 4

Execute width: 4

Retire width: 4

| Instruction Type | Latency |
|------------------|----------|
| INT.ARITH | 1 cycle |
| FLOAT.SP | 2 cycles |
| MEM.LOAD.SP | 4 cycles |
| BRANCH | 1 cycle |

*** Instructions *** ** Type ***

0. tmp = V[i] MEM.LOAD.SP

1. sum = sum + tmp FLOAT.SP

2. tmp = W[i] MEM.LOAD.SP

3. sum = sum + tmp FLOAT.SP

4. i = i + 1 INT.ARITH

5. c = N != i INT.ARITH

6. if c go back BRANCH

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Dependencies graph

