

Dispatch width: 4 Execute width: 4 Retire width: 4

INT. BRANCH

FLOAT, INT

INT

MEM.LOAD, MEM.STORE

Espera por dependencia

Espera por puerto Espera por retirement

INT.ARITH	1 cycle
FLOAT.SP	2 cycles
MEM.LOAD.SP	4 cycles
BRANCH	1 cycle
*** Instructions ***	** Type ****
0. tmp = V[i]	MEM.LOAD.SF
1. sum = sum + tmp	FLOAT.SP
2. (tmp = W[i]	MEM. LOAD. SF
3. sum = sum + tmp	FLOAT.SP
4. i = i + 1	INT.ARITH
5. c = N != i	INT.ARITH
6. if c go back	BRANCH 10

Instruction Type Latency

## Dependencies graph

