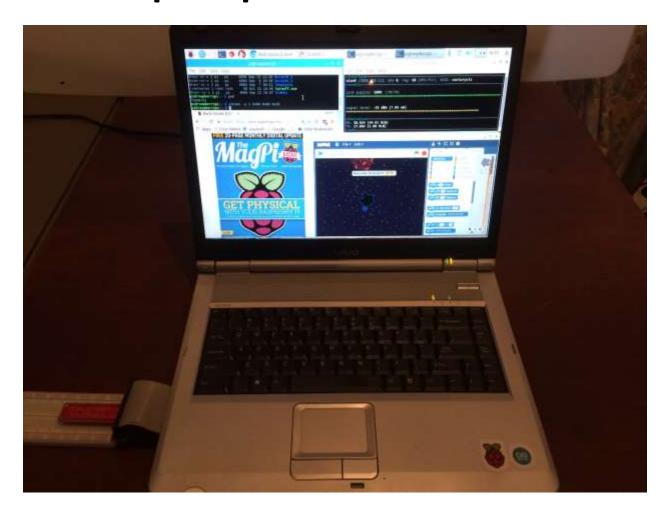
Raspberry Pi & Teensy Laptop Conversion



Repo at https://github.com/thedalles77/Pi Teensy Laptop

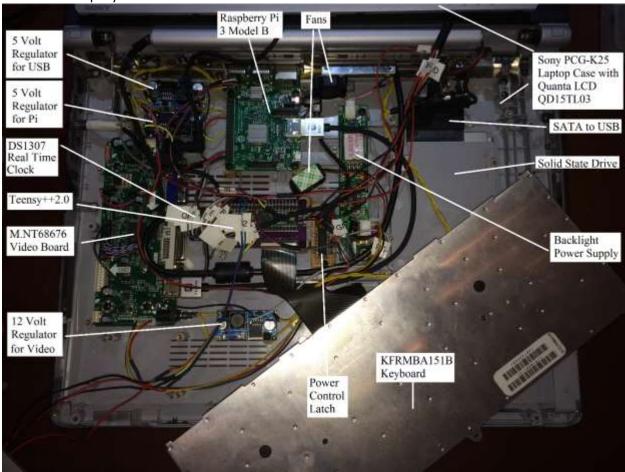
Introduction – This document will describe how I converted a Sony Vaio PCG-K25 computer into a Raspberry Pi 3B and Teensy ++2.0 laptop. Most of the circuits, software, and modifications are based on information available on the internet. The circuits in the laptop are listed below and then described in detail in the following pages:

- A Teensy ++2.0 is used to interface the keyboard and touchpad with the Pi via USB. This required
 a new circuit board to route the keyboard connector to the Teensy I/O pins. A Teensyduino
 sketch scans the keyboard and touchpad and sends changes to the Pi over USB.
- 2. The HDMI from the Pi is converted to LVDS for the display by an M.NT68676 video board. The video cable had to be lengthened to reach into the laptop base.
- 3. The control pad for the M.NT68676 video board was replaced by the Teensy.
- 4. The Pi boots from a 240GB solid state drive using a SATA to USB cable. The micro SD card has been removed.
- 5. The laptop Wi-Fi antenna is connected directly to the Pi for improved reception. A connector was added to the Pi that fits the laptop antenna cable.
- 6. A Real Time Clock and the Teensy are connected to the Pi over the I2C bus.
- 7. Three separate Buck regulators provide power to the M.NT68676 video board, the Pi, and the 4 USB ports. The regulators were modified so they can be enabled with a control signal.
- 8. The laptop power switch turns on the regulators and the Teensy turns off the regulators. This is accomplished with a NAND gate latch circuit that controls the regulator enable signal.
- 9. The laptop LEDs were rewired to show incoming power, regulator power, caps lock, and code debug.
- 10. The Teensy will reset the Pi if Control-Alt-r is typed or shut down the laptop if Control-Alt-s is typed. These actions can also be initiated by the Pi over the I2C bus.
- 11. The Pi GPIO signals are brought out the side of the laptop for bread boarding.
- 12. The Pi audio is cabled to a 3.5mm jack on the side of the laptop for headphones.
- 13. Audio sent to the M.NT68676 board via HDMI is connected to the laptop speakers.
- 14. A fan blows over the Pi and another fan blows out the back. Testing shows this gives adequate cooling to the Pi when overclocked at 1300 MHz.
- 15. The original laptop battery powers the laptop and is charged from the wall supply. The Pi reads the battery status registers over a bit-bang SMBus and turns off the laptop when the battery is depleted. The Teensy ADC monitors battery voltage and returns the result over the I2C bus.

A short YouTube video for this laptop project can be found <u>here</u>. I added battery operation after making the video. Two Hackaday articles have been written about this laptop, <u>here</u> and <u>here</u>.

A parts list with vendor links is located in the Appendix.

Board Mounting – The locations of all the boards in the laptop are shown in the following picture (except for the battery charger and ADC reference). Wood dowels were cut to make standoffs for the boards. The dowels were screwed to each board and then the dowels were attached to the laptop floor with JB Weld epoxy.



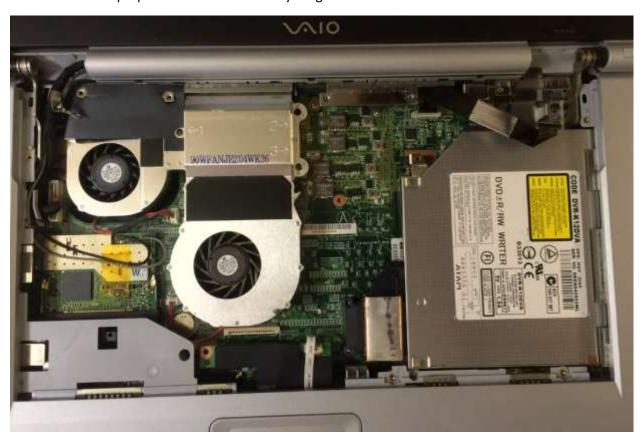
This picture shows the metal shield that helps to stiffen the keyboard.



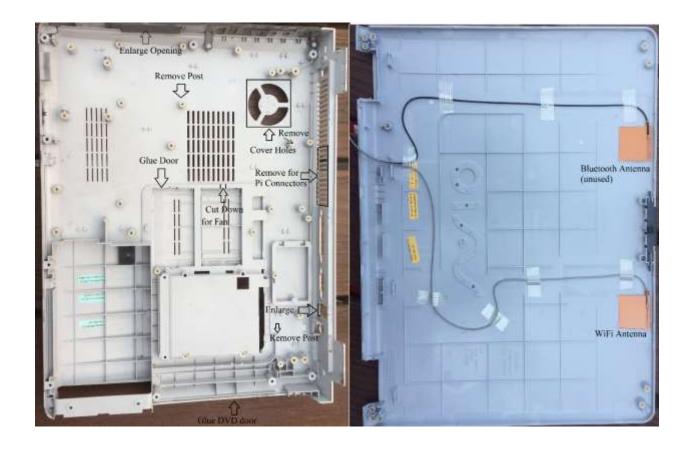
First Steps – I was given a nonfunctional Sony laptop specifically for this project. Unlike modern ultrathin laptops, this one has over an inch of thickness at the back so the Pi's USB and network connectors don't need to be removed to fit in the case. The laptop was in good shape but I needed to know if the LCD and keyboard were functional. After some troubleshooting, I was able to get the BIOS to boot up (there was no hard drive) and the display and keyboard worked fine. Using my Seeed DSO oscilloscope, I probed the 24 keyboard signals at the flexible printed circuit (FPC) connector. I found 16 of the signals were driven by the motherboard and pulsed low every 30 msec. The remaining 8 signals are inputs to the motherboard and were at 5 volts but would pulse low if I pressed certain keys. Powering down the laptop and switching to an ohm meter, I was able to determine the keyboard connections by systematically checking for a connection as I pressed each key. It's a long tedious process but I eventually created a matrix of the keyboard connections. I can see why people write software routines that automatically determine the matrix as each key is pressed.

I probed the touch pad connector pins with the scope but saw no clock or data pulses because the touchpad had not been initialized by the BIOS. I found the 5 volt and ground connections and two other pins that measured 5 volts but my ohm meter showed no connection to the 5 volt bus. I assumed these pins were the clock and data for the PS/2 interface and would figure which was which once I had the Teensy up and running.

This is what the laptop looked like before everything was removed:



Sony Vaio PCG-K series disassembly – I followed the "Inside my laptop" directions for disassembly of the base and LCD sections. I kept unscrewing everything and removed all metal shielding until I had a bare case as shown below. I used a Dremel tool and a file to make the cuts shown by arrows. JB Weld two part epoxy was used to glue the memory access door shut and to permanently secure the DVD door. I blocked the 3 fan holes with Duct Tape and then poured in JB Weld. I should have removed all of the original motherboard mounting posts so I didn't have to come back later and Dremel another one each time I tried to mount a board.



Keyboard – I chose a Teensy ++2.0 for my keyboard controller because I wanted extra I/O for controlling the touchpad, video card and anything else that might come up without adding multiplexing chips. As I expected, I've used up all 38 of the Teensy's digital I/O pins. This Teensy has been around for a while so I figured the software was stable and there would be lots of examples to follow. The Teensy developer, PJRC has created an Arduino add-on called Teensyduino that allows the controller to communicate via USB as a Human Interface Device (HID), aka keyboard and mouse. I followed the PJRC steps for installing Arduino and Teensyduino on a PC and then started experimenting with the Keyboard.set_key and Keyboard.set_modifier commands. I used PJRC's "Micro Manager" method to send the keyboard data over USB.

The finished sketch is a complete keyboard controller routine that scans the keys every 30 msec and sends the keypresses to the Pi over USB. I'm a hardware guy so don't expect pretty code but it works and can certainly be improved on. The Modifier keys (Ctrl, Alt, Shift, and GUI) can be pressed at the same time in any order and they will be sent to the Pi. The rest of the keys (called normal keys) are sent in 6 different slots so that you can have more than one key pressed at a time. The program sends the keypress in slot 1 if it's not in use, otherwise it moves on to slot 2 on up to slot 6. If the keyboard had diodes to isolate the keys, then up to 6 keys could be pressed at the same time. Unfortunately there are no diodes so ghosting causes extra keys to be sent after 3 or 4 keys are held down. Go to Geekhack and Deskthority to learn more about keyboards and check out the TMK and Soarer keyboard controllers for examples of far more sophisticated keyboard routines. For everything that I do, the keyboard has worked perfectly including playing Scratch games, typing this document, and general Linux stuff. An earlier version of my keyboard routine sent all keypresses in slot 1 and there were lots of times that I would type one key and not quite release it before typing another key which would cause the second key to be missed. And a real deal breaker was when my Scratch games wouldn't move diagonally because the up arrow and right arrow couldn't be pressed at the same time.

My Teensy code, "Keyboard_and_Touchpad.ino" is available on github.

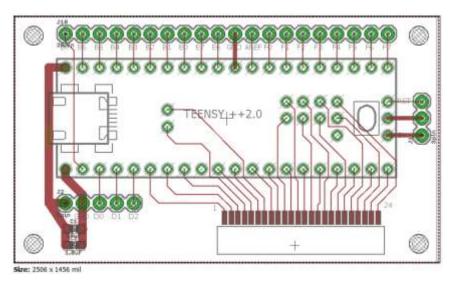
Keyboard Matrix – The keyboard part number is KFRMBA151B and it is used on all Sony Vaio PCG K series laptops. The 8 columns across the top of the matrix shown below are inputs to the Teensy. The 16 rows on the left side are outputs from the Teensy. Only one row is driven low at a time and the other 15 rows are set to high impedance. The 8 columns have pull ups inside the Teensy so if a key is not pressed, it is read as a logic high. I couldn't find a connection for the print screen and num lock keys on my keyboard so I'm assuming they're broke. I don't need num lock but I might want a print screen key so I use the Menu key as a print screen.

Firmware		Columns	0	1	2	3	4	5	6	7
	Teensy	Inputs	E0	E4	C1	A0	C2	A5	C4	A6
Rows	Outputs	Conn Pin#	6	7	11	12	13	14	17	18
0	D3	1		 	CTRL-R			 	CTRL-L	<u> </u>
1	D4	2		ARROW-L	ARROW-D	ARROW-U	PAGE-D	PAGE-U	END	ARROW-R
2	D5	3		ENTER	l I]	I I	[=	I	l I
3	E5	4	F12	MENU/PrtSc	/	;	[Р	i -	BCKSPACE
4	D7	5	INSERT				\	HOME	ĹL	DELETE
5	E1	8	F10	СОММА	PERIOD	i	ZERO	9	!F	F11
6	C0	9	F8	М	В	8	U	0	J	F9
7	A4	10	F7	N	G	Υ	K	7	Н	6
8	C3	15	F5	V	S	Т	R	5	С	F6
9	A1	16	F3	Х	l	E	4	3	D	F4
10	C5	19	F1	Z	SPACE	Q	2	1	W	F2
11	A2	20			 		 	SHIFT-L	 	SHIFT-R
12	C6	21	~		Α		TAB	CAPS LCK	i L	ESC
13	A7	22		ALT-R	 	ALT-L	i I	i L	 	i i
14	C7	23			I L	 	GUI	ī L	 	[[
15	A3	24	Fn	 				 	[]

My code doesn't report the Fn – Function (multimedia) keys to the Pi but certain Fn – Function key combinations cause the Teensy to control the video converter card. This eliminates the need for the push button keypad that comes with the video card.

Teensy Circuit Board – The 24 signals from the FPC keyboard connector are wired to the Teensy with a 2.5" x 1.5" circuit board I designed using the Freeware version of Eagle software. All signals are on the top layer with plated thru holes for pins. I found schematic and layout symbols on the web and modified those that weren't exactly what I needed. I had never used Eagle software but I found it fairly easy to figure out with lots of information online when I got stuck. Do the schematic first and it will make a layout with air wires that guide where you need to put traces. Sparkfun has excellent tutorials for Eagle schematic and layout.

The 24 signals on the keyboard FPC cable have a 1mm pitch. I purchased compatible FPC connectors from Ali express. 24 of the Teensy digital I/O pins are routed to the FPC connector pads and the remaining Teensy I/O are routed to thru hole pads for connecting to other devices (Touchpad, Video card, LEDs, and Pi). I added pads for 5 volt bypass caps in case the Teensy or Touchpad had power problems but found they weren't needed. The layout is shown below without the solid copper fill so you can easily see the signal traces. This is a revised layout that includes some corrections from the original that I used.



Three boards were fabricated by Oshpark in under 2 weeks. Oshpark accepts the Eagle layout file directly so you don't have to create Gerber files or even know how to use Eagle to order the boards. I put a drop of super glue on the FPC connector and attached it to the board (just to hold it in place). Then I used my Hakko soldering iron and a T18-C05 tip to solder the 24 connector pins to the board. I definitely needed geezer goggles to see the small solder joints. Flow soldering with the toaster oven would have worked much better but my wife vetoed that plan.

I bought the Teensy without pins so I could solder my own header pins around the edge and in the interior. A standard pin won't fit in the E4 and E5 locations but a small wire from a 1/8 watt resistor will work. The finished board with Teensy and FPC connector is shown below. I included pins so I could wire a reset switch to the back of the laptop case to initiate the Teensy loader but the reset button is only needed the very first time the loader is used. After that, the loader can initiate everything over USB so I didn't bother adding a button. This worked fine until I updated the Arduino/Teensyduino software to the latest version, then I had to open up the case and push the button for the first load.

My original layout had mounting holes that were way too small. I had to drill larger holes but ended up with a power to ground short when I installed the lower left mounting screw. I've enlarged the hole size and moved the 5 volt trace to fix these problems. The schematic and revised layout files are posted in the Sony_keyboard folder at my <u>Github</u> repo.

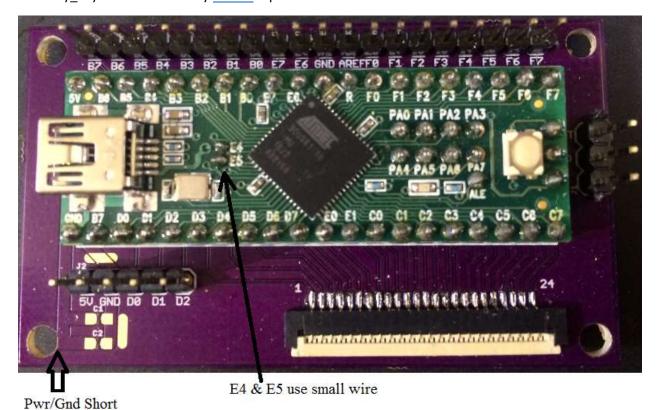


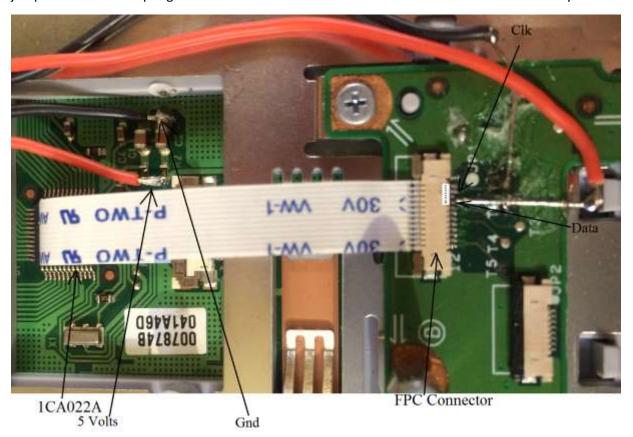
Table of FPC pins to Teensy pins

FPC	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Teensy	D3	D4	D5	E5	D7	E0	E4	E1	C0	A4	C1	A0	C2	A5	C3	A1	C4	A6	C5	A2	C6	A7	C7	A3

I used a Dremel tool to cut the air vent bars on the back of the laptop so that the Pi's network and USB connectors are accessible. The USB cables from the Teensy and from the SSD come out the back of the laptop and loop back to the connectors on the Pi as shown below. I will eventually move the USB cables inside once I'm done fooling around with the Teensy code.



Touchpad – The following picture of the Touchpad circuit boards show the connections to Clock, Data, +5 volts, and ground. The +5 volts and ground were connected on the circuit board that contains the 1CA022A touchpad chip. The Clock and Data were connected on the button board. The Clock is on pad T1 and connector pins 1 & 2, Data is on pad T2 and connector pins 3 & 4. It was difficult to attach the Clock and Data wires to the small pads. To give the solder joint some strain relief, I tinned a wire with solder and glued it to the board. After the glue dried, I soldered it to the connection point and to the jumper wire. I used super glue but I think JB Weld would work better because it's not so runny.



I started with ps/2 touchpad code from playground.arduino and modified it for USB. I also added a watchdog timer to break out of the "while loops". The touchpad uses the ps/2 mouse protocol which sends X and Y movement values as signed 9 bit values. The Teensy code converts the movement values to signed 8 bits and inverts the sign of the Y data to match the polarity of the PJRC Mouse.move USB function. The left and right buttons on the touchpad are converted from ps/2 to the PJRC Mouse.set_buttons USB function. Normally a touchpad sends position and button data in stream mode whenever a change is detected and the host runs an interrupt service routine. I needed the touchpad to send position and button data when queried by the Teensy (remote mode). In my code, after completing a keyboard scan, the Teensy requests data from the touchpad and if any movement or buttons are pressed, the information is sent to the Pi over the same USB cable as the keyboard data (known as USB Composite). The touchpad USB data can be disabled by pressing Fn and F12 on the keyboard. It can be turned back on with the same sequence. This allows a USB or Bluetooth mouse to be used instead of the touchpad.

Display - The LCD from the Sony laptop is a Quanta Display Inc. part number QD15TL03. It needs LVDS video signals so the HDMI out of the Pi must be converted by the M.NT68676 board. Several eBay companies sell the M.NT68676 board. Verify with an eBay message prior to purchase that the board is compatible with your LCD model number. The Monitor Control Board Specification gives the interface definition of the M.NT68676 board.

The board contains a 4 Megabit serial flash memory (part number Winbond 25X40CLNIG) that is programmed by the vendor for the LCD parameters. The main chip on the board is marked NOVATEK NT68676UFG but I could find no data sheet online. The board takes in 12 VDC and a 54329E buck regulator outputs 5 volts. There are two 3.3 volt linear regulators, part number 1117B 33 that feed the LCD and the NT68676UFG chip. The NT68676UFG is also fed 1.8 volts from an 1117B 18 regulator. A two watt stereo audio amplifier, part number TDA7496LG, is fed from the HDMI audio and outputs to a 3.5mm jack which I have cabled to the laptop speakers. I've tried 3 different speaker models and they all have pretty low volume. Linux commands from a terminal window are used to switch the audio to the speakers or to the headphone jack.

Speakers:

amixer cset numid=3 2

Headphone jack:

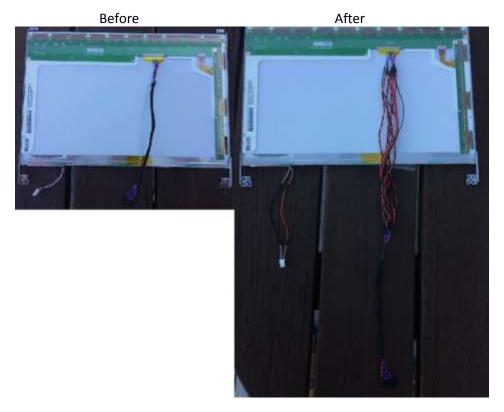
amixer cset numid=3 1

I've made aliases called "speakers" and "headphones" in .bashrc that execute the amixer commands so I don't need to remember them.

Before making any video cable modifications, I connected the M.NT68676 video board to the Pi and confirmed it correctly drove the LCD. The keypad menu items on the screen were in Chinese. Luckily I found an <u>Alex Eames video</u> which shows the key sequence to change the language from Chinese to English. The sequence is Menu, Right, Menu, Menu, then use the Left or Right buttons to highlight English, and then push Menu to select it.

I needed to lengthen the LVDS cable so it would reach the board when mounted in the laptop. I added 10 inches to each wire including the 4 twisted pairs using 28 gauge stranded wire. I also added 4 inches to the 2 wires that feed the backlight as shown in these before – after pictures. The display was noisy and not syncing properly with the longer cable but once I pulled the wires tight and wrapped them with electrical tape, it worked fine.

In hindsight, I should have lengthened the original laptop video cable by about 4 inches instead of the cable that came with the kit. The original cable has a shielded twisted pair clock with grounded conductive tape over all wires and is flat for the section that fits behind the LCD. My only concern with using this cable was the wire gauge is really tiny so I thought it might be fragile and hard to solder.

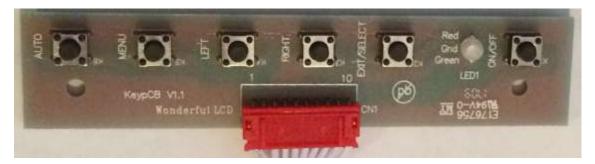


The pinout for the video cable is below:

LCD Connector	Signal Name	Card Connector			
1	GND	4			
2	3.3V	1			
3	3.3V	2			
8	LVDS ODD 0-	7			
9	LVDS ODD 0+	8			
10	GND	5			
11	LVDS ODD 1-	9			
12	LVDS ODD 1+	10			
13	GND	6			
14	LVDS ODD 2-	11			
15	LVDS ODD 2+	12			
16	GND	13			
17	LVDS ODD CLK-	15			
18	LVDS ODD CLK+	16			
19	GND	14			

After lengthening my video cable, I found that 25 inch cables can be purchased from <u>eBay</u>. The \$23 price tag is pretty steep considering I bought the converter card, backlight supply, control pad, and short LVDS cable for \$25.

I replaced the key pad switch board shown below with the Teensy and Fn-Function Keys.



The only switches I wired were Left (volume down), Right (volume up), Menu, and On/Off (Power). Auto didn't seem to do anything and Exit/Select wasn't needed because the controller will exit if no keys are pressed for a couple seconds. The switches are pulled up on the video card to 3.3 volts and go to ground when pushed. The Teensy must not drive these signals to 5 volts or it could damage the control chip. The following picture shows where 4 wires were soldered to the keypad connector on the backside of the video card. The connections to the Teensy are given in the table.



Key Function	Board	Teensy			
Volume Up	K1	B7			
Volume Down	K2	B6			
Menu	K4	B5			
Power	K0	B4			

The Teensy code floats the 4 control signals but sends a low when the Fn & Function keys are pressed per the following table. Menu, Volume Up, and Volume down can be sent low with manual keypresses to navigate thru the video card selections. This works for commands that are seldom changed but would get old fast if I had to do this for Mute or Brightness. For these commands, the Teensy pulses the Menu, Volume Up, and Volume Down signals in a sequence to reach the desired command. I tried speeding up the pulsing sequence but the video controller chip expects human speed or it doesn't work properly.

Key Sequence	Result
Fn and F1	Pulse the Menu signal low
Fn and F2	Mute/Unmute the HDMI Audio ¹
Fn and F3	Send the Volume Down signal low
Fn and F4	Send the Volume Up signal low
Fn and F5	Lower the Display Brightness ²
Fn and F6	Raise the Display Brightness ³
Fn and F7	Pulse the Display Power signal low

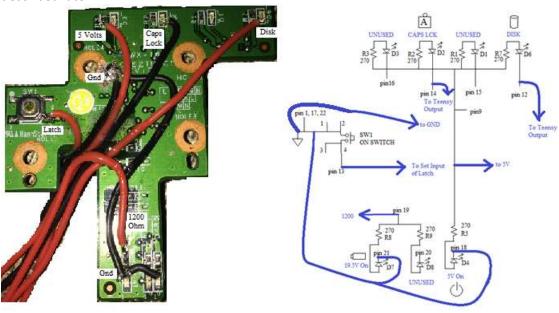
¹ Pulse Menu, Volume Up, Menu, Volume Down, Menu, Volume Down

I use the display power signal to momentarily turn off the display when the battery is depleted and needs to be plugged into the charger.

² Pulse Menu, Menu, Menu, and then send Volume Down low until key is released

³ Pulse Menu, Menu, Menu, and then send Volume Up low until key is released

Power and LEDs – The following schematic of the Power Switch & LED Board shows blue connections where I added wires to the board. I chose to solder directly to the surface mount components instead of dealing with the ribbon cable connector. The Battery LED turns on when the 19.5 volts is connected and the Power LED lights when the Pi is powered with 5 volts. The Caps Lock LED is driven by the Teensy and is based on the "keyboard_leds" variable controlled by the Pi. The Disk LED is also driven by the Teensy and is used for code debug. The power switch is connected to the "Set" input of the Latch circuit described later.



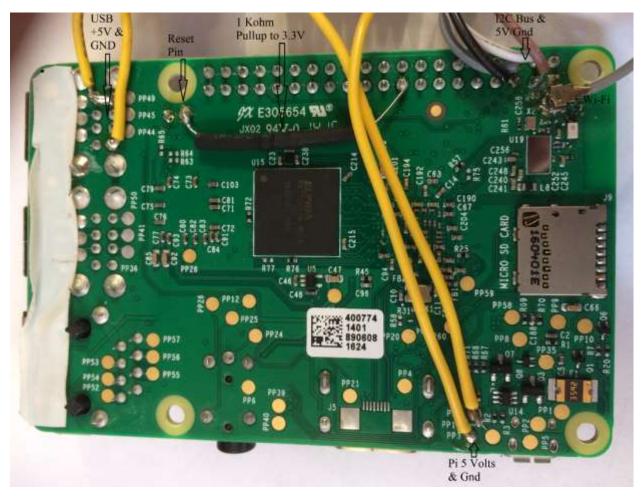
The four LEDs are all turned on in this picture.

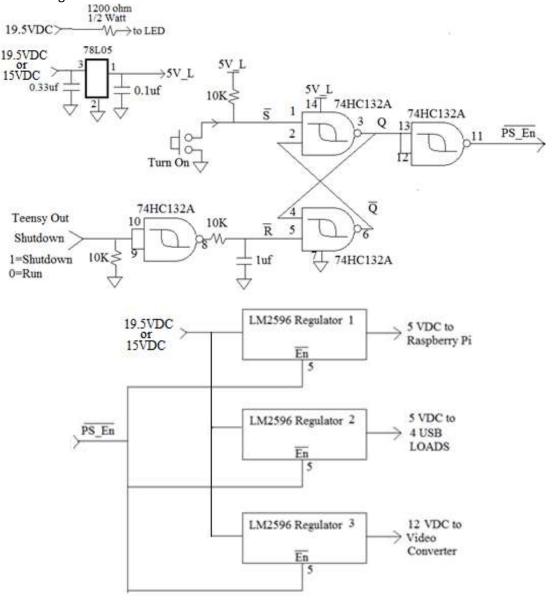


Power Supplies – The laptop is fed 19.5 volts DC if the AC adapter is installed, otherwise the battery voltage is fed to 3 regulator boards based on the LM2596. These boards drop the voltage down to 12V for the video card, 5V for the Pi, and 5V for the USB ports. Many versions of this board can be found on Amazon and EBay. I adjusted the potentiometer on each board to give the desired voltage, then connected them to their respective loads and readjusted the voltage. The unmodified LM2596 board is shown below.

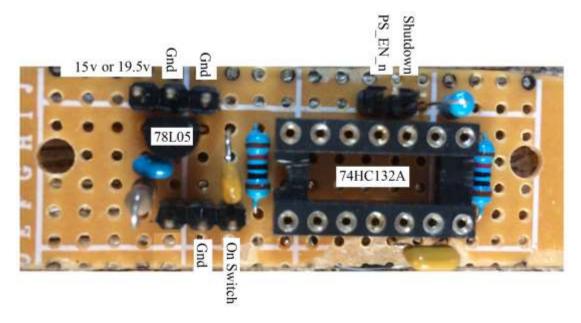


The 5 volt power from regulator #1 is soldered to the Pi (back side) at the large pad labeled PP7 near the micro USB connector. For USB power, the AP2253 at location U13 on the Pi (front side) was removed in order to separate the Pi 5 volt bus from the USB 5 volt bus. The 5 volts from regulator #2 is soldered to the power pins of the USB Connector as shown in the picture below. The power to the video card from regulator #3 uses a 2.5 x 5.5mm connector. This picture also shows the Wi-Fi connector, the reset pullup, and the four I2C wires.

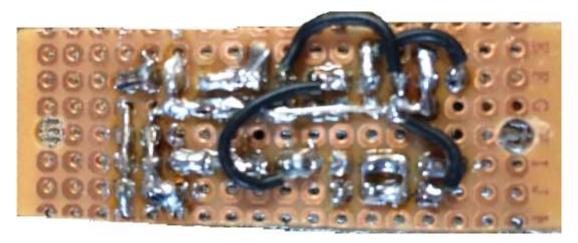




The Teensy will cause a shutdown if control-alt-s is typed on the keyboard or if a Linux shutdown is given. The latch circuit board shown below has a 14 pin socket for the 74HC132A and header pins for easy jumper attachment.



The components are soldered with point to point "ugly" wires on the back side.



When the laptop is turned off and not plugged into the charger, the three (disabled) LM2596 regulators, 78L05, and 74HC132A measure about 2ma from the battery. Another 4ma is drawn from the battery by the voltage divider resistors for the ADC (described later). The constant 6ma current from the battery will drain a fully charged battery in 4Ah/6mA = 667 hours = 27.8 days.

Occasionally I will lock up the Pi with a bad program but instead of cycling power, I reset the Pi with Control-Alt-r. I connected the Pi's reset pad to one of the Teensy's I/O pins. I also added a 1K pull up on the reset so I can plug and unplug USB cables and not cause a glitch that resets the Pi. This picture also shows the USB power switch that was removed at location U13 so that the Pi's 5 volt bus is separated from the USB 5 volt bus.



Solid State Drive – I used a standard USB to SATA cable and followed the <u>ETA Prime procedure</u> to boot from an external drive. I had a spare 240GB SSD which is a bit of an overkill but the price was right. Now I no longer need to worry about wearing out the micro SD card or running out of space. The micro SD card has been removed from the Pi which means I don't need to mount the Pi with the SD card connector accessible. The SSD fits in the laptop hard drive compartment (see below) but there was no mounting hardware available. To keep the drive from moving around, I used tightly rolled paper to take up the remaining space.

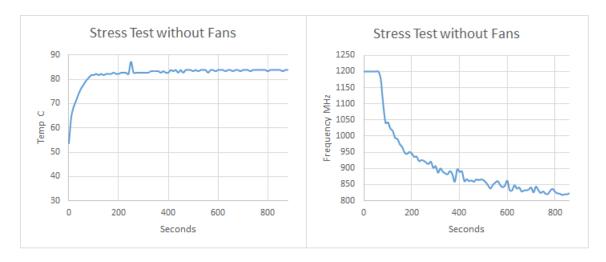


Wi-Fi Antenna – The laptop has a Wi-Fi antenna above the LCD, fed by a coax with a U.FL connector. In order to use the laptop's Wi-Fi antenna, I had to add a matching U.FL connector to the Pi and move a zero ohm resistor. This mod was a bit difficult due to the small size of the components. The <u>wardr instructions</u> are very clear and include some magnified pictures. A microscope would really come in handy for this mod. I used the <u>Wavemon</u> program to take before and after signal strength measurements so I could tell if the new antenna was working. The following table shows a definite improvement in the Wi-Fi signal strength.

Location & distance to source	Signal Level Before Mod	Signal Level After Mod
Desk (next to Wi-Fi router)	-20dBm	-17dBm
Chair (10ft)	-40dBm	-37dBm
Bedroom (15ft)	-59dBm	-50dBm
Dining room (15ft & wall)	-68dBm	-54dBm
Kitchen (20ft & wall)	-69dBm	-62dBm

The laptop has a Bluetooth antenna with coax and a U.FL connector that I'm currently not using. I haven't found anything on the web yet for hooking this up to the Pi.

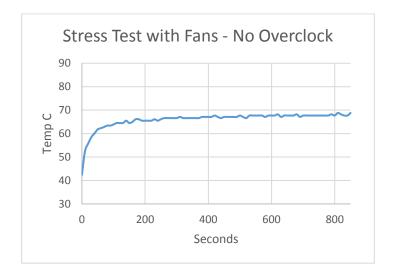
Cooling - I ran the <u>stress test</u> from Core Electronics on the laptop to measure the temperature of the ARM processor and its clock speed. Core Electronics says their stress test gives "real life, high-load usage across all of the Pi's resources". My Pi has a small aluminum heat sink on the ARM processor and I wanted to see what would happen if I didn't have any fans. The curves below show that the temperature quickly rose to 83°C and the clock speed was heavily throttled to avoid overheating.



I placed a 5 volt fan below the Pi, near the video connector but I found it didn't help very much because the height of the video connector blocked the airflow and the ARM heat sink fins needed to be rotated 90 degrees. I moved the fan to the right, away from the video connector and angled it at the processor. I also added a 12 volt fan at the rear of the case to blow the hot air out the back and draw cool air up thru the floor vents. The fans are sandwiched between the case floor and the metal shield for the keyboard. Double back foam tape keeps them from moving and vibrating (they're still pretty loud). The fan locations are shown below.



The curve below shows the temperature not exceeding 69°C so the 1200MHz clock frequency never throttled. This is the fan configuration that I have continued to use.



I changed the /boot/config.txt file to add the "usually-safe" overclock settings given on the <u>overclocking wiki</u>. So far I've had no issues with these values.

total_mem=1024

arm_freq=1300

gpu_freq=500

core_freq=500

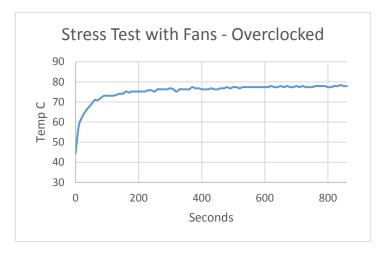
sdram_freq=500

sdram_schmoo=0x02000020

over_voltage=2

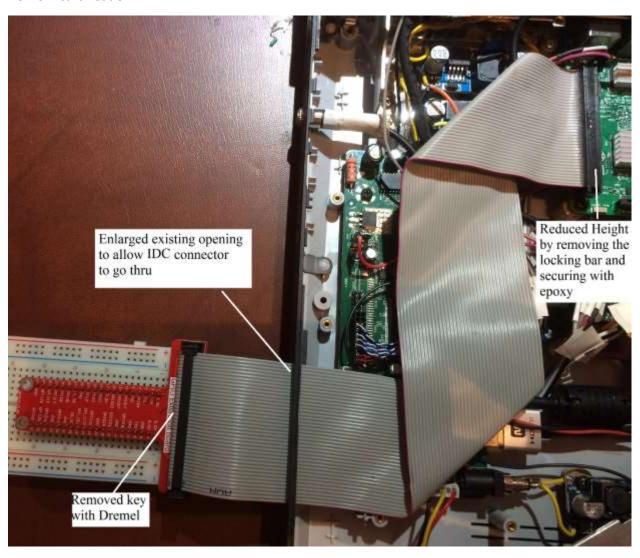
sdram_over_voltage=2

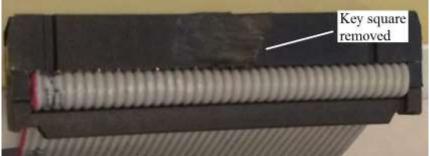
I repeated the stress test with these overclock settings. The 1300 MHz clock speed never throttled and the temperature maxed out at 78°C. I'll keep these settings unless I notice any problems.



For a final cooling test, I ran cpuburn-a53. Core Electronics says it "maxes out your Pi's CPU completely" and "is the most intense CPU stress test you can run and doesn't represent a realistic real-life scenario". The temperature quickly maxed out at 83°C and the clock speed throttled down to 820MHz. I guess I should be happy that the Pi didn't lock up (or burn up).

GPIO - A 40 pin ribbon cable is attached to the GPIO connector on the Pi and routed out the side of the laptop for bread-boarding. The IDC connector on the Pi end of the cable was too tall and touched the keyboard so I removed the locking piece from the top of the connector to reduce the height and glued it together with JB Weld. I used my Dremel tool to remove the key square on the breadboard end of the IDC connector so it can plug into the GPIO expansion board. I also used the Dremel to enlarge the existing opening in the laptop case so the IDC connector can pass thru. This opening was originally for a PCMCIA card reader.





I2C Bus - I installed the Adafruit DS1307 based real time clock (RTC) per their <u>instructions</u> and connected it to the Pi's I2C bus by soldering jumpers per this table. I also connected the Teensy to the I2C bus.

I2C Name	Pi GPIO & Pin Number	RTC Pin Name	Teensy Pin
Clock	GPIO 3 Pin 5	SCL	D0
Data	GPIO 2 Pin 3	SDA	D1
+5 Volts	Pin 4	5V	
Ground	Pin 6	GND	

I cut the small traces between the large pads at the locations shown below in order to disconnect the clock and data pull up resistors. These resistors are pulled to 5 volts which is not compatible with the Pi.



The 1.8K pull up resistors on the Pi are the only pull ups on the I2C bus. The Pi pull ups go to 3.3 volts but the DS1307 is a 5 volt device. The DS1307 <u>datasheet</u> says the minimum voltage for a logic high is 2.2 volts and all testing so far shows the RTC and Teensy work fine with 3.3 volt signals. The RTC is at I2C address 0x68.

The Teensy is at I2C address 0x08 and if it receives a value of 0x5a, it waits six seconds to let the Pi finish and then resets the power latch to disable the regulators and power down the laptop.

The i2cset command shown below is used to send values to the Teensy. This command is part of the i2ctools that were loaded per the Adafruit RTC instructions.

i2cset -y 1 0x08 0x00 0x5a

The –y disables interactive mode, 1 specifies the i2c bus number, 0x08 is the i2c address for the Teensy, 0x00 is the register pointer (always zero for my code), 0x5a is the value sent to the Teensy. Other values can be sent to the Teensy per the following table.

5a = Power off the laptop b7 = Reset the Pi and the Teensy 10 = Turn on the "Disk" LED 11 = Turn off the "Disk" LED e2 = Turn the laptop display off momentarily I had been using a turnoff script which sent 0x5a to the Teensy and then issued a sudo shutdown -h now command to the Pi. This method worked but I wanted to use systemd to send the 0x5a value automatically when a shutdown command from a terminal window or from the GUI is given. A post on stackexchange describes how to do this with a systemd unit file which you then load and enable with terminal commands, systemctl daemon-reload and systemctl enable servicename -now The following is my power_off.service unit file located at /lib/systemd/system. The power_down program that it executes contains a single command: i2cset -y 1 0x08 0x00 0x5a [Unit] Description=Turn off power latch Service [Service] Type=oneshot RemainAfterExit=true ExecStop=/home/pi/power_down [Install]

WantedBy=multi-user.target

Battery Operation - I used the laptop for a couple months with the AC adapter while I figured out battery operation. Luckily this Sony Vaio laptop has the battery charging circuit on a separate board that still fits in the case with all my new hardware. This picture shows the finished laptop (less the keyboard) with the battery charging board and battery.



The laptop battery has a 2 wire SMBus interface that I couldn't make work with the Pi's I2C bus. The Pi would not issue a repeated start which is required to read the registers in the battery. I tried all the suggestions given on the internet with no luck so I wrote a crude C program for the Pi that bit-bangs two GPIO pins to make a dedicated SMBus for the battery. The program displays the battery voltage, current, temperature, status, and state of charge. It also displays the time to full if it's charging or the time to empty if it's not charging. The "battery" alias executes the C program as shown below.

```
pi@raspberrypi:~ $ battery
Voltage = 16.791 Volts
Current = 166 mA
Temperature = 24.65 degrees C
State of Charge = 81 percent
Time to full = 30 minutes
Battery Status
Initialized
```

I used the Sparkfun <u>tutorial</u> on WiringPi and the Geany IDE to write my C program. The table below gives the register names and numbers as well as the values I read from my battery. Bold items are read by the "read_battery.c" program which is available at my <u>Github</u> repo. See the <u>Smart Battery Data Specification</u> for a complete description of each register and the SMBus protocol.

REMAINING CAPACITY ALARM REMAINING TIME ALARM REMAINING CAPACITY REMPERATURE REMPERATURE REMPERATURE REMPERATURE REMPERATURE REMAINING CAPACITY RELATIVE SOC RELATIVE SOC REMAINING CAPACITY REMAINING CAPACITY RUN TIME TO EMPTY RAVERAGE TIME TO FULL RESIGN CHARGE CURRENT RESIGN CAPACITY R	Register Name	Number	Typical Value and Description
BATTERY MODE 0x03 Report in 10mWh, Internal Charge Controller Supported TEMPERATURE 0x08 24.25°C to 29.35°C VOLTAGE 0x09 14.513V at 4% SoC, 16.620V at 100% SoC CURRENT 0x0A -1.4A w/o charger, +1.547A at 4% SoC w/ charger, 4ma@100% AVERAGE CURRENT 0x0B same as above except with one minute rolling average MAX ERROR 0x0C 25% error in SoC calculation RELATIVE SoC 0x0D 100% to 5% fully charged to nearly empty ABSOLUTE SoC 0x0E 85% when fully charged, and drops with SoC REMAINING CAPACITY 0x0F 50.51Wh when fully charged, and drops as SoC drops FULL CHARGE CAPACITY 0x10 50.51Wh doesn't change with SoC AVERAGE TIME TO EMPTY 0x11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY 0x12 same as above with 1 minute rolling average DESIGN CHARGE CURRENT 0x14 1.5A 0x15 16.672V DX16 0x00A0 Init & fully charged (at 100%) 0x00CO Init & discharge (at 70%) 0x002CO remaining cap alarm, init & discharge (at 10%) 0x0280 remaining cap alarm, init & discharge (at 5%) 0x0280 remaining cap alarm, init (at 10% w/ charger) 0x0080 init (at 30% w/ charger) 0x0080 init (at 30% w/ charger) 0x16 0x15 14.8V MFG DATE 0x18 June 8, 2004	REMAINING CAPACITY ALARM	0x01	5.92Wh (10% of 59.2Wh Design Capacity)
TEMPERATURE0x0824.25°C to 29.35°CVOLTAGE0x0914.513V at 4% SoC, 16.620V at 100% SoCCURRENT0x0A-1.4A w/o charger, +1.547A at 4% SoC w/ charger, 4ma@100%AVERAGE CURRENT0x0Bsame as above except with one minute rolling averageMAX ERROR0x0C25% error in SoC calculationRELATIVE SoC0x0D100% to 5% fully charged to nearly emptyABSOLUTE SoC0x0E85% when fully charged, and drops with SoCREMAINING CAPACITY0x0F50.51Wh when fully charged, and drops as SoC dropsFULL CHARGE CAPACITY0x1050.51Wh doesn't change with SoCRUN TIME TO EMPTY0x11137 minutes at full charge, 7 minutes at 5% SoCAVERAGE TIME TO EMPTY0x12same as above with 1 minute rolling averageDESIGN CHARGE CURRENT0x141.5ADESIGN CHARGE VOLTAGE0x1516.672VBATTERY STATUS0x160x00A0 Init & fully charged (at 100%)AVERAGE TIME TO EMPTY0x160x00A0 Init & fully charged (at 100%)DESIGN CHARGE CURRENT0x160x00A0 Init & fully charged (at 100%)DESIGN CHARGE VOLTAGE0x1516.672VBATTERY STATUS0x160x00A0 Init & fully charged (at 100%)AVERAGE TIME TO EMPTY0x160x00A0 Init & fully charged (at 100%)AVERAGE TIME TO EMPTY0x160x00A0 Init & fully charged (at 100%)DESIGN CHARGE CURRENT0x17235 charge cyclesDESIGN CAPACITY0x1859.20WhDESIGN VOLTAGE0x1914.8VMFG DATE0x18	REMAINING TIME ALARM	0x02	10 minutes
VOLTAGE0x0914.513V at 4% SoC, 16.620V at 100% SoCCURRENT0x0A-1.4A w/o charger, +1.547A at 4% SoC w/ charger, 4ma@100%AVERAGE CURRENT0x0Bsame as above except with one minute rolling averageMAX ERROR0x0C25% error in SoC calculationRELATIVE SoC0x0D100% to 5% fully charged to nearly emptyABSOLUTE SoC0x0E85% when fully charged, and drops with SoCREMAINING CAPACITY0x0F50.51Wh when fully charged, and drops as SoC dropsFULL CHARGE CAPACITY0x1050.51Wh doesn't change with SoCRUN TIME TO EMPTY0x11137 minutes at full charge, 7 minutes at 5% SoCAVERAGE TIME TO FULL0x12same as above with 1 minute rolling averageDESIGN CHARGE CURRENT0x141.5ADESIGN CHARGE VOLTAGE0x1516.672VBATTERY STATUS0x160x00A0 Init & fully charged (at 100%) 0x002C0 remaining cap alarm, init & discharge (at 10%) 0x02C0 remaining cap & time alarm, init & discharge (at 5%) 0x0280 remaining cap alarm, init (at 10% w/ charger)CYCLE COUNT0x17235 charge cyclesDESIGN CAPACITY0x1859.20WhDESIGN VOLTAGE0x1914.8VMFG DATE0x18June 8, 2004	BATTERY MODE	0x03	Report in 10mWh, Internal Charge Controller Supported
CURRENT 0x0A -1.4A w/o charger, +1.547A at 4% SoC w/ charger, 4ma@100% AVERAGE CURRENT 0x0B same as above except with one minute rolling average MAX ERROR 0x0C 25% error in SoC calculation RELATIVE SoC 0x0D 100% to 5% fully charged to nearly empty ABSOLUTE SoC 0x0E 85% when fully charged, and drops with SoC REMAINING CAPACITY 0x0F 50.51Wh when fully charged, and drops as SoC drops FULL CHARGE CAPACITY 0x10 50.51Wh doesn't change with SoC RULL CHARGE CAPACITY 0x11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY 0x11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO FULL 0x13 118 min when charge starts at 5% SoC (1 min rolling average) DESIGN CHARGE CURRENT 0x14 1.5A DESIGN CHARGE VOLTAGE 0x15 16.672V BATTERY STATUS 0x16 0x00A0 Init & fully charged (at 100%) 0x00C0 remaining cap alarm, init & discharge (at 10%) 0x02C0 remaining cap alarm, init & discharge (at 5%) 0x0280 remaining cap alarm, init & discharge (at 5%) 0x0080 init (at 30% w/ charger) CYCLE COUNT 0x17 235 charge cycles DESIGN CAPACITY 0x18 59.20Wh DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x00A DIT AT SOL Ox10 14.8V	TEMPERATURE	0x08	24.25°C to 29.35°C
AVERAGE CURRENT 0x0B same as above except with one minute rolling average MAX ERROR 0x0C 25% error in SoC calculation RELATIVE SoC 0x0D 100% to 5% fully charged to nearly empty ABSOLUTE SoC 0x0E 85% when fully charged, and drops with SoC REMAINING CAPACITY 0x0F 50.51Wh when fully charged, and drops as SoC drops FULL CHARGE CAPACITY 0x10 50.51Wh doesn't change with SoC RUN TIME TO EMPTY 0x11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY 0x12 same as above with 1 minute rolling average AVERAGE TIME TO FULL 0x13 118 min when charge starts at 5% SoC (1 min rolling average) DESIGN CHARGE CURRENT 0x14 1.5A DESIGN CHARGE VOLTAGE 0x15 16.672V BATTERY STATUS 0x16 0x00A0 Init & fully charged (at 100%) 0x00C0 Init & discharge (at 70%) 0x02C0 remaining cap alarm, init & discharge (at 10%) 0x03C0 remaining cap alarm, init & discharge (at 5%) 0x0280 remaining cap alarm, init (at 10% w/ charger) 0x0080 init (at 30% w/ charger) CYCLE COUNT 0x17 235 charge cycles DESIGN CAPACITY 0x18 59.20Wh DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x00 10x00	VOLTAGE	0x09	14.513V at 4% SoC, 16.620V at 100% SoC
MAX ERROR 0x0C 25% error in SoC calculation RELATIVE SoC 0x0D 100% to 5% fully charged to nearly empty ABSOLUTE SoC 0x0E 85% when fully charged, and drops with SoC REMAINING CAPACITY 0x0F 50.51Wh when fully charged, and drops as SoC drops FULL CHARGE CAPACITY 0x10 50.51Wh doesn't change with SoC RUN TIME TO EMPTY 0x11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY 0x12 same as above with 1 minute rolling average AVERAGE TIME TO FULL 0x13 118 min when charge starts at 5% SoC (1 min rolling average) DESIGN CHARGE CURRENT 0x14 1.5A DESIGN CHARGE VOLTAGE 0x15 16.672V BATTERY STATUS 0x16 0x00A0 Init & fully charged (at 100%) 0x00C0 Init & discharge (at 70%) 0x02C0 remaining cap alarm, init & discharge (at 10%) 0x02C0 remaining cap alarm, init & discharge (at 5%) 0x0280 remaining cap alarm, init (at 10% w/ charger) CYCLE COUNT 0x17 235 charge cycles DESIGN CAPACITY 0x18 59.20Wh DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x06 1 100% to 5% fully charged to nearly empty 100x06 init (at 30% w/ charger)	CURRENT	0x0A	-1.4A w/o charger, +1.547A at 4% SoC w/ charger, 4ma@100%
RELATIVE SoC0x0D100% to 5% fully charged to nearly emptyABSOLUTE SoC0x0E85% when fully charged, and drops with SoCREMAINING CAPACITY0x0F50.51Wh when fully charged, and drops as SoC dropsFULL CHARGE CAPACITY0x1050.51Wh doesn't change with SoCRUN TIME TO EMPTY0x11137 minutes at full charge, 7 minutes at 5% SoCAVERAGE TIME TO EMPTY0x12same as above with 1 minute rolling averageAVERAGE TIME TO FULL0x13118 min when charge starts at 5% SoC (1 min rolling average)DESIGN CHARGE CURRENT0x141.5ADESIGN CHARGE VOLTAGE0x1516.672VBATTERY STATUS0x160x00A0 Init & fully charged (at 100%)0x00CO Init & discharge (at 70%)0x002C0 remaining cap alarm, init & discharge (at 10%)0x02C0 remaining cap alarm, init (at 10% w/ charger)0x0280 remaining cap alarm, init (at 10% w/ charger)CYCLE COUNT0x17235 charge cyclesDESIGN CAPACITY0x1859.20WhDESIGN VOLTAGE0x1914.8VMFG DATE0x18June 8, 2004	AVERAGE CURRENT	0x0B	same as above except with one minute rolling average
ABSOLUTE SoC REMAINING CAPACITY OXOF SO.51Wh when fully charged, and drops with SoC REMAINING CAPACITY OX10 SO.51Wh when fully charged, and drops as SoC drops FULL CHARGE CAPACITY OX10 SO.51Wh doesn't change with SoC RUN TIME TO EMPTY OX11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY OX12 Same as above with 1 minute rolling average AVERAGE TIME TO FULL OX13 118 min when charge starts at 5% SoC (1 min rolling average) DESIGN CHARGE CURRENT DESIGN CHARGE VOLTAGE BATTERY STATUS OX16 OX00A0 Init & fully charged (at 100%) OX00CO Init & discharge (at 70%) OX00CO remaining cap alarm, init & discharge (at 5%) OX0280 remaining cap alarm, init (at 10% w/ charger) CYCLE COUNT OX17 OX17 OX18 S9.20Wh DESIGN VOLTAGE MFG DATE OX0B June 8, 2004	MAX ERROR	0x0C	25% error in SoC calculation
REMAINING CAPACITY	RELATIVE SoC	0x0D	100% to 5% fully charged to nearly empty
FULL CHARGE CAPACITY RUN TIME TO EMPTY Ox11 137 minutes at full charge, 7 minutes at 5% SoC AVERAGE TIME TO EMPTY AVERAGE TIME TO FULL DESIGN CHARGE CURRENT DESIGN CHARGE VOLTAGE BATTERY STATUS Ox16 0x00A0 Init & fully charged (at 100%) Ox02C0 remaining cap alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) CYCLE COUNT DESIGN CAPACITY DESIGN CAPACITY DESIGN CAPACITY Ox18 59.20Wh MFG DATE Ox10 50.51Wh doesn't change with SoC RUNT 137 minutes at full charge, 7 minutes at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge, 7 minutes at full charge, 8 SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min when charge starts at 5% SoC (1 min rolling average) 118 min w	ABSOLUTE SoC	0x0E	85% when fully charged, and drops with SoC
RUN TIME TO EMPTY	REMAINING CAPACITY	0x0F	50.51Wh when fully charged, and drops as SoC drops
AVERAGE TIME TO EMPTY AVERAGE TIME TO FULL DESIGN CHARGE CURRENT DESIGN CHARGE VOLTAGE BATTERY STATUS Ox16 Ox17 Ox18 Ox19 Ox18 Ox19 Ox18 Ox19 Ox18 Ox19 Ox18 Ox19 Ox18 Ox18 Ox18 Ox18 Ox18 Ox19 Ox18 Ox18	FULL CHARGE CAPACITY	0x10	50.51Wh doesn't change with SoC
AVERAGE TIME TO FULL DESIGN CHARGE CURRENT DESIGN CHARGE VOLTAGE BATTERY STATUS Ox16 Ox00A0 Init & fully charged (at 100%) Ox00CO Init & discharge (at 70%) Ox03CO remaining cap alarm, init & discharge (at 10%) Ox0280 remaining cap alarm, init (at 10% w/ charger) CYCLE COUNT DESIGN CAPACITY DESIGN VOLTAGE Ox18 S9.20Wh DESIGN VOLTAGE Ox18 June 8, 2004	RUN TIME TO EMPTY	0x11	137 minutes at full charge, 7 minutes at 5% SoC
DESIGN CHARGE CURRENT DESIGN CHARGE VOLTAGE BATTERY STATUS Ox16 Ox16 Ox00A0 Init & fully charged (at 100%) Ox00C0 Init & discharge (at 70%) Ox02C0 remaining cap alarm, init & discharge (at 10%) Ox02S0 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT DESIGN CAPACITY Ox18 S9.20Wh DESIGN VOLTAGE Ox19 June 8, 2004	AVERAGE TIME TO EMPTY	0x12	same as above with 1 minute rolling average
DESIGN CHARGE VOLTAGE BATTERY STATUS Ox16 Ox00A0 Init & fully charged (at 100%) Ox00CO Init & discharge (at 70%) Ox02CO remaining cap alarm, init & discharge (at 10%) Ox03CO remaining cap & time alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) CYCLE COUNT Ox17 Ox17 Ox17 Ox18 S9.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 Ox10 Init & fully charged (at 100%) Ox0080 init & discharge (at 5%) Ox0080 init (at 30% w/ charger) 14.8V MFG DATE Ox18 June 8, 2004	AVERAGE TIME TO FULL	0x13	118 min when charge starts at 5% SoC (1 min rolling average)
BATTERY STATUS Ox16 Ox00A0 Init & fully charged (at 100%) Ox00C0 Init & discharge (at 70%) Ox02C0 remaining cap alarm, init & discharge (at 10%) Ox03C0 remaining cap & time alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 Ox17 Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004	DESIGN CHARGE CURRENT	0x14	1.5A
Ox00C0 Init & discharge (at 70%) Ox02C0 remaining cap alarm, init & discharge (at 10%) Ox03C0 remaining cap & time alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004	DESIGN CHARGE VOLTAGE	0x15	16.672V
Ox02C0 remaining cap alarm, init & discharge (at 10%) Ox03C0 remaining cap & time alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004	BATTERY STATUS	0x16	0x00A0 Init & fully charged (at 100%)
Ox03C0 remaining cap & time alarm, init & discharge (at 5%) Ox0280 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004			0x00C0 Init & discharge (at 70%)
Ox0280 remaining cap alarm, init (at 10% w/ charger) Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004			0x02C0 remaining cap alarm, init & discharge (at 10%)
Ox0080 init (at 30% w/ charger) CYCLE COUNT Ox17 235 charge cycles DESIGN CAPACITY Ox18 59.20Wh DESIGN VOLTAGE Ox19 14.8V MFG DATE Ox18 June 8, 2004			0x03C0 remaining cap & time alarm, init & discharge (at 5%)
CYCLE COUNT 0x17 235 charge cycles DESIGN CAPACITY 0x18 59.20Wh DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x1B June 8, 2004			0x0280 remaining cap alarm, init (at 10% w/ charger)
DESIGN CAPACITY 0x18 59.20Wh DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x1B June 8, 2004			0x0080 init (at 30% w/ charger)
DESIGN VOLTAGE 0x19 14.8V MFG DATE 0x1B June 8, 2004	CYCLE COUNT	0x17	235 charge cycles
MFG DATE 0x1B June 8, 2004	DESIGN CAPACITY	0x18	59.20Wh
·	DESIGN VOLTAGE	0x19	14.8V
SERIAL NUM 0x1C 3292	MFG DATE	0x1B	June 8, 2004
	SERIAL NUM	0x1C	3292

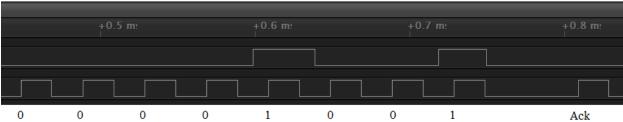
Pi GPIO 26, (connector pin 37) is programmed to be the SMBus Clock with a 40 usec period. GPIO 19 (connector pin 35) is programmed to be the SMBus Data. Logic analyzer screen captures of the Clock (bottom trace) and Data (top trace) during a register read operation are given below. The battery will sink the Clock line low (known as clock stretching) to hold off the Pi from sending more clocks. I used the logic analyzer to see how long I needed to wait for the battery to release the clock after each sequence so that my code doesn't need to deal with clock stretching. The first figure shows the entire sequence needed to read the battery voltage. Each of the sequences is shown in greater detail in the subsequent figures.



The start sequence (falling clock when data is low) causes the battery to capture the next 8 data bits on the rising clock. To talk to the battery, the first 7 data bits must be 0001011 (0x0b). The 8th data bit is 0 which signifies a write will follow in the next sequence. All 8 bits together are 0x16 when writing to the battery. After the 8th clock pulse, the Pi floats the data line so the battery can sink it low to acknowledge it has received the 8 bits. The Ack/Nack bit is clocked in on the 9th rising edge and an error flag is set in the code if a 1 is received from the battery.



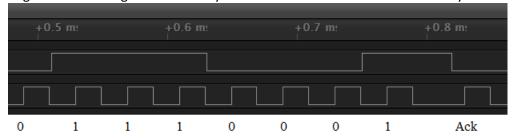
This sequence loads the register pointer in the battery so that future sequences can either read or write to the desired register. The 8 rising clock edges load 0x09 which points to the 16 bit register that contains the battery voltage in millivolts. The Ack/Nack from the battery should be low on the 9th rising clock edge.



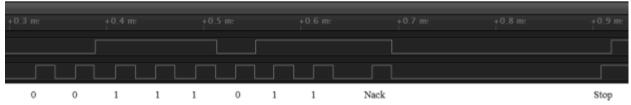
A "Repeated Start" is just like a regular start (falling clock while data is low) and it is used to keep control of the bus in a multi-master system while switching from a write to a read. This is the bus sequence that I couldn't get the Pi's I2C controller to implement so I had to make my own bus. After the repeated start, the battery is addressed with 7 rising clocks that capture 0001011 (just like before) but the 8th bit is a 1 this time to signify a read. The full 8 bit value is 0x17 when reading from the battery. The Ack/Nack from the battery should be low on the 9th rising clock edge.



The Pi floats the data line so the battery can send back the low 8 bits that are stored in register 0x09. The Pi captures the bits on the 8 rising clock edges and then sinks the data low for the 9th rising clock edge to acknowledge to the battery that it has received the data. The low byte value is 0x71.

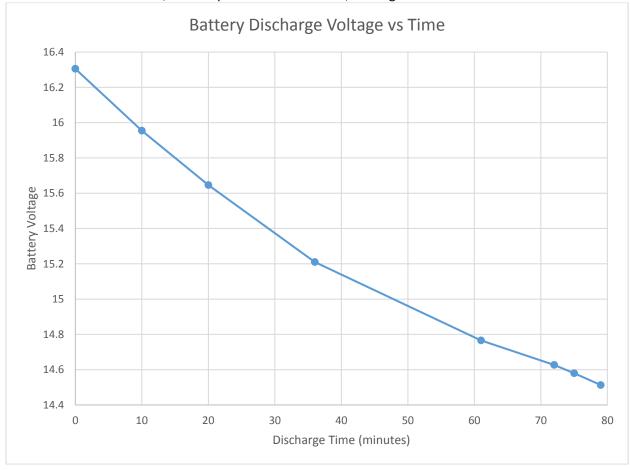


The high byte is received by the Pi in the same fashion but the Pi lets the data go high for the Ack/Nack bit to let the battery know that it is done reading the register bits. The high byte value is 0x3b. The 16 bit value of 0x3b71 is a battery voltage of 15217 mv. After a delay, the Pi gives a stop sequence (data low on rising clock) to finish the bus operation.

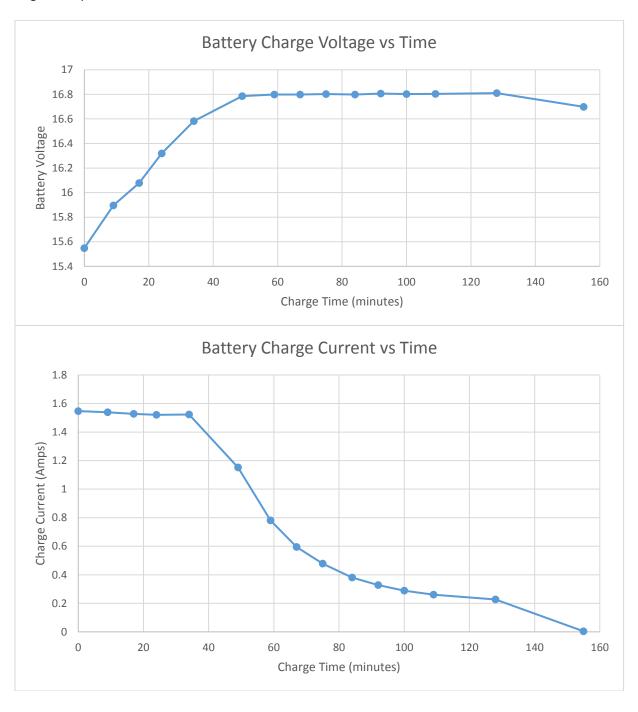


I have noticed that sometimes my code will read a bad value (usually all 1's) back from the battery. When this occurs, the logic analyzer shows that in the middle of one of the sequences, the Pi stops clocking and then comes back after a while and proceeds with the sequence but by then the battery has timed out and gives Nack's that set the error flag. My assumption is that Linux paused my code while it serviced some other process. I don't know how to make Linux act like an Arduino and ignore other tasks. I even added the command piHiPri(99) to the code which is supposed to make it the highest priority process but it made no difference. My final solution detects when a bad value or a Nack is read from the battery and repeats the entire register read sequence.

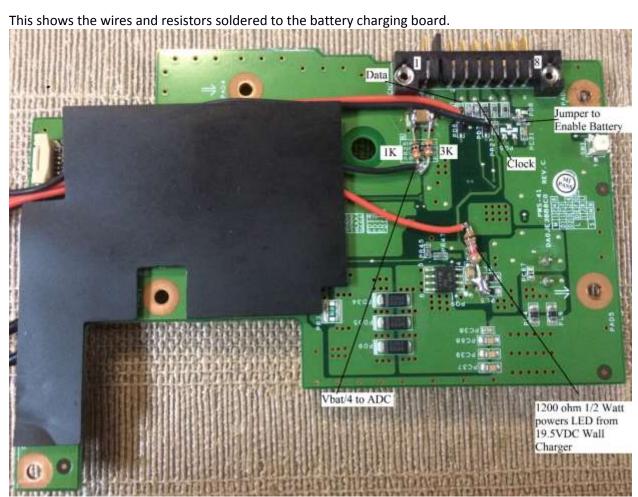
The discharge voltage curve of the battery is given below. The "Time to Empty" reported by the battery was 150 minutes at the start of the discharge cycle yet it only lasted 79 minutes (to 4% SoC). The current delivered to the laptop stayed relatively constant at 1.3 amps. At time 0, the battery temperature was 24.25°C and at 79 minutes, the temperature was 29.35°C, a 5 degree rise.



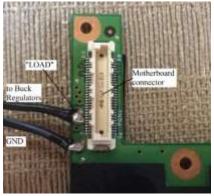
The charge voltage and current curves are given below. The different charging stages can be seen on the curves. The first ~40 minutes is a constant current of 1.5 Amps until the voltage reaches 16.8 volts. Then the voltage is held constant as the current declines, eventually reaching a trickle charge level. The "Time to Full" reported by the battery was 118 minutes at the start of the charge cycle yet it took 155 minutes. At time 0, the battery temperature was 29.35°C and at 155 minutes, the temperature was 24.25°C, a 5 degree drop.



The Sony Laptop uses a 14.8V, 4Ah Li-ion battery p/n PCGA-BP2NY that plugs into the Max 1873 based circuit board part number A1061942A shown below. I haven't been able to find a schematic other than the "Typical Application" in the Max 1873 data sheet. Tracing out the connections with an ohm meter shows the incoming 19.5VDC is switched by a PFET before going to the Max 1873. There is a PFET that connects the battery to a large "Load" pad that used to feed the power supplies on the motherboard. The battery measures 0 volts on all pins except "enable" on pin 6, which measures 5 volts. To enable the battery, I soldered a jumper that ties the enable pin to ground thru a 1K resistor (PR30). When enabled, the battery voltage on pins 1 & 2 measures from 14.4 to 16.8 volts. The SMBus data and clock signals on pins 3 and 4 are at 0 volts (no pull ups in the battery). I have added 3K pullups to 3.3 volts on the data and clock signals so the Pi can interface to the battery without a level translator. I believe pin 5 is a temperature sensor and pin 6 is now tied low. Pins 7 and 8 are the ground connection. When 19.5 volts is connected to the board from the external supply, there is 19.5 volts at the Load pad. When the external supply is disconnected, the Load pad measures a diode drop below the battery voltage. Disconnecting and connecting the 19.5 volt supply causes no voltage dropouts at the Load pad. I connected the Load pad to the inputs of the 3 buck regulators and the linear regulator.



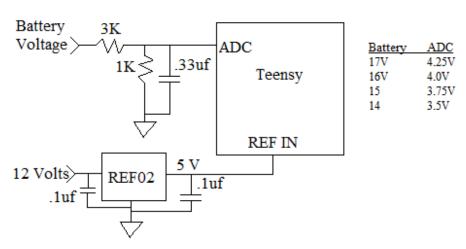
This shows the large Load pad at the motherboard connector that is wired to the 3 buck regulators and linear regulator.



The laptop battery voltage can be over 16.8 volts so a resistor divider is needed to drop the voltage to under 5 volts for the Teensy's ADC. The resistors draw about 4ma from the battery, even when the laptop is off. I may try increasing the resistor divider to 30K and 10K but for now I wanted small values for better noise immunity. The 5 volt (adjustable) buck regulator powering the Teensy would not make a good reference for the ADC so I added a REF02 5 volt reference on a small board next to the Teensy (see below).



The resistor divider and reference schematic are shown below.



Without the battery installed and the laptop running a YouTube video over Wi-Fi, the 19.5 volt source measures 1.06 amps with a multimeter. Running the "Stress" program increases the current to 1.16 amps. This equals a total laptop power of 20.7 to 22.6 watts. When running from the battery under the same two load conditions, the laptop voltage and current reported over the SMBus equate to a power range of 20.4 to 22.6 watts. Measuring the battery voltage with a multimeter gives nearly the same value as the voltage reported over the SMBus. All of this shows the voltage and current measurements made by the battery are very accurate.

The theoretical run time for a new battery is approximately (14.8 V x 4 Ah) / (14.8 V x 1.38 A) = 2.9 hours. My old battery has 235 charge cycles and gives a full charge capacity of 50.51 Wh instead of the 59.2 Wh original design capacity. It will run down to 4% in about 1 hour 9 minutes. I read an old review for this Sony laptop on the web that said the battery lasted 1.5 to 3 hours so this battery never gave a long run time. A new battery may get my run time up over 2 hours but not much more. If I don't plan on using the laptop for a while, I unplug the battery to stop the small discharge from the ADC resistors and power latch circuit. Lithium batteries scare me so just to be on the safe side, I never leave the laptop unattended while charging.

The Teensy measures the battery voltage after each keyboard scan using its ADC. It reads the battery voltage 8 times and takes the average to filter out the occasional noisy read. The ADC result can be read by the Pi over the I2C bus using the i2cdump –y 1 0x08 i command as shown below. In addition to the battery voltage, the Teensy code version, date, and author is given. The ASCII is converted to text on the right hand side of each line.

pi@ı	ras	bei	rry	oi:-	\$	i20	dur	np -	- y 1	L Θ)	(08	i					
		1	2	3	4	5	6	7	8	9	a	b	С	d		f	0123456789abcdef
ΘΘ:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
10:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
20:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
30:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
40:																	Battery = 15.5v
50:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
60:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
70:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
80:																	Battery = 15.5v
90:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
a0:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
b0:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
c0:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
d0:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA
e0:	42	61	74	74	65	72	79	20	3d	20	31	35	2e	35	76	20	Battery = 15.5v
f0:	56	33	2e	30	20	32	2f	31	34	2f	31	38	20	4d	46	41	V3.0 2/14/18 MFA

I tried having the Teensy turn off the laptop based on the battery voltage read by the ADC but I found it unreliable. I also didn't like turning off power to the Pi without giving a shutdown command. To solve these problems, I wrote a C program that constantly runs on the Pi and reads the battery state of charge over the SMBus every minute. If it's under 10%, the "Disk" LED is turned on as my first warning. If it falls to 7%, the display is momentarily turned off which usually gets my attention. If it falls to 5%, the command "sudo shutdown —h now" is given which automatically commands the Teensy to turn off the power latch per the power_off.service unit file described earlier. The "monitor_battery.c" program is available at my Github repo. It is run at startup using a systemd unit file per this Dexter Industries tutorial. The following bat_monitor.service unit file is located at /lib/systemd/system. Make sure you load and enable the service with terminal commands, systemctl daemon-reload and systemctl enable bat_monitor --now

[Unit]
Description=Battery Monitor Service
After=multi-user.target

[Service]
Type=idle
ExecStart=/home/pi/monitor_battery

[Install] WantedBy=multi-user.target

About the Author – I was an engineer at Boeing for 33 years, designing circuit boards, multi-chip modules, and ASICs for various systems. I built lots of Heathkits and Ham radios when I was a teenager but didn't do much electronics at home during my Boeing career. Now that I'm retired, I'm getting back into it and discovering how great the "Maker" community is. "Thank you" to all the people that helped me with your forum posts, website articles, and YouTube videos. I started writing this document to keep track of the numerous sources of information and decided I should share it in case others want to do a similar project. I'm sure I've made mistakes so feel free to send me an email (thedalles77@gmail.com) if you have any comments, improvements or questions. I'll update this document along with the Teensy and Pi code as I learn more.

Cheers

Appendix A – Parts List

Purchased Items:

Raspberry Pi 3 model B from Amazon for \$39

Five FPC connectors from Ali express for \$4.51 + shipping

Three boards from Oshpark for \$18 (includes shipping)

Teensy ++2.0 from PJRC for \$24 + shipping

M.NT68676 board from ebay for \$25 + shipping

Short HDMI cable from Amazon for \$8.49

Four LM2596 regulator boards from <u>Amazon</u> for \$11

Latch components and Reference from <u>Digikey</u> for \$17 + shipping

USB to SATA Cable from Amazon for \$11

Two U.FL connectors from Amazon for \$5

RTC & battery from Adafruit for \$8.50 + shipping

Two 40 pin ribbon cables with IDC Connectors from Amazon for \$9.20

5 volt fan from Amazon for \$9

Two 12 volt fans from Amazon for \$10

Total is over \$225 with shipping

Parts from the junk drawer:

Donor Laptop with AC Power Supply and Battery

Solid State Drive

USB Cable – Teensy to Pi

Various gauge wire, shrink tubing, solder, mounting screws, wood dowels, and electrical tape

Header pins for Teensy and RTC

Jumpers to attach to header pins

Perf board for Latch and Ref circuit

JB Weld Epoxy

Appendix B – Alternative Power Connection

I tested another method of powering the Pi and Teensy that involved feeding the video card directly from a 12 VDC 3 amp - AC plugin adapter and then powering the Pi and USB from the 54329E 5 volt buck regulator on the video card. This would be a good approach if building a simple, low cost RetroPi gaming laptop.

The 5 volts feeding into the Pi measured 650ma to 950ma with USB loads of an SSD and a Teensy. I surfed the web, played YouTube video's and played an mp4 from the SSD with no issues

Moving the amp meter to the 12 volts feeding into the video converter card (which is powering the entire system) shows 1.2 to 1.55 amps at 12 volts, $(1.55 \times 12 = 18.6 \text{ watts})$.

The 12 volts feeding the backlight measures 450ma. To find the total load on the 5 volt buck regulator, the backlight power ($12 \times .45 = 5.4$ watts) is subtracted from the 18.6 watt input power.

18.6W total input power - 5.4W to the backlight = 13.2W into the 5 volt buck regulator

If you assume the buck regulator is 90% efficient, then 13.2W in but only $(13.2 \times .9) = 11.9W$ out. The load current on the buck regulator is approximately 11.9W/5V=2.38 amps. The regulator is rated for 3 amps so the buck regulator on the video card seems to have enough power for the Pi, SSD, and Teensy but not much extra. After making these measurements, I learned about the "stress" test program which adds about 300ma to the 5 volt current. This puts the total 5 volt current up near 2.7 amps.

I chose to not use this method of powering the system because I wanted enough current for powering projects on the GPIO bus, plugging in one or two Passport external USB drives and still have plenty to spare. Another factor is that starting with 12 volts instead of 19.5 volts would not work with the 14 volt Sony battery.

Document Revision History

November 5, 2017 – Original release

November 10, 2017 – Added YouTube link and i2cdump command description.

December 25, 2017 – Added battery operation, SMBus bit-bang interface, and Teensy ADC reference.

January 1, 2018 – Added Hackaday links and general cleanup.

January 3, 2018 – Added Soarer's firmware link, Github file names, and ADC filtering.

January 8, 2018 – Added battery power measurements and general cleanup.

February 23, 2018 – Added Pi program to monitor the battery SoC. Added two systemd unit files.

March 24, 2018 – Corrected the touchpad board clock and data pad numbers.

June 24, 2018 – Clarified systemd procedure, fixed broken links and general cleanup.

July 7, 2018 – Removed paragraph about Teensy code oddities. Delays at startup no longer needed.