\$40	(read_n / write)	ID \$08 ID \$D4	MSX2+ FS-A1 Series MSX++ 1chipMSX, Zemmix Neo, etc.	Z80B 5.37MHz OCM-PLD v2.4 or later			SMART COMMANDS TABLE	
		BIT 0	WISKT TEMPORISK, Zeminik Neo, etc.	OCIVI-1 ED V2.4 OF later	\$00	(000)	Null Command (reserved)	
\$41	(read_n/write)	BIT 1 BIT 2			\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON	
		BIT 3	Smart Command ID	CPL or \$FF (Null)	\$03	(003)	Set Standard Speed 3.58MHz	
*		BIT 4 BIT 5			\$04 \$05	(004)	Set Custom Speed 4.10MHz Set Custom Speed 4.48MHz	
		BIT 6 BIT 7			\$06 \$07	(006)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz	
		BIT 0	CPU Clock	Virtual DIP-SW1	\$08	(008)	Set Custom Speed 6.10MHz	
\$42	(read / write_n)	BIT 1	Video Output (MSB)	Virtual DIP-SW2	\$09	(009)	Set Custom Speed 6.96MHz	
		BIT 2 BIT 3	Video Output (LSB) Cartridge Slot-1	Virtual DIP-SW3 Virtual DIP-SW4	\$0A \$0B	(010) (011)	Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default) Set Turbo MegaSD OFF	
Ç42		BIT 4 BIT 5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Virtual DIP-SW5 Virtual DIP-SW6	\$0C \$0D	(012) (013)	Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2	
		BIT 6	Current Mapper Size	Virtual DIP-SW7	\$0E	(014)	Set Internal SCC-I Slot-1 + External Slot-2	
		BIT 7	Current MegaSD Mode	Virtual DIP-SW8 CPU Clock	\$0F \$10	(015)	Set External Slot-1 + Internal SCC-I Slot-2 Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2	
	(read/write n)	BIT 1		Video Output	\$11	(017)	Set External Slot-1 + Internal ASCII-8K Slot-2	
642		BIT 2 BIT 3	to describe the Toronto	Audio Mixer & SCRLK Cartridge Slot-1	\$12 \$13	(018) (019)	Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2 Set External Slot-1 + Internal ASCII-16K Slot-2	
\$43	(read / write_n)	BIT 4	Lock Mask of the Toggles	Cartridge Slot-2	\$14	(020)	Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2 Set Japanese Keyboard Layout	
		BIT 5 BIT 6		Hard Reset Key Internal Mapper	\$15 \$16	(021) (022)	Set Non-Japanese Keyboard Layout	
		BIT 7		Internal MegaSD Led 1 Status	\$17 \$18	(023)	Set Display Mode 15KHz Composite/S-Video Set Display Mode 15KHz RGB w/ Audio Out	
	(read / write_n)	BIT 1		Led 2 Status	\$19	(025)	Set Display Mode 31Khz VGA for LED TV or LED Display	also HDMI AV on SM-X
		BIT 2 BIT 3	Lights I/O	Led 3 Status Led 4 Status	\$1A \$1B	(026) (027)	Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output) Set VDP Speed Normal Mode (default)	also HDMI AV on SM-X
\$44		BIT 4	(SM-X and SX-2 only use Led 8 but	Led 5 Status	\$1C	(028)	Set VDP Speed Fast Mode (V9958 only)	
		BIT 5 BIT 6	this register works the same way)	Led 6 Status Led 7 Status	\$1D \$1E	(029)	Reserve MegaSD OFF (warm reset to go) Reserve MegaSD ON (warm reset to go)	
		BIT 7		Led 8 Status	\$1F \$20	(031)	Set MegaSD Blink OFF Set MegaSD Blink ON (default)	
\$45	(read / write_n)	BIT 0 BIT 1	PSG Volume Level	BIT 0 (LSB) BIT 1	\$20	(032)	Set Lights Mode OFF w/ Auto LEDs Control (default)	
	(read only)	BIT 2 BIT 3	PSG Mute	BIT 2 (MSB) Status	\$22 \$23	(034)	Set Lights Mode ON + Red Led OFF Set Lights Mode ON + Red Led ON	
		BIT 4		BIT 0 (LSB)	\$24	(036)	Internal Audio Preset #1 "Mute Sound"	
	(read / write_n)	BIT 5 BIT 6	Master Volume Level	BIT 1 BIT 2 (MSB)	\$25 \$26	(037)	Internal Audio Preset #2 "Middle Sound" Internal Audio Preset #3 "High Sound" (default)	
	(read only)	BIT 7	Master Mute	Status	\$27	(039)	Set CMT OFF (default) (disabled w/ MSXtR BIOS)	n/a on SM-X / SX-2
	(read / write_n)	BIT 0 BIT 1	OPLL Volume Level	BIT 0 (LSB) BIT 1	\$28 \$29	(040) (041)	Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS) Lock Turbo Toggles	n/a on SM-X / SX-2
\$46		BIT 2 BIT 3	OPLL Mute	BIT 2 (MSB) Status	\$2A \$2B	(042)	Unlock Turbo Toggles Lock Display Toggles	
	(read only)	BIT 4		BIT 0 (LSB)	\$2C	(044)	Unlock Display Toggles	
	(read / write_n)	BIT 5 BIT 6	SCC-I Volume Level	BIT 1 BIT 2 (MSB)	\$2D \$2E	(045) (046)	Lock Audio Mixer & SCRLK Toggles Unlock Audio Mixer & SCRLK Toggles	
	(read only)	BIT 7	SCC-I Mute	Status	\$2F	(047)	Lock Slot-1 Toggles	
		BIT 0 BIT 1	CPU Custom Speed Level	BIT 0 (LSB) BIT 1	\$30 \$31	(048) (049)	Unlock Slot-1 Toggles Lock Slot-2 Toggles	
\$47		BIT 2		BIT 2 (MSB)	\$32	(050)	Unlock Slot-2 Toggles	
	(read only)	BIT 3 BIT 4	Turbo MegaSD (tMSD) Turbo Pana Redirection (tPR)	Status Status	\$33 \$34	(051) (052)	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles	
		BIT 5	VDP Speed Mode	0=Normal, 1=Fast	\$35	(053)	Lock Hard Reset Key Unlock Hard Reset Key	
		BIT 6 BIT 7	Mapper Size Req MegaSD Mode Req	0=2048kB, 1=4096kB Status	\$36 \$37	(054)	Lock Mapper Toggle	
\$48	(read only)	BIT 0	Turbo Pana	Status	\$38	(056) (057)	Unlock Mapper Toggle Lock MegaSD Toggle	
		BIT 1 BIT 2	Current Keyboard Layout SCRLK Toggle	0=JP, 1=Non-JP Status	\$39 \$3A	(058)	Unlock MegaSD Toggle	
		BIT 3 BIT 4	Lights Mode Red Mode (Led 0)	0=Auto, 1=ON Status	\$3B \$3C	(059)	Lock All Toggles Unlock All Toggles (default)	
		BIT 5	Last Reset Flag	0=Cold, 1=Warm	\$3D	(061)	Set Pseudo-Stereo OFF (default)	
		BIT 6 BIT 7	Reset Required Flag MegaSD Blink	Status Status	\$3E \$3F	(062)	Set Pseudo-Stereo ON (needs an external sound cartridge) Sync External Bus Clock to CPU Speed (default)	
		BIT 0	Pseudo Stereo	Status	\$40	(064)	Set External Bus Clock 3.58MHz	
		BIT 1 BIT 2	External Clock Mode Machine Type ID	0=Sync to CPU, 1=3.58Mhz BIT 0 (LSB)	\$41 \$42	(065) (066)	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)	
\$49	(read only)	BIT 3 BIT 4	(0=1chipMSX, 1=Zemmix Neo/SX-1 and related,	BIT 1 BIT 2	\$43 \$44	(067) (068)	Set Right Inverse Audio ON Internal Audio Preset #4 "Emphasis PSG Sound"	
		BIT 5	2=SM-X/MC2P, 3=SX-2, 4=SM-X Mini/SMX-HB, 5=DE0CV, 6-14=Free, 15=Unknown)	BIT 3 (MSB)	\$45	(069)	Internal Audio Preset #5 "Emphasis SCC-I Sound"	
		BIT 6 BIT 7	NTSC/PAL Type Forced Video Mode	0=Forced, 1=Auto 0=60Hz (NTSC), 1=50Hz (PAL)	\$46 \$47	(070) (071)	Internal Audio Preset #6 "Emphasis OPLL Sound" Vertical Offset 16 (useful for Ark-A-Noah)	
\$4A	(read only)	BIT 0	Right Inverse Audio	Status	\$48	(072)	Vertical Offset 17	
		BIT 1 BIT 2	Pixel Ratio 1:1 for LED Display	BIT 0 (LSB) BIT 1	\$49 \$4A	(073) (074)	Vertical Offset 18 Vertical Offset 19 (default)	
		BIT 3	Contractor WIV No. do /DDF No. d	BIT 2 (MSB)	\$4B \$4C	(075)	Vertical Offset 20 Vertical Offset 21	
		BIT 5	Centering YJK Modes/R25 Mask Assignment of Legacy Output	Status 0=To VGA, 1=To VGA+	\$4D	(076) (077)	Vertical Offset 22	
		BIT 6 BIT 7	Internal Slot-1 Linear Internal Slot-2 Linear	Status Status	\$4E \$4F	(078) (079)	Vertical Offset 23 Vertical Offset 24 (useful for Space Manbow)	
		BIT 0	VGA Scanlines Level (ID*25%)	BIT 0 (LSB) only for SM-X / SX-2	\$50 \$53	(080 083)	Set VGA Scanlines 0% 25% 50% 75% (default is 0%)	only for SM-X / SX-2
	IN A,(\$44) : if A=\$00	BIT 1 BIT 2	Internal PSG2	BIT 1 (MSB) only for SM-X / SX-2 Status only for SM-X / SX-2	\$54 \$55	(084) (085)	Internal PSG2 OFF (default) Internal PSG2 ON (this second PSG acts as an external PSG)	only for SM-X / SX-2 only for SM-X / SX-2
\$4B	(read only)	BIT 3	SDRAM Size	BIT 0 (LSB) BIT 1 (MSB)	\$56	(086)	Set Extra-Mapper 4096 kB OFF (default)	
	(1000 0111,)	BIT 4 BIT 5	(0=8MB, 1=16MB, 2=32MB, 3=reserved) OCM-BIOS Reloading Req	Status	\$57 	(087)	Set Extra-Mapper 4096 kB ON (only available if SDRAM > 8MB)	
	[dynamic port 4B]	BIT 6 BIT 7	Extra-Mapper 4096kB Req Slot-0 Mode Req	Status 0=Primary, 1=Expanded	 \$7F	 (127)	Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)	
		BIT 0	Siot o Wode Req	BIT 0 (LSB)	\$80	(128)	Null Command (useful for programming)	
	IN A,(\$44) : if A>\$00	BIT 1 BIT 2		BIT 1 BIT 2	\$81 \$82	(129) (130)	Assign Legacy Output to VGA Assign Legacy Output to VGA+ (default)	
\$4B	(read only)	BIT 3	Free	BIT 3	\$83	(131)	Set Internal Slot-1 Linear OFF (default)	
	(. cau only)	BIT 4 BIT 5		BIT 4 BIT 5	\$84 \$85	(132) (133)	Set Internal Slot-1 Linear ON (requires SCC-I preset) Set Internal Slot-2 Linear OFF (default)	
	[dynamic port 4B]	BIT 6 BIT 7		BIT 6 BIT 7 (MSB)	\$86 \$87	(134)	Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset) Set Internal OPL3 OFF ON (default)	only for SM-X / SX-2
		BIT 0	CPU Clock	Hard DIP-SW1	\$88	(136)	Set Internal OPL3 ON	only for SM-X / SX-2
\$4C	(read only)	BIT 1 BIT 2	Video Output (MSB) Video Output (LSB)	Hard DIP-SW2 Hard DIP-SW3	\$89 \$8F 	(137 143) 	Reserved (Ducasp)	only for SM-X / SX-2
		BIT 3	Cartridge Slot-1	Hard DIP-SW4	400 4		Set Marter Volume 0 7 (defectly level in 7)	
		BIT 4 BIT 5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Hard DIP-SW5 Hard DIP-SW6	\$B0 \$B7 \$B8 \$BF	(176 183) (184 191)	Set Master Volume 0 7 (default level is 7) Set PSG Volume 0 7 (default level is 4)	
		BIT 6 BIT 7	Internal Mapper Internal MegaSD	Hard DIP-SW7 Hard DIP-SW8	\$C0 \$C7 \$C8 \$CF	(192 199) (200 207)	Set SCC-I Volume 0 7 (default level is 4) Set OPLL Volume 0 7 (default level is 4)	
		BIT 0	cina megaso	BIT 0 (LSB)	\$D0	(208)	Force NTSC Mode	
\$4D	(read / write_n)	BIT 1 BIT 2	64kB VRAM Slot ID (Page 0)	BIT 1 BIT 2	\$D1 \$D2	(209) (210)	Standard NTSC/PAL Mode (bound by Control Register 9) (default) Force PAL Mode	
		BIT 3		BIT 3 (MSB)	\$D3	(211)	Restore Default Keyboard Layout	
		BIT 4 BIT 5	GALP VIDAMA CLALID (Days 4)	BIT 0 (LSB) BIT 1	\$D4 \$D5	(212) (213)	Null Command (reserved) Restore Default Turbo Mode	
		BIT 6	64kB VRAM Slot ID (Page 1)	BIT 2	\$D6	(214)	Set Centering YJK Modes/R25 Mask OFF (default) Set Centering YJK Modes/R25 Mask ON	
\$4E	(read only)	BIT 7		BIT 3 (MSB) BIT 0 (LSB)	\$D7 \$F8	(215)	Reserve OCM-BIOS Reloading (cold reset or warm reset to go)	
		BIT 1 BIT 2		BIT 1 BIT 2	\$F9 \$FA	(249)	Reserve Slot-O Primary Mode (warm reset to go) (internal OPLL disabled) Reserve System Logo ON (warm reset only)	
		BIT 3	OCM-PLD main version X.Y(.z)	BIT 3	\$FB	(251)	Cold Reset	
		BIT 4 BIT 5	(range 0.0.z - 25.5.z)	BIT 4 BIT 5	\$FC \$FD	(252) (253)	Warm Reset w/ Mapper 2048kB (RAM size 6144kB if Extra-Mapper is ON) Warm Reset	
		BIT 6		BIT 6	\$FE	(254)	Warm Reset w/ Mapper 4096kB (RAM size 8192kB if Extra-Mapper is ON)	
		BIT 7 BIT 0		BIT 7 (MSB) BIT 0 (LSB)	\$FF	(255)	Restore All Default + Reserve Default Mapper & MegaSD	
\$4F	(read only)	BIT 1	1 2 I/O Revision ID (0 - 31) 3 4	BIT 1	More	info on Switched I/O	ports at MSX Assembly Page! < http://map.grauw.nl/resources/msx_io_ports.	php#switch_io >
		BIT 3		BIT 2 BIT 3				
				BIT 4 (MSB) BIT 0 (LSB)		Positive	0 = OFF 1 = ON (read / write)	
		BIT 6	(range x.y.0 - x.y.3)	BIT 1 (MSB)	R/W Logic	Negative	0 = ON (read n/write n)	
		BIT 7	Default Keyboard Layout	Status		CPU Clo	1 = OFF	
			Reserved to IPL-ROM			Video Out	tput [(SHIFT+)PRTSCR] or [DIP-SW2/3]	SCRLK key could handle
\$4C	(write_n only)	BIT 0 - 7	VDP ID Selector	0-1=ID0 (V9938), 2-255=ID2 (V9958)		Audio Mixer 8 Cartridge S		CMT or OPL3 depending on the type of machine
\$4E	(write_n only)	BIT 0-6		0-1101 - 1102 - 1-1100	Toggles	Cartridge S	Slot-2 [SHIFT+SCRLK] or [DIP-SW5/6]	
		BIT 7 BIT 0 - 6	JIS2 Enabler -	0=JIS1+JIS2, 1=JIS1 only -		System Re Internal Ma		
\$4F	(write_n only)	BIT 7	F4 Device Mode	0=Normal, 1=Inverted		Internal Me		