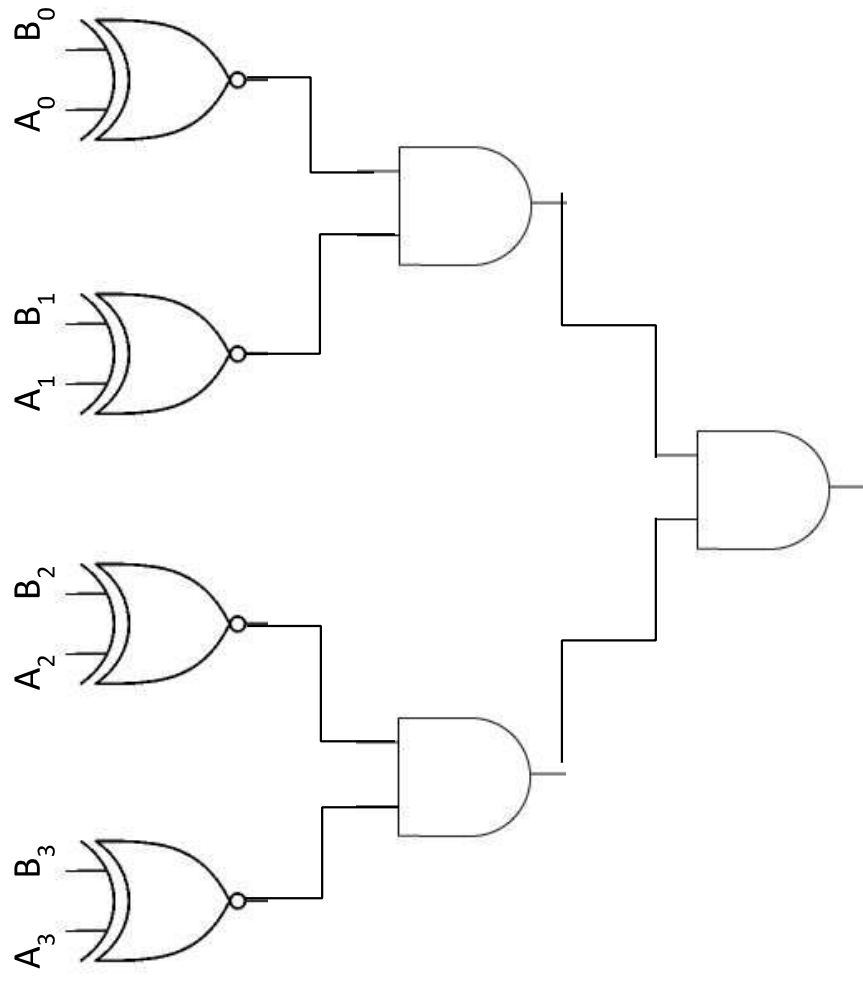
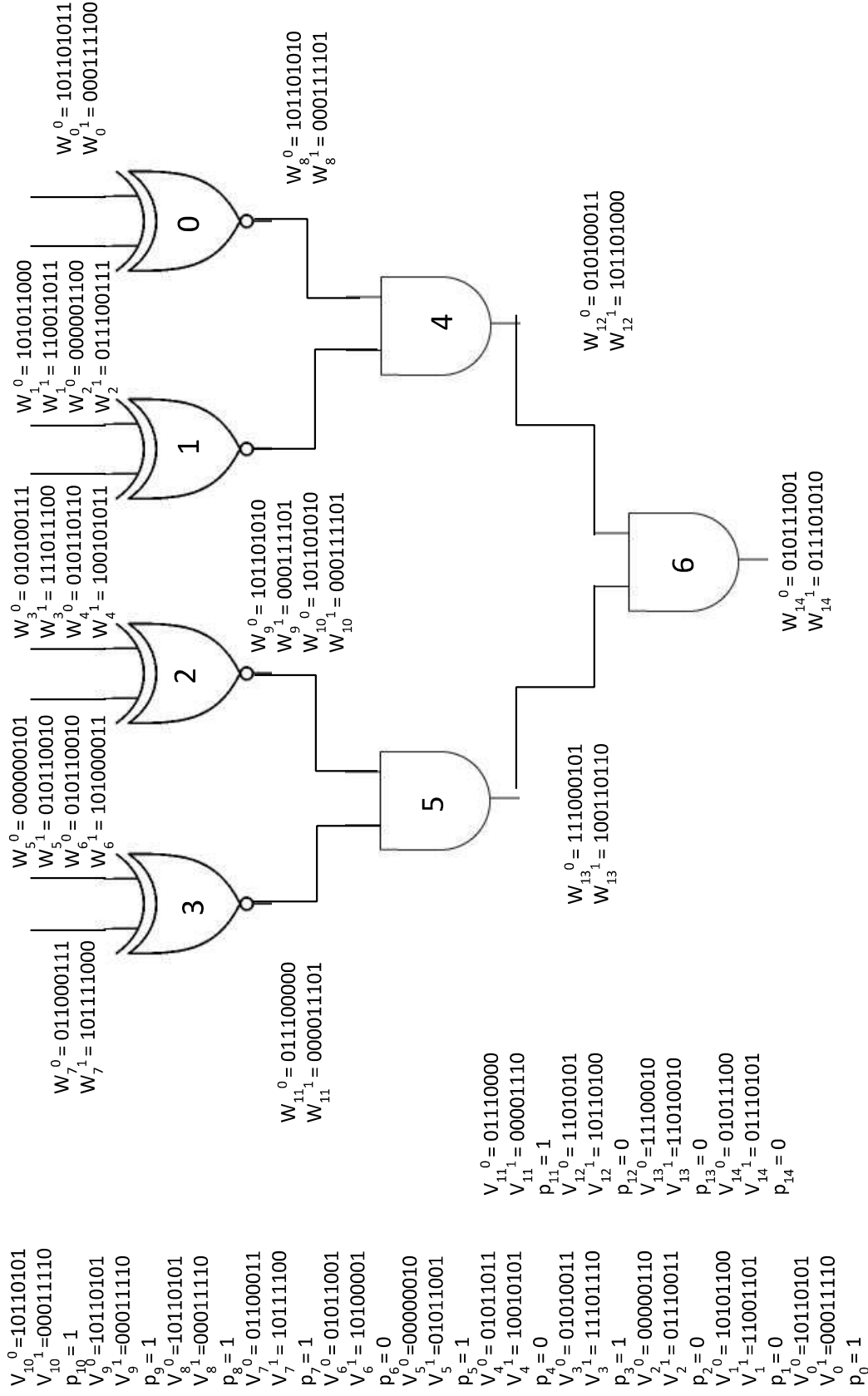


Original Circuit(let a denote alice's input and b bob's)



• Garbled Circuit(Truth tables on next page)



0

Encrypt			Shuffle		
x	y	out	x	y	out
W_1^0	W_0^0	W_8^1	W_1^0	W_0^0	W_8^1
W_1^0	W_0^1	W_8^0	W_1^0	W_0^1	W_8^0
W_1^1	W_0^0	W_8^0	W_1^1	W_0^0	W_8^0
W_1^1	W_0^1	W_8^1	W_1^1	W_0^1	W_8^1
W_1^0	W_0^0	W_8^0	W_1^0	W_0^0	W_8^0
W_1^0	W_0^1	W_8^1	W_1^0	W_0^1	W_8^1

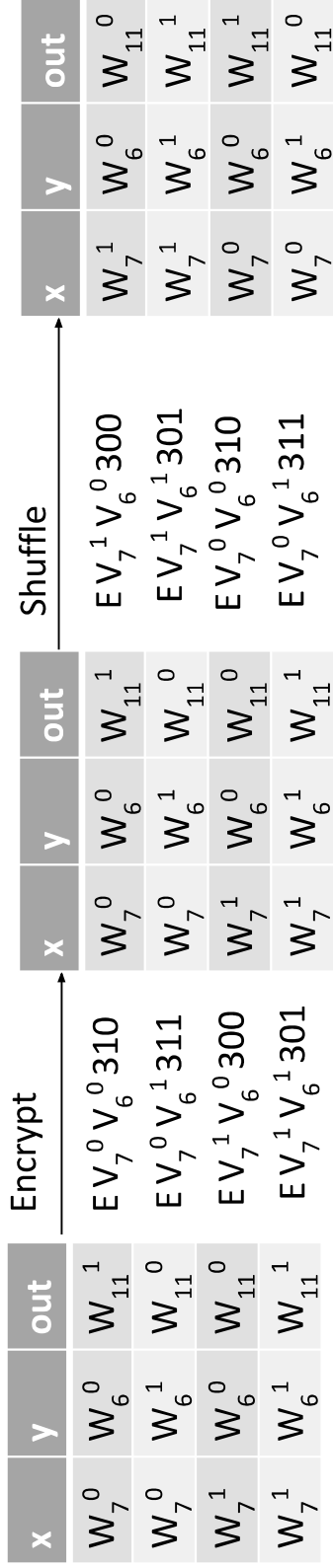
1

Encrypt			Shuffle		
x	y	out	x	y	out
W_3^0	W_2^0	W_9^1	W_3^1	W_2^0	W_9^0
W_3^0	W_2^1	W_9^0	W_3^1	W_2^1	W_9^1
W_3^1	W_2^0	W_9^0	W_3^0	W_2^0	W_9^1
W_3^1	W_2^1	W_9^1	W_3^0	W_2^1	W_9^0
W_3^0	W_2^0	W_9^1	W_3^1	W_2^0	W_9^1
W_3^0	W_2^1	W_9^0	W_3^1	W_2^1	W_9^0

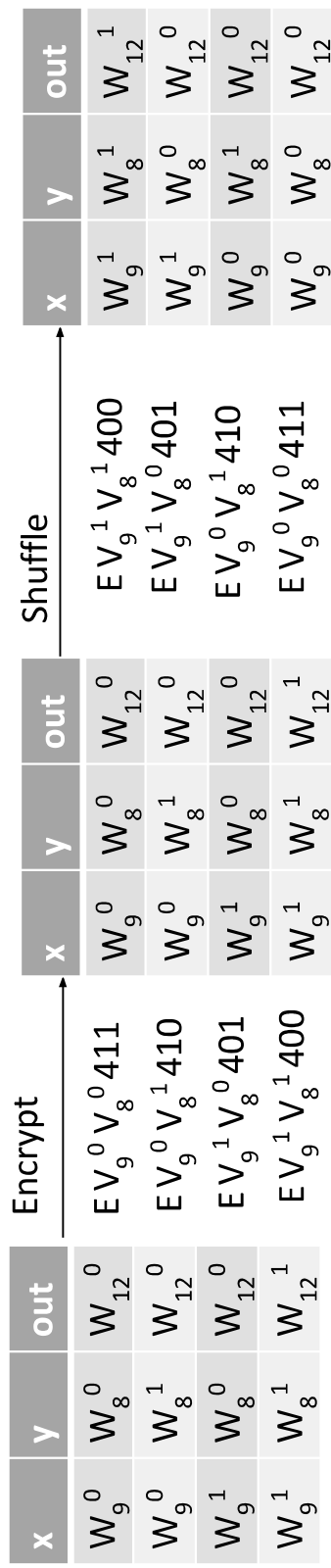
2

Encrypt			Shuffle		
x	y	out	x	y	out
W_5^0	W_4^0	W_{10}^1	W_5^1	W_4^0	W_{10}^0
W_5^0	W_4^1	W_{10}^0	W_5^1	W_4^1	W_{10}^1
W_5^1	W_4^0	W_{10}^0	W_5^0	W_4^0	W_{10}^1
W_5^1	W_4^1	W_{10}^1	W_5^0	W_4^1	W_{10}^0
W_5^0	W_4^0	W_{10}^1	W_5^1	W_4^0	W_{10}^1
W_5^0	W_4^1	W_{10}^0	W_5^1	W_4^1	W_{10}^0

3



4



5

