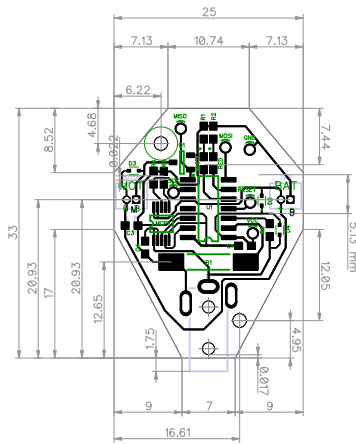


2

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REV	DESCRIPTION	DATE
2.1	Initial two-sided prototype	10/26/2014
2.2	Full Build Canidate	11/14/2014
2.5	Shifted C1 to allow more space for LEDs and button. Minimum trace width increased to 0.008. Reverted MCP1 pads to standard size. Removed copper pour until production.	11/17/2014
2.5.2	Change power connector J1 pads to milled slots. Extended programming traces to bottom of board.	11/19/2014
2.5.3	Renumber pins on connectors and button. Add polarity ledgend info the bottom silk. Correct pin locations on Q1. Reduce size of silk marks on test points.	11/19/2014
2.6.0	Added keepouts around connectors. Reduced pad size for diodes. Moved components to accomodate keepouts. Increased trace width around decoupling cap. Added orentation dots for LEDs on silk. Added new trace for battery charger chip testing.	12/18/2014
2.6.1	Moved R6 to shorten the traces around there. Made all diode traces leave symetrically. Cleared out the area around the jack. Allowed more mask for traces leading to test points.	12/21/2014

B

B

A

A

VIBE V2 Board				
	SIZE	FSCM NO.	DWG NO.	REV
	SCALE			Sheet 1

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