Lecture 12: CMOS Amplifiers: The Differential Pair - Second Part

Javier Ardila

Reference: Razavi (Fundamentals) - Chapter 10

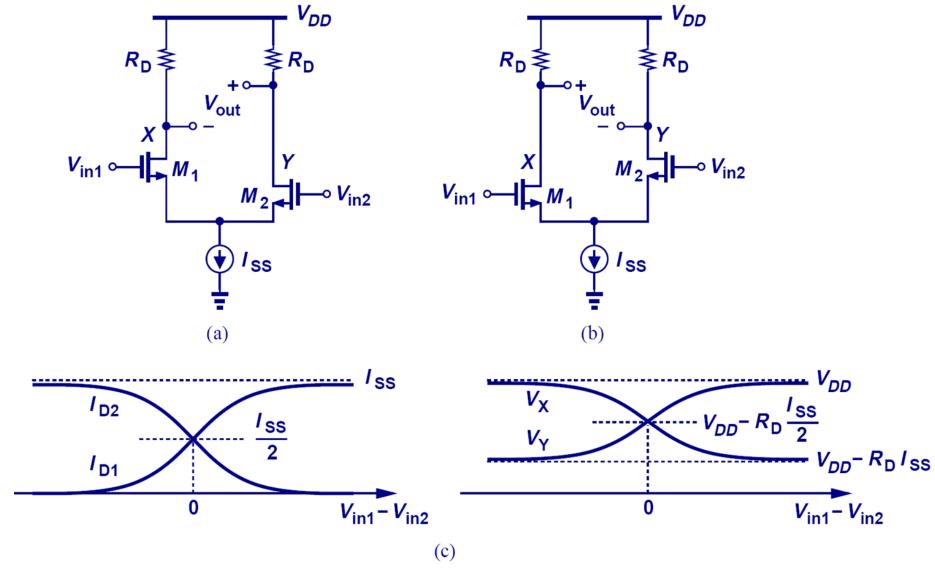
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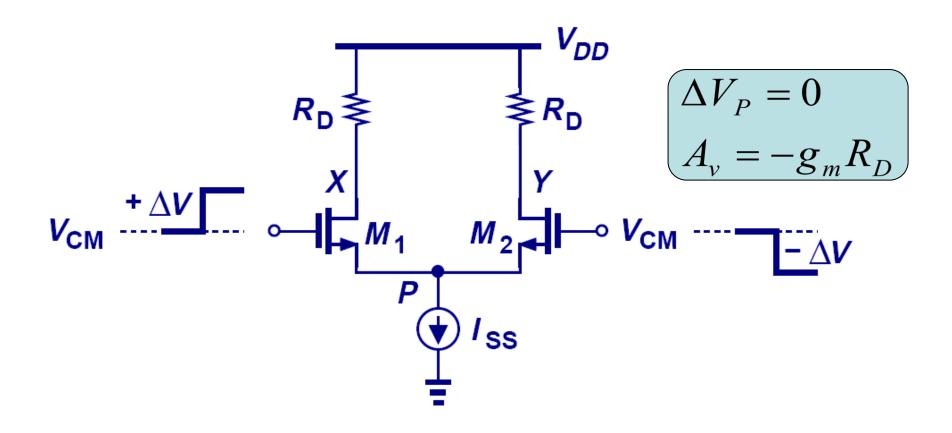




Differential Response

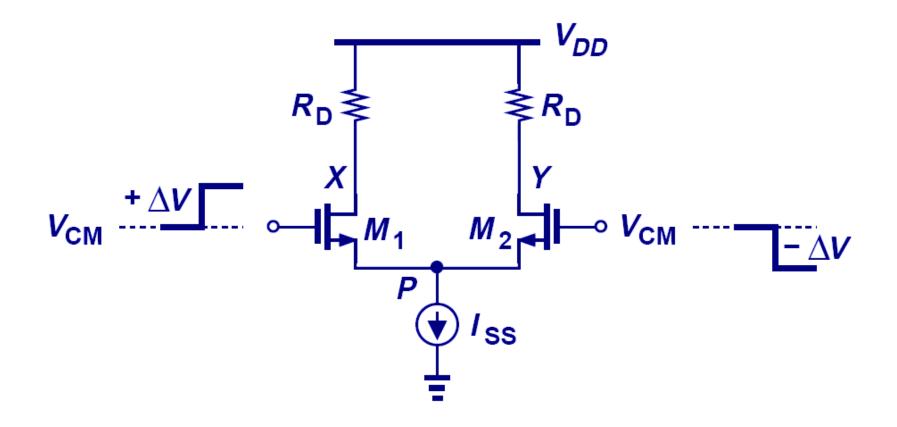


Small-Signal Response

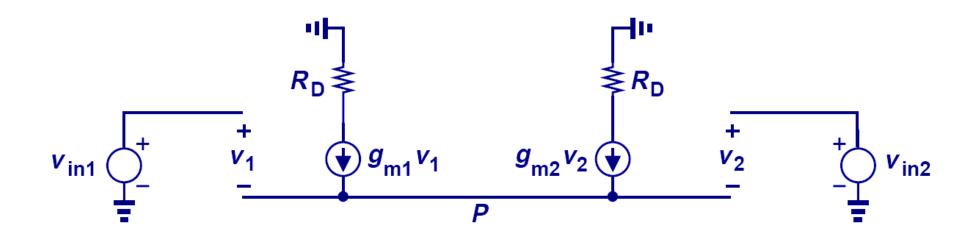


Similar to its bipolar counterpart, the MOS differential pair exhibits the same virtual ground node and small signal gain.

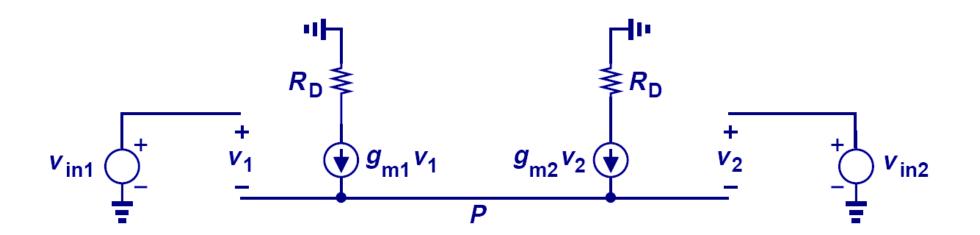
Power and Gain Tradeoff

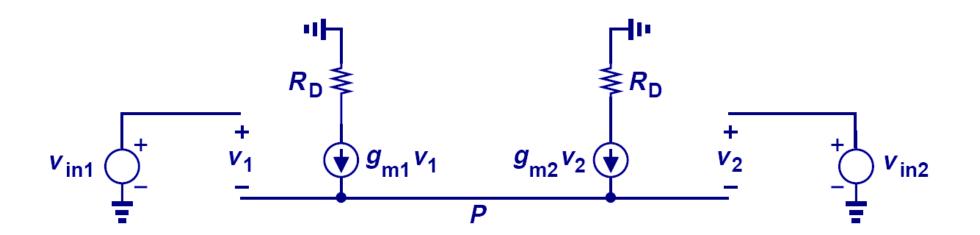


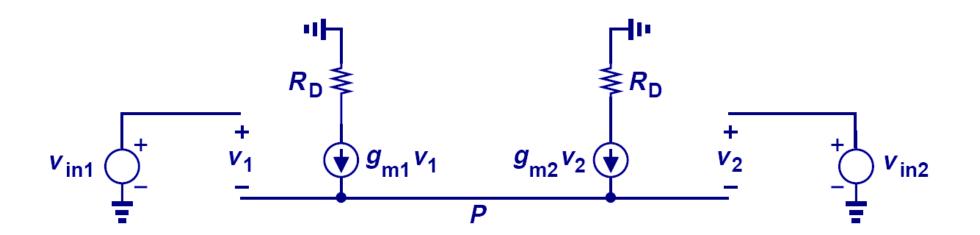
➤ In order to obtain the source gain as a CS stage, a MOS differential pair must dissipate twice the amount of current. This power and gain tradeoff is also echoed in its bipolar counterpart.

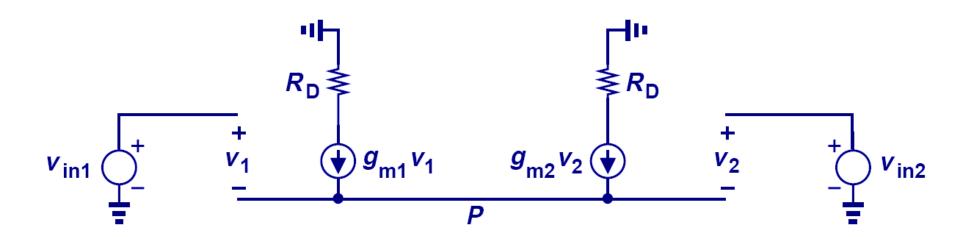


When the input differential signal is small compared to 4I_{ss}/μ_nC_{ox}(W/L), the output differential current is linearly proportional to it, and small-signal model can be applied.

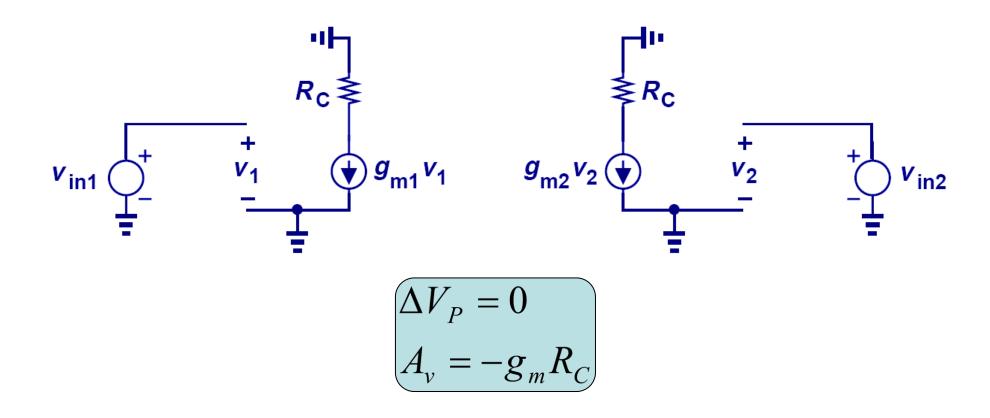






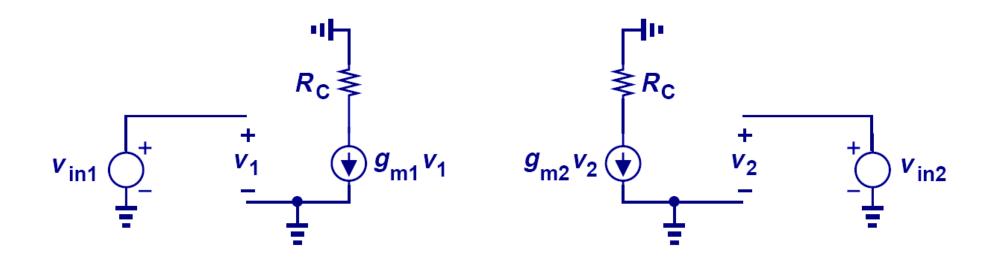


Virtual Ground and Half Circuit

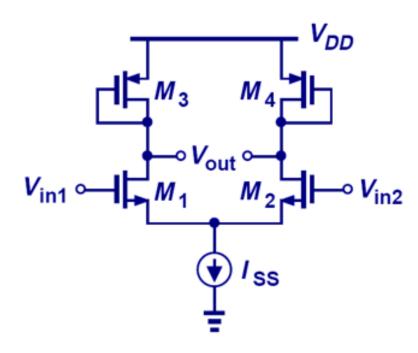


Node P will not move for small input signals and the concept of half circuit can be used to calculate the gain.

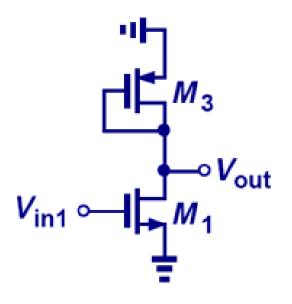
Virtual Ground and Half Circuit



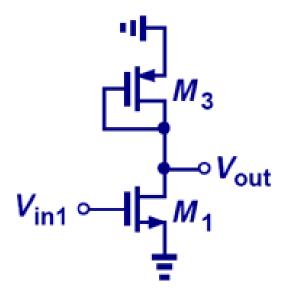
MOS Differential Pair Half Circuit Example I



MOS Differential Pair Half Circuit Example I



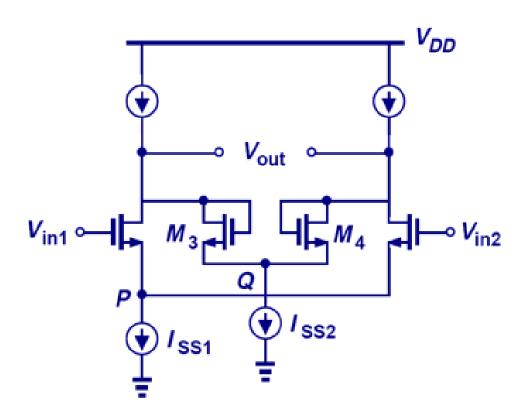
MOS Differential Pair Half Circuit Example I



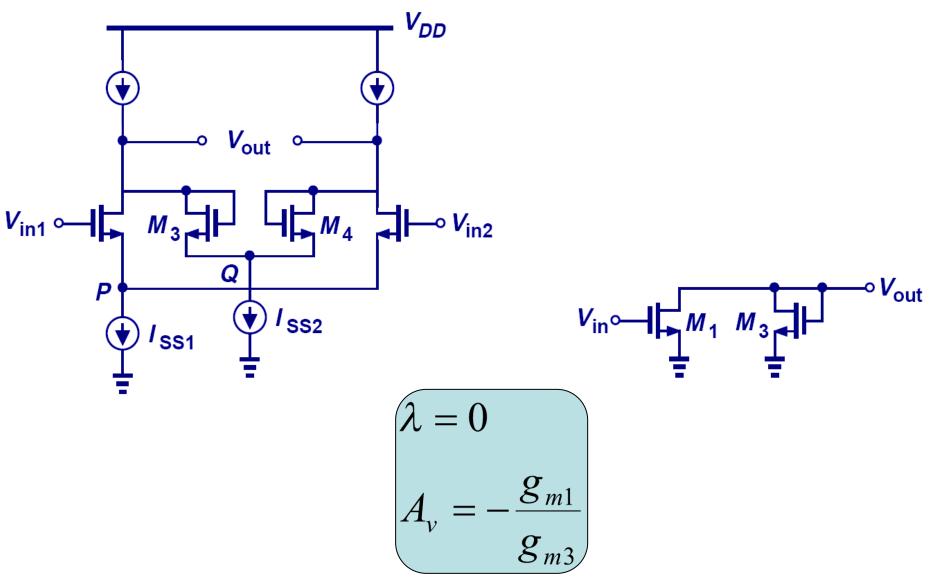
$$\lambda \neq 0$$

$$A_{v} = -g_{m1} \left(\frac{1}{g_{m3}} \parallel r_{O3} \parallel r_{O1} \right)$$

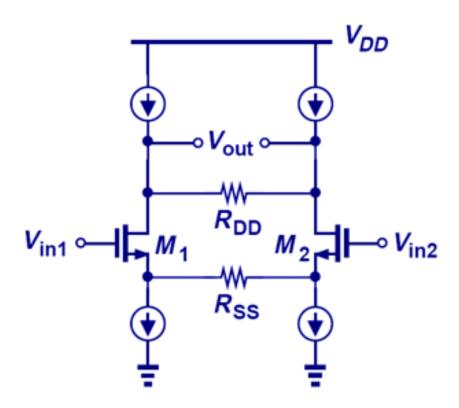
MOS Differential Pair Half Circuit Example II



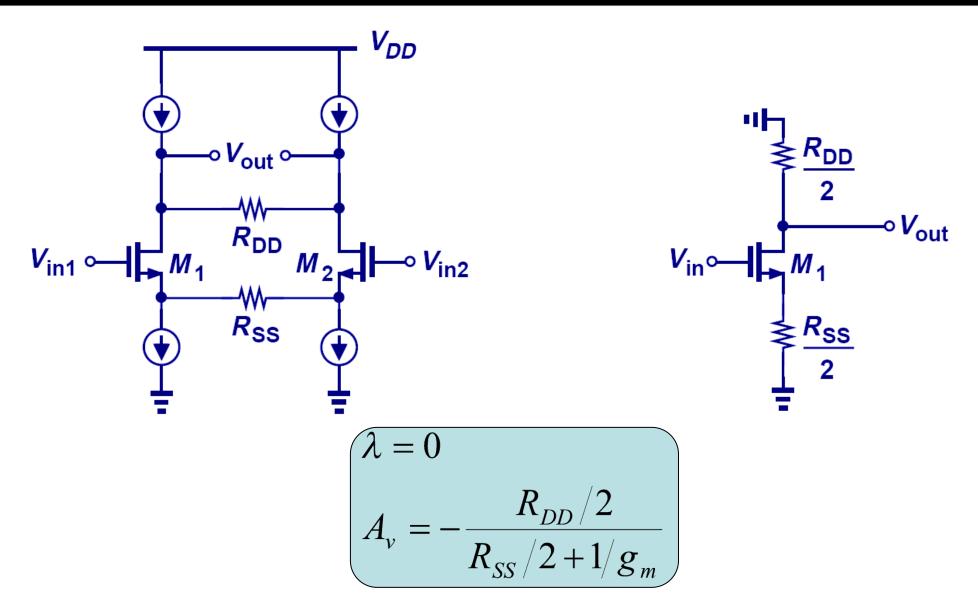
MOS Differential Pair Half Circuit Example II



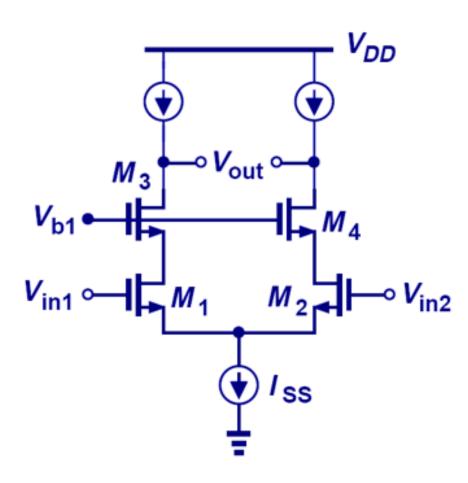
MOS Differential Pair Half Circuit Example III



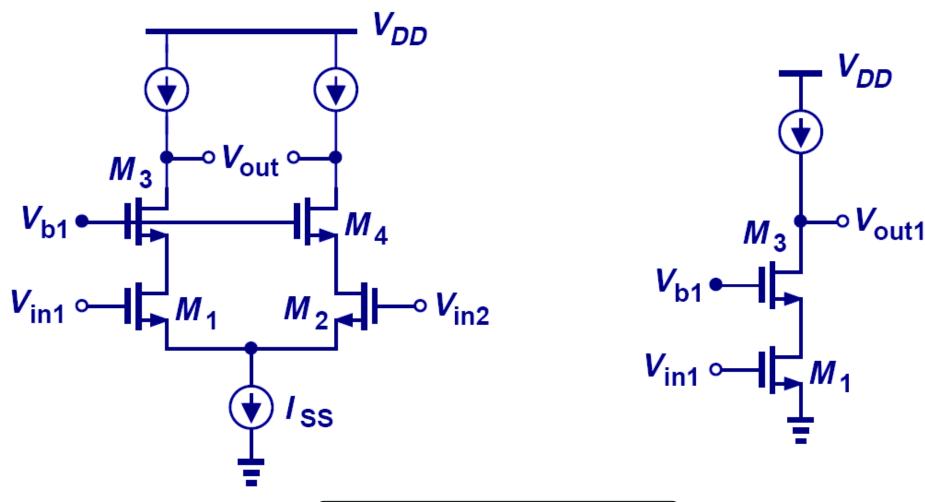
MOS Differential Pair Half Circuit Example III



MOS Cascode Differential Pair

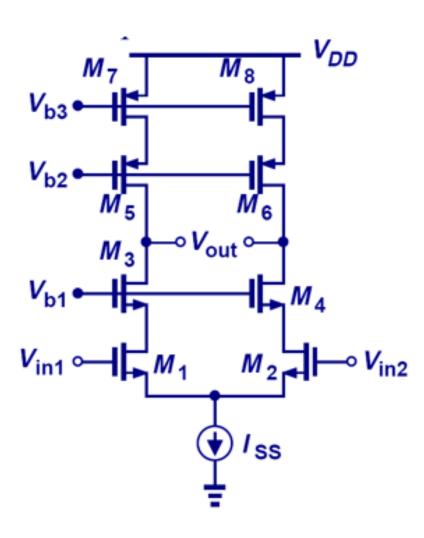


MOS Cascode Differential Pair

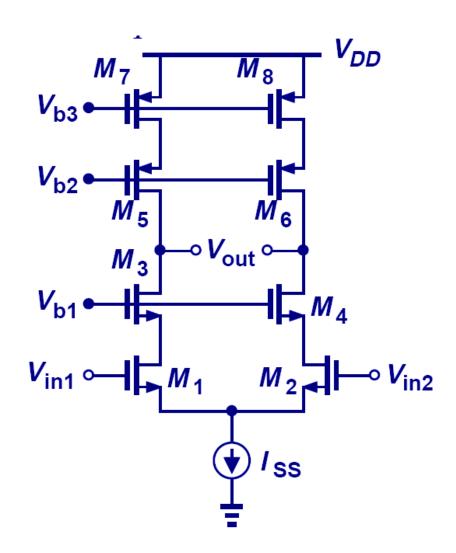


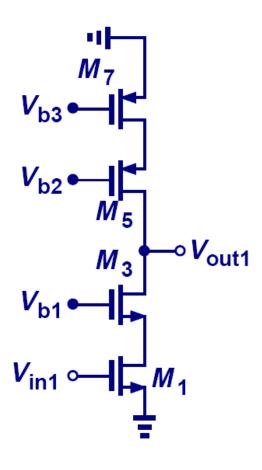
$$A_v \approx -g_{m1}r_{O3}g_{m3}r_{O1}$$

MOS Telescopic Cascode



MOS Telescopic Cascode





Thanks

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