Single Supply Quad Operational Amplifiers

The LM324 series are low–cost, quad operational amplifiers with true differential inputs. They have several distinct advantages over standard operational amplifier types in single supply applications. The quad amplifier can operate at supply voltages as low as 3.0 V or as high as 32 V with quiescent currents about one–fifth of those associated with the MC1741 (on a per amplifier basis). The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications. The output voltage range also includes the negative power supply voltage.

- Short Circuited Protected Outputs
- True Differential Input Stage
- Single Supply Operation: 3.0 V to 32 V (LM224, LM324, LM324A)
- Low Input Bias Currents: 100 nA Maximum (LM324A)
- Four Amplifiers Per Package
- Internally Compensated
- Common Mode Range Extends to Negative Supply
- Industry Standard Pinouts
- ESD Clamps on the Inputs Increase Ruggedness without Affecting Device Operation

MAXIMUM RATINGS (T_A = +25°C, unless otherwise noted.)

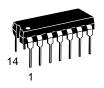
Rating	Symbol	LM224 LM324, LM324A	LM2902, LM2902V	Unit
Power Supply Voltages Single Supply Split Supplies	V_{CC}	32 ±16	26 ±13	Vdc
Input Differential Voltage Range (Note 1)	V _{IDR}	±32	±26	Vdc
Input Common Mode Voltage Range	V _{ICR}	-0.3 to 32	-0.3 to 26	Vdc
Output Short Circuit Duration	t _{SC}	Conti	nuous	
Junction Temperature	TJ	1	50	°C
Storage Temperature Range	T _{stg}	–65 to	+150	°C
Operating Ambient Temperature Range	T _A			°C
LM224		-25 to +85		
LM324, 324A		0 to +70		
LM2902			-40 to +105	
LM2902V, NCV2902			-40 to +125	

^{1.} Split Power Supplies.



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PDIP-14 N SUFFIX CASE 646

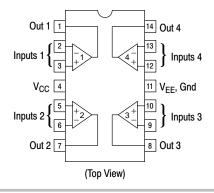


SO-14 D SUFFIX CASE 751A



TSSOP-14 DTB SUFFIX CASE 948G

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 10 of this data sheet.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0 \text{ V}$, $V_{EE} = Gnd$, $T_A = 25$ °C, unless otherwise noted.)

			LM224			LM324	Α		LM324			LM290	2	LM29	02V/NC	V2902	
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
Input Offset Voltage $V_{CC} = 5.0 \text{ V to } 30 \text{ V}$ $(26 \text{ V for LM2902, V)},$ $V_{ICR} = 0 \text{ V to}$ $V_{CC} - 1.7 \text{ V},$ $V_{O} = 1.4 \text{ V}, R_{S} = 0 \Omega$ $T_{A} = 25^{\circ}\text{C}$ $T_{A} = T_{high} \text{ (Note 2)}$ $T_{A} = T_{low} \text{ (Note 2)}$	V _{IO}		2.0	5.0 7.0 7.0		2.0	3.0 5.0 5.0		2.0	7.0 9.0 9.0		2.0	7.0 10 10		2.0	7.0 13 10	mV
Average Temperature Coefficient of Input Offset Voltage T _A = T _{high} to T _{low} (Notes 2 and 4)	ΔV _{IO} /ΔΤ	-	7.0	-	-	7.0	30	-	7.0	-	-	7.0	-	-	7.0	-	μV/°C
Input Offset Current $T_A = T_{high} \text{ to } T_{low}$ (Note 2)	I _{IO}	-	3.0	30 100	-	5.0	30 75	-	5.0 –	50 150	- -	5.0 –	50 200	- -	5.0 -	50 200	nA
Average Temperature Coefficient of Input Offset Current $T_A = T_{high}$ to T_{low} (Notes 2 and 4)	ΔΙ _{ΙΟ} /ΔΤ	-	10	-	-	10	300	-	10	-	-	10	-	-	10	-	pA/°C
Input Bias Current $T_A = T_{high} \text{ to } T_{low}$ (Note 2)	I _{IB}	-	-90 -	-150 -300	-	-45 -	-100 -200	-	-90 -	-250 -500	-	-90 -	-250 -500	-	-90 -	-250 -500	nA
Input Common Mode Voltage Range (Note 3) V _{CC} = 30 V (26 V for LM2902, V) T _A = +25°C T _A = T _{high} to T _{low} (Note 2)	V _{ICR}	0		28.3 28	0		28.3 28	0		28.3 28	0	- -	24.3 24	0	- -	24.3 24	V
Differential Input Voltage Range	V _{IDR}	_	-	V _{CC}	_	_	V _{CC}	_	-	V _{CC}	_	_	V _{CC}	-	_	V _{CC}	V
Large Signal Open Loop Voltage Gain $R_L = 2.0 \text{ k}\Omega$, $V_{CC} = 15 \text{ V}$, for Large V_O Swing	A _{VOL}	50	100	-	25	100	-	25	100	-	25	100	-	25	100	-	V/mV
$T_A = T_{high}$ to T_{low} (Note 2)		25	-	_	15	_	-	15	ı	_	15	_	_	15	-	-	
Channel Separation 10 kHz \leq f \leq 20 kHz, Input Referenced	CS	-	-120	-	-	-120	П	-	-120	-	-	-120	-	-	-120	-	dB
Common Mode Rejection, $R_S \le 10 \text{ k}\Omega$	CMR	70	85	-	65	70	_	65	70	_	50	70	_	50	70	-	dB
Power Supply Rejection	PSR	65	100	_	65	100	-	65	100	_	50	100	_	50	100	_	dB

2. LM224: T_{low} = -25°C, T_{high} = +85°C LM324/LM324A: T_{low} = 0°C, T_{high} = +70°C LM2902: T_{low} = -40°C, T_{high} = +105°C LM2902V & NCV2902: T_{low} = -40°C, T_{high} = +125°C NCV2902 is qualified for automotive use.

4. Guaranteed by design.

^{3.} The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is V_{CC} –1.7 V.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0 \text{ V}$, $V_{EE} = Gnd$, $T_A = 25^{\circ}C$, unless otherwise noted.)

			LM224		., .	LM324	A		LM324	ļ		LM290	2	LM29	02V/NC	V2902	
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage— High Limit (T _A = T _{high to} T _{low}) (Note 5) V _{CC} = 5.0 V, R _L =	V _{ОН}	3.3	3.5	-	3.3	3.5	-	3.3	3.5	_	3.3	3.5	_	3.3	3.5	_	V
2.0 k Ω , T _A = 25°C V _{CC} = 30 V (26 V for LM2902, V), R _L = 2.0 k Ω		26	-	-	26	-	-	26	-	_	22	-	_	22	_	-	
$V_{CC} = 30 \text{ V}$ (26 V for LM2902, V), $R_L = 10 \text{ k}\Omega$		27	28	ı	27	28	I	27	28	-	23	24	-	23	24	-	
$ \begin{aligned} & \text{Output Voltage} - \\ & \text{Low Limit,} \\ & \text{V}_{CC} = 5.0 \text{ V,} \\ & \text{R}_{L} = 10 \text{ k}\Omega, \\ & \text{T}_{A} = \text{T}_{high} \text{ to T}_{low} \\ & \text{(Note 5)} \end{aligned} $	V _{OL}	1	5.0	20	-	5.0	20	-	5.0	20	-	5.0	100	-	5.0	100	mV
Output Source Current $(V_{ID} = +1.0 \text{ V}, V_{CC} = 15 \text{ V})$ $T_A = 25^{\circ}\text{C}$	I _{O +}	20	40	_	20	40	_	20	40	_	20	40	_	20	40	_	mA
$T_A = T_{high}$ to T_{low} (Note 5)		10	20	-	10	20	-	10	20	-	10	20	_	10	20	_	
Output Sink Current $(V_{ID} = -1.0 \text{ V},$ $V_{CC} = 15 \text{ V})$ $T_A = 25^{\circ}\text{C}$	I _O –	10	20	-	10	20	-	10	20	-	10	20	-	10	20	-	mA
$T_A = T_{high}$ to T_{low} (Note 5)		5.0	8.0	-	5.0	8.0	-	5.0	8.0	_	5.0	8.0	_	5.0	8.0	_	
$(V_{ID} = -1.0 \text{ V},$ $V_{O} = 200 \text{ mV},$ $T_{A} = 25^{\circ}\text{C})$		12	50	I	12	50	Ι	12	50	_	-	I	-	-	_	_	μА
Output Short Circuit to Ground (Note 6)	I _{SC}	_	40	60	-	40	60	-	40	60	-	40	60	-	40	60	mA
$\begin{aligned} & \text{Power Supply Current} \\ & (T_A = T_{high} \text{ to } T_{low}) \\ & (\text{Note 5}) \\ & V_{CC} = 30 \text{ V} \\ & (26 \text{ V for LM2902, V)}, \end{aligned}$	I _{CC}	_	-	3.0	_	1.4	3.0	-	-	3.0	_	-	3.0	-	_	3.0	mA
$V_{O} = 0 \text{ V}, R_{L} = \infty$ $V_{CC} = 5.0 \text{ V},$ $V_{O} = 0 \text{ V}, R_{L} = \infty$	0.7	-	_	1.2	-	0.7	1.2	-	-	1.2	-	-	1.2	-	-	1.2	

^{5.} LM224: T_{low} = -25°C, T_{high} = +85°C
 LM324/LM324A: T_{low} = 0°C, T_{high} = +70°C
 LM2902: T_{low} = -40°C, T_{high} = +105°C
 LM2902V & NCV2902: T_{low} = -40°C, T_{high} = +125°C
 NCV2902 is qualified for automotive use.
6. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is V_{CC} -1.7 V.

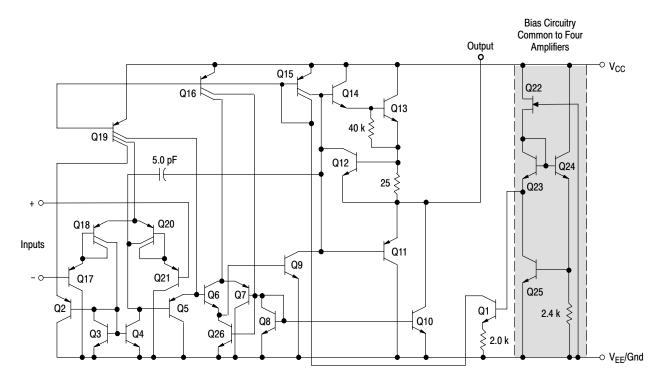


Figure 1. Representative Circuit Diagram (One–Fourth of Circuit Shown)

CIRCUIT DESCRIPTION

The LM324 series is made using four internally compensated, two-stage operational amplifiers. The first stage of each consists of differential input devices Q20 and Q18 with input buffer transistors Q21 and Q17 and the differential to single ended converter Q3 and Q4. The first stage performs not only the first stage gain function but also performs the level shifting and transconductance reduction functions. By reducing the transconductance, a smaller compensation capacitor (only 5.0 pF) can be employed, thus saving chip area. The transconductance reduction is accomplished by splitting the collectors of Q20 and Q18. Another feature of this input stage is that the input common mode range can include the negative supply or ground, in single supply operation, without saturating either the input devices or the differential to single-ended converter. The second stage consists of a standard current source load amplifier stage.

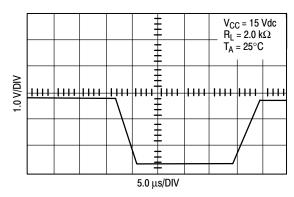
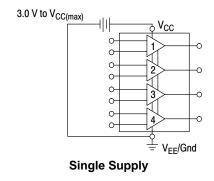
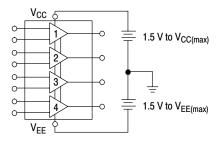


Figure 2. Large Signal Voltage Follower Response

Each amplifier is biased from an internal-voltage regulator which has a low temperature coefficient thus giving each amplifier good temperature characteristics as well as excellent power supply rejection.





Split Supplies

Figure 3.

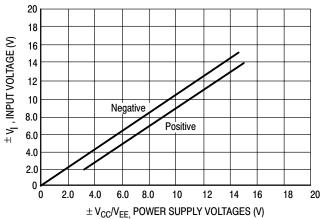


Figure 4. Input Voltage Range

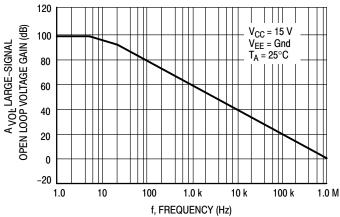


Figure 5. Open Loop Frequency

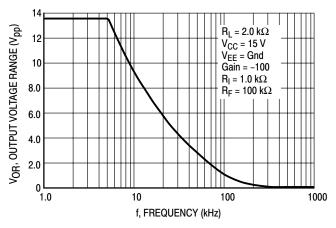


Figure 6. Large-Signal Frequency Response

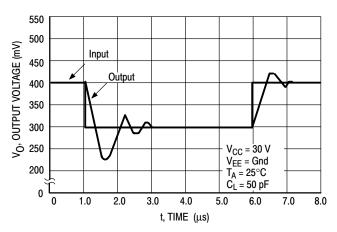


Figure 7. Small–Signal Voltage Follower Pulse Response (Noninverting)

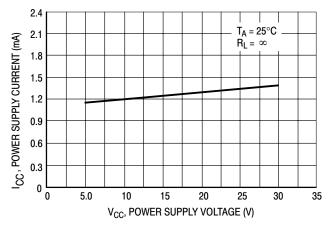


Figure 8. Power Supply Current versus Power Supply Voltage

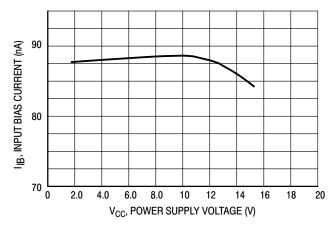


Figure 9. Input Bias Current versus Power Supply Voltage

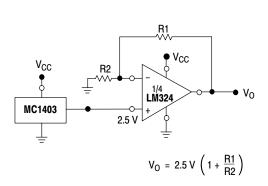


Figure 10. Voltage Reference

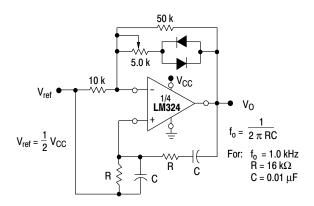


Figure 11. Wien Bridge Oscillator

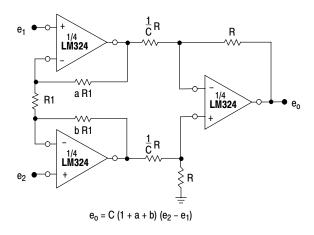


Figure 12. High Impedance Differential Amplifier

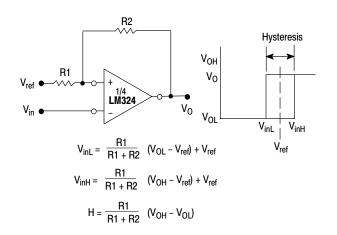


Figure 13. Comparator with Hysteresis

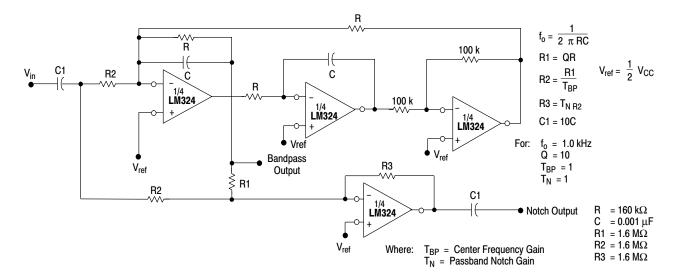


Figure 14. Bi-Quad Filter

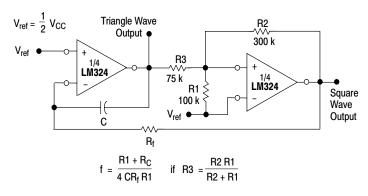


Figure 15. Function Generator

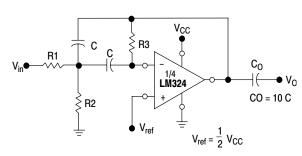


Figure 16. Multiple Feedback Bandpass Filter

Given: f_0 = center frequency

A(f₀) = gain at center frequency

Choose value f₀, C

Then: R3 =
$$\frac{Q}{\pi f_0}$$

$$R1 = \frac{R3}{2 A(f_0)}$$

$$R2 = \frac{R1 R3}{4Q^2 R1 - R3}$$

For less than 10% error from operational amplifier, $\frac{Q_0\,f_0}{BW}\,<0.1$

where fo and BW are expressed in Hz.

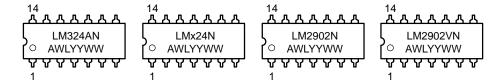
If source impedance varies, filter may be preceded with voltage follower buffer to stabilize filter parameters.

ORDERING INFORMATION

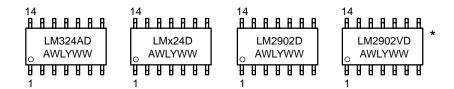
Device	Package	Operating Temperature Range	Shipping
LM224D	SO-14		55 Units/Rail
LM224DR2	SO-14	1	2500 Tape & Reel
LM224DTB	TSSOP-14	−25° to +85°C	96 Units/Rail
LM224DTBR2	TSSOP-14	1	2500 Tape & Reel
LM224N	PDIP-14	1	25 Units/Rail
LM324D	SO-14		55 Units/Rail
LM324DR2	SO-14	1	2500 Tape & Reel
LM324DTB	TSSOP-14	1	96 Units/Rail
LM324DTBR2	TSSOP-14	1	2500 Tape & Reel
LM324N	PDIP-14	T	25 Units/Rail
LM324AD	SO-14	0° to +70°C	55 Units/Rail
LM324ADR2	SO-14	1	2500 Tape & Reel
LM324ADTB	TSSOP-14	1	96 Units/Rail
LM324ADTBR2	TSSOP-14	1	2500 Tape & Reel
LM324AN	PDIP-14	1	25 Units/Rail
LM2902D	SO-14		55 Units/Rail
LM2902DR2	SO-14	1	2500 Tape & Reel
LM2902DTB	TSSOP-14	−40° to +105°C	96 Units/Rail
LM2902DTBR2	TSSOP-14	1	2500 Tape & Reel
LM2902N	PDIP-14	1	25 Units/Rail
LM2902VD	SO-14		55 Units/Rail
LM2902VDR2	SO-14	1	2500 Tape & Reel
LM2902VDTB	TSSOP-14	1	96 Units/Rail
LM2902VDTBR2	TSSOP-14	-40° to +125°C	2500 Tape & Reel
LM2902VN	PDIP-14	1	25 Units/Rail
NCV2902DR2	SO-14	1	2500 Tape & Reel

MARKING DIAGRAMS

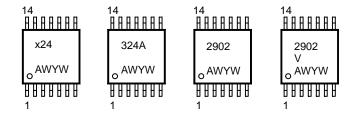
PDIP-14 N SUFFIX CASE 646



SO-14 D SUFFIX CASE 751A



TSSOP-14 DTB SUFFIX CASE 948G



x = 2 or 3

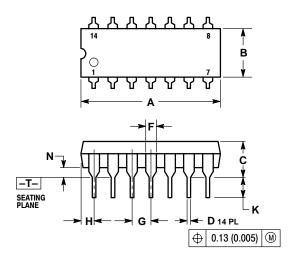
A = Assembly Location

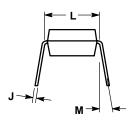
WL = Wafer Lot YY, Y = Year WW, W = Work Week

*This marking diagram also applies to NCV2902.

PACKAGE DIMENSIONS

PDIP-14 **N SUFFIX** CASE 646-06 ISSUE M





- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- Y 14.5M, 1982.

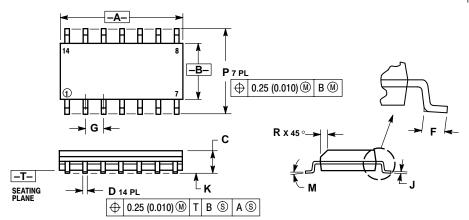
 CONTROLLING DIMENSION: INCH.

 DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.

 DIMENSION B DOES NOT INCLUDE MOLD FLASH.
- 5. ROUNDED CORNERS OPTIONAL.

	INC	HES	MILLIN	IETERS			
DIM	MIN	MAX	MIN	MAX			
Α	0.715	0.770	18.16	18.80			
В	0.240	0.260	6.10	6.60			
С	0.145	0.185	3.69	4.69			
D	0.015	0.021	0.38	0.53			
F	0.040	0.070	1.02	1.78			
G	0.100	BSC	2.54 BSC				
Н	0.052	0.095	1.32	2.41			
J	0.008	0.015	0.20	0.38			
K	0.115	0.135	2.92	3.43			
L	0.290	0.310	7.37	7.87			
M		10°		10°			
N	0.015	0.039	0.38	1.01			

SO-14 **D SUFFIX** CASE 751A-03 ISSUE F



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- Y14.5M, 1982.

 C CONTROLLING DIMENSION: MILLIMETER.

 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.

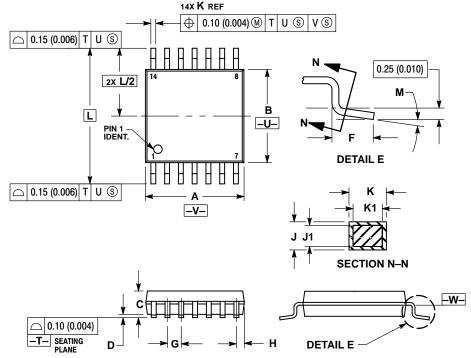
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

 5. DIMENSION D DOES NOT INCLUDE DAMBAR
- PROTRUSION. ALLOWABLE DAMBAR
 PROTRUSION. SHALL BE 0.127 (0.005) TOTAL
 IN EXCESS OF THE D DIMENSION AT
 MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES					
DIM	MIN	MAX	MIN	MAX				
Α	8.55	8.75	0.337	0.344				
В	3.80	4.00	0.150	0.157				
С	1.35	1.75	0.054	0.068				
D	0.35	0.49	0.014	0.019				
F	0.40	1.25	0.016	0.049				
G	1.27	BSC	0.050 BSC					
J	0.19	0.25	0.008	0.009				
K	0.10	0.25	0.004	0.009				
M	0°	7°	0°	7°				
Р	5.80	6.20	0.228	0.244				
R	0.25	0.50	0.010	0.019				

PACKAGE DIMENSIONS

TSSOP-14 **DTB SUFFIX** CASE 948G-01 **ISSUE O**



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
 DIMENSION B DOES NOT INCLUDE INTERLEAD
- FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
 DIMENSION K DOES NOT INCLUDE DAMBAR
- PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION. TERMINAL NUMBERS ARE SHOWN FOR
- REFERENCE ONLY.

 7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

	MILLIN	IETERS	INC	HES		
DIM	MIN	MAX	MIN	MAX		
Α	4.90	5.10	0.193	0.200		
В	4.30	4.50	0.169	0.177		
С		1.20		0.047		
D	0.05	0.15	0.002	0.006		
F	0.50	0.75	0.020	0.030		
G	0.65	BSC	0.026 BSC			
Н	0.50	0.50 0.60		0.024		
J	0.09	0.20	0.004	0.008		
J1	0.09	0.16	0.004	0.006		
K	0.19	0.30	0.007	0.012		
K1	0.19	0.25	0.007	0.010		
L	6.40		0.252 BSC			
M	0°	8°	0°	8°		

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