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Silicon Controlled Rectifiers _

C106 Series

File Number 1005

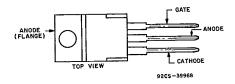
4-A Sensitive-Gate Silicon **Controlled Rectifiers**

For Power-Switching and Control Application

Features:

- 3.5-A(rms) on-state current ratings
- 20-A peak surge capability
- Glass-passivated chip for stability
- Formed-lead options available

TERMINAL DESIGNATIONS



JEDEC TO-220AB

The RCA-C106 series of sensitive-gate silicon controlled rectifiers are designed for switching ac and dc currents. The types within the series differ in their voltage ratings; the voltage ratings are identified by suffix letters in type designations.

These SCR's have microampere gate-current requirements which permit operation with low-level logic circuits. They

can be used for lighting, power-switching, and motor-speed controls, and for gate-current amplification for driving large

All types in the series utilize the JEDEC-TO-202AB (RCA VERSATAB) plastic package.

MAXIMUM RATINGS, Absolute-Maximum Values:

C106F C106A C106B C106C C106D C106E C106M C106S C106N

V _{RRM} : R _{GK} = 1000 Ω, T _C = -40 to 110° C)							700	000	٧
V _{DRM}	> 50	100	200	300	400	500	600	700	800	
R _{GK} = 1000 Ω, T _C = -40 to 110°C	,				0.0					Α
I _{T(AV)} (T _C = 45°C)										Ä
I _{T(RMS)} (T _C = 45°C)										A
I _{T(DC)} (T _C = 70°C)										
I _{TSM} : For one cycle of applied principal voltage, T _C = 45°C					20					
60 Hz (sinusoidal)										. A
50 Hz (sinusoidal)										Ā
I_{GM} (t = 10 μ s)					0.2					v
V _{GRM} · · · · · · · · · · · · · · · · · · ·										•
di/dt:					100					A/μs
$V_{DM} = V_{DRM}$, $I_G = 1$ mA, $t_r = 0.5 \mu s$, $T_C = 110^{\circ} C$										• •
I ² t [At T _C shown for I _{T(RMS)}]:					1 77					. A2s
t = 10 ms					- 1.67					. A2s
8.33 ms					0.82					A29
1 ms										. 'W
P _{GM} (For 10 µs max.)										. v
P _{G(AV)} (Averaging time = 10 ms max.)										
T _{sta}					40 10 7	110				٠,
To					-40 (0 +	110				. •6
T _T (During soldering for 10 s max.)					250					

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