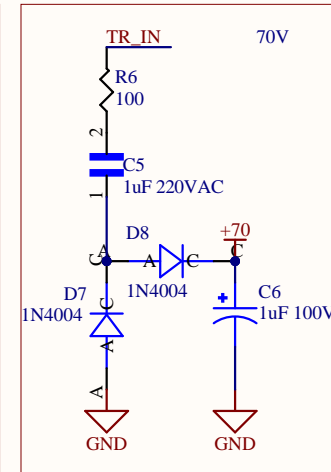
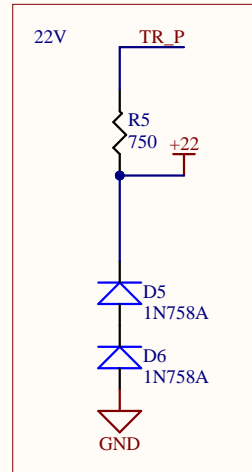
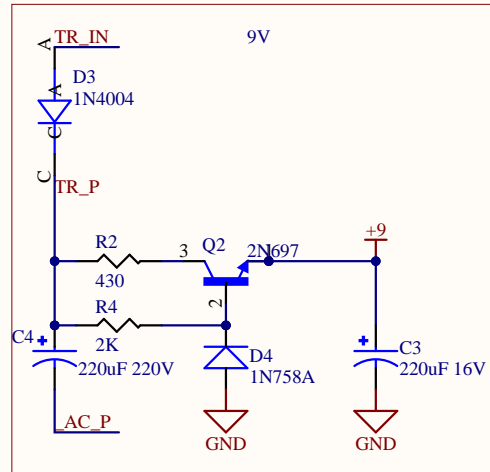
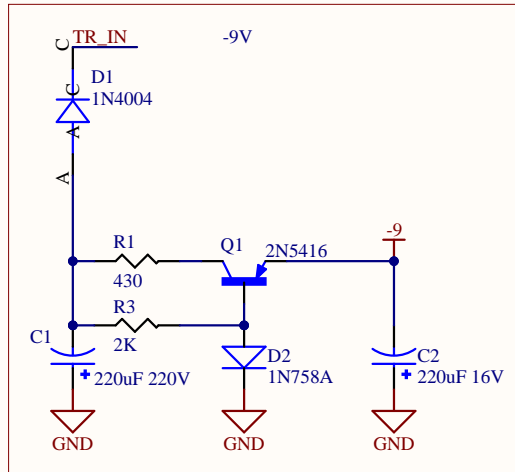
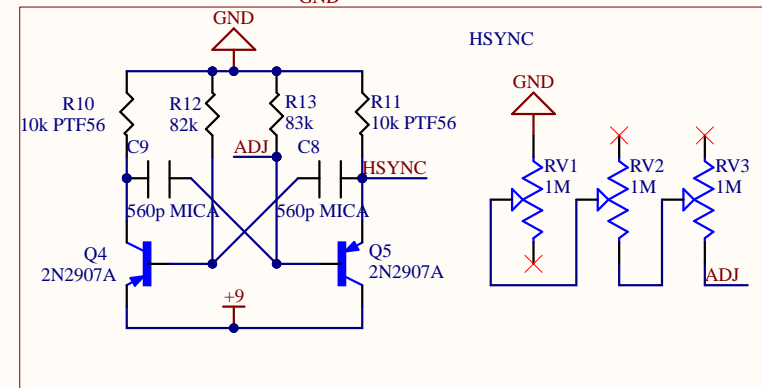
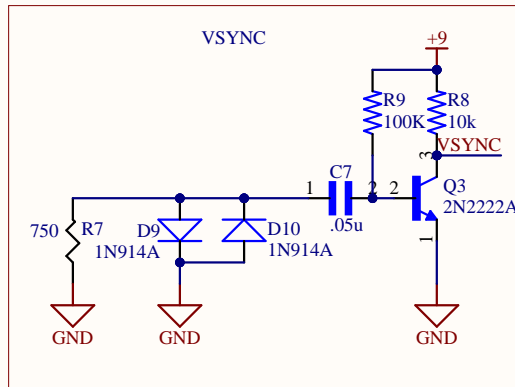


Title		
Size	Number	
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\Magnavox_TV2\PCB\TVG2.SchDoc	Drawn By:



VSYNC TVG2[1A], V\_DMV[3C], V\_DMV\_Mono[3C]  
 HSYNC TVG2[1A], Chroma\_VDO[3C], H\_DMV[3C], Retrace[3C]  
 TR\_IN TVG2[1B]

+9 -9 TVG2[1B], Chroma phase shifter[4B], Chroma\_VDO[3C], Ch  
 -9 +9 TVG2[1A], Chroma phase shifter[4B], Chroma\_VDO[3C], H  
 +22 +22 TVG2[1A]  
 +70 +70 TVG2[1A]  
 GND TVG2[1B], Chroma phase shifter[4B], Chroma\_VDO[3C], Ch  
 GND



Title

Size

A

Number

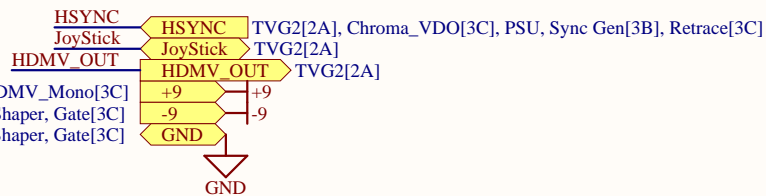
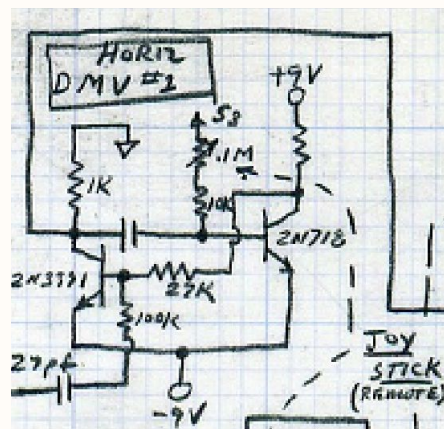
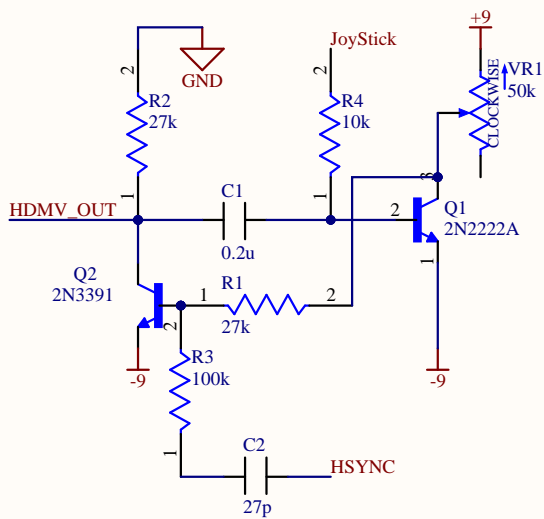
Date: 8/06/2023

File: D:\Git\...\PSU, Sync Gen.SchDoc

Revision

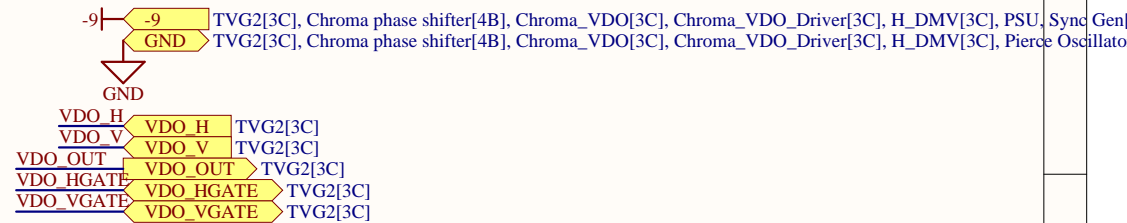
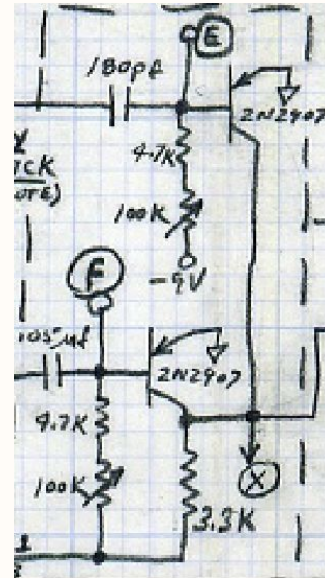
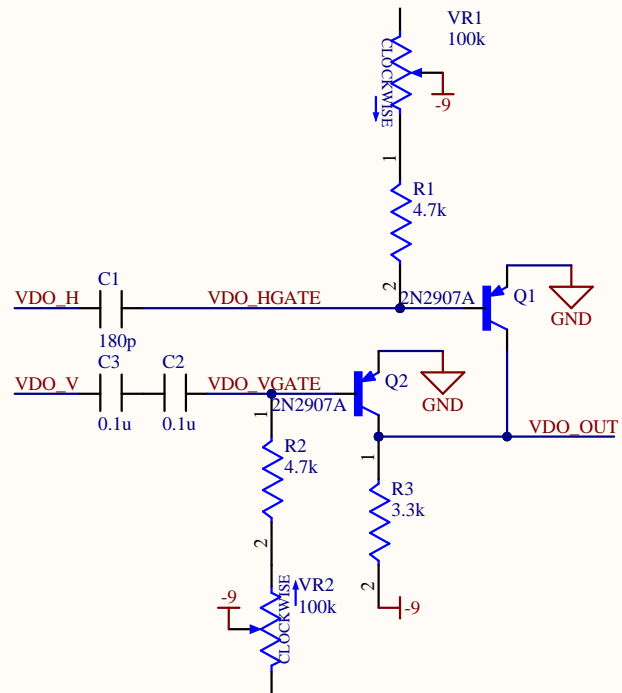
Sheet of

Drawn By:

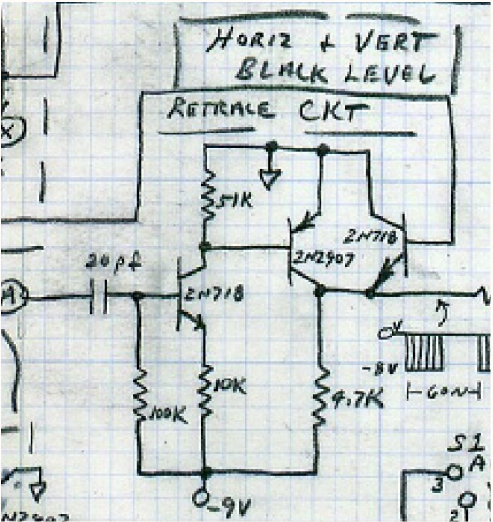
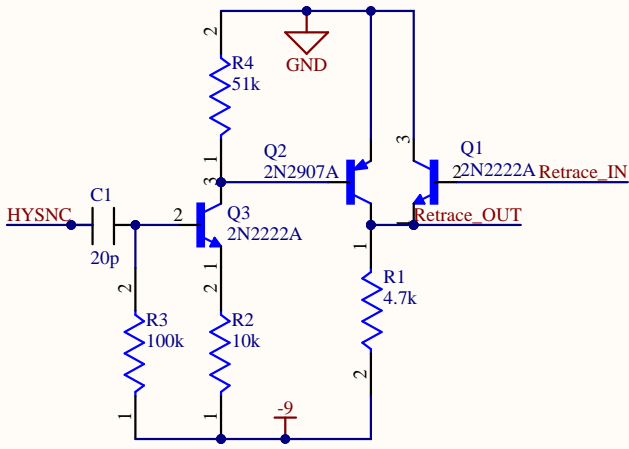


TVG2[2A], Chroma phase shifter[4B], Chroma\_VDO[3C], Pierce Oscillator[3C], PSU, Sync Gen[3C], V\_DMV[3C], V\_DMV\_Mono[3C]  
Chroma\_VDO[3C], Chroma\_VDO\_Driver[3C], PSU, Sync Gen[3B], Retrace[3C], V\_DMV[3C], V\_DMV\_Mono[3C], VDO Shaper, Gate[3C]  
Chroma\_VDO\_Driver[3C], Pierce Oscillator[3C], PSU, Sync Gen[3C], Retrace[3C], V\_DMV[3C], V\_DMV\_Mono[3C], VDO Shaper, Gate[3C]

Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\Magnavox_TV2\PCB\H_DMV_Sch	Drawn By:



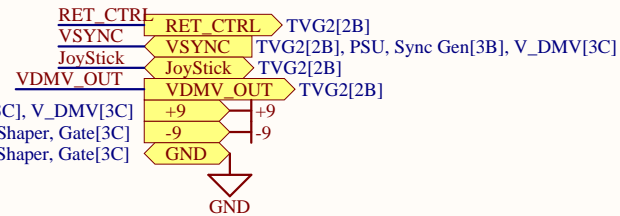
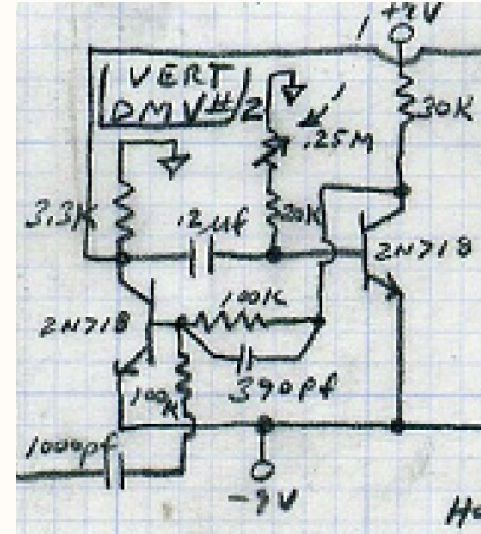
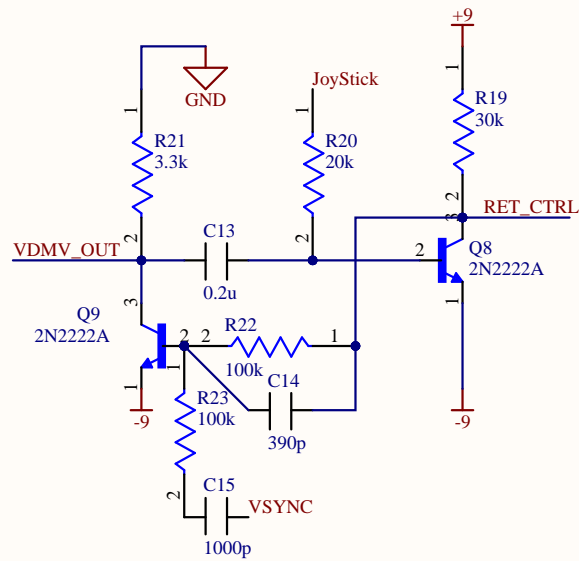
Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\...\VDO Shaper, Gate.SchDoc	Drawn By:



Retrace\_OUT OUT TVG2[3B]  
Retrace\_IN IN TVG2[3B]  
HYSNC HSYNC TVG2[3B], Chroma\_VDO[3C], H\_DMV[3C], PSU, Sync Gen[3B]

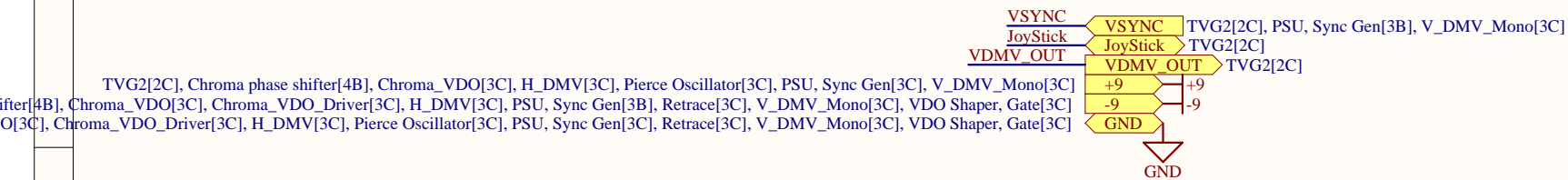
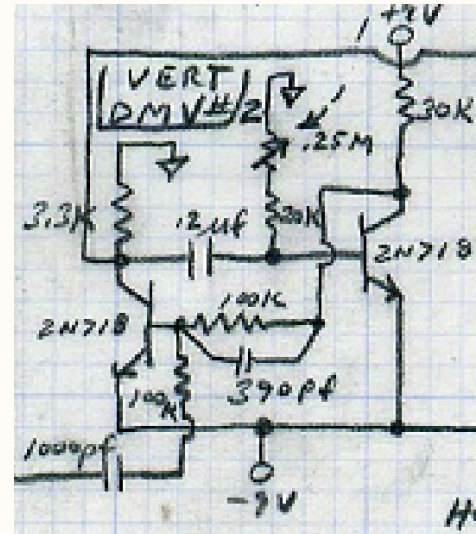
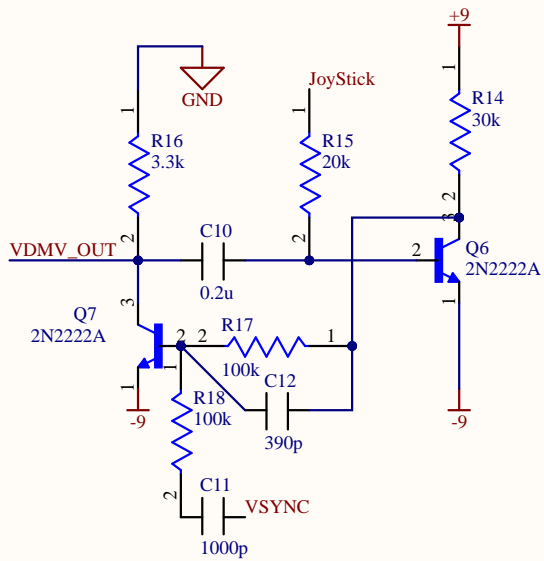
-9 -9 TVG2[3B], Chroma phase shifter[4B], Chroma\_VDO[3C], Chroma\_VDO\_Driver[3C], H\_DMV[3C], PSU  
GND GND TVG2[3B], Chroma phase shifter[4B], Chroma\_VDO[3C], Chroma\_VDO\_Driver[3C], H\_DMV[3C], PSU  
GND

Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\Magnavox_TV2\PCB\Retrace_Sch	Drawn By:



TVG2[2B], Chroma phase shifter[4B], Chroma\_VDO[3C], H\_DMV[3C], Pierce Oscillator[3C], PSU, Sync Gen[3C], V\_DMV[3C]  
[4B], Chroma\_VDO[3C], Chroma\_VDO\_Driver[3C], H\_DMV[3C], PSU, Sync Gen[3B], Retrace[3C], V\_DMV[3C], VDO Shaper, Gate[3C]  
C], Chroma\_VDO\_Driver[3C], H\_DMV[3C], Pierce Oscillator[3C], PSU, Sync Gen[3C], Retrace[3C], V\_DMV[3C], VDO Shaper, Gate[3C]

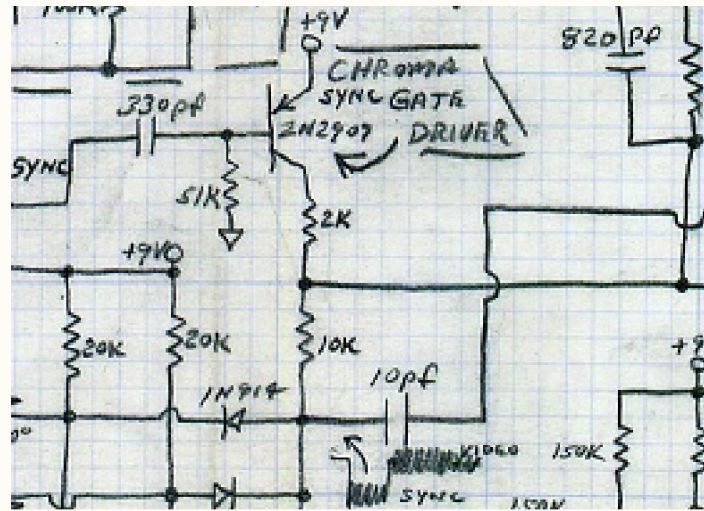
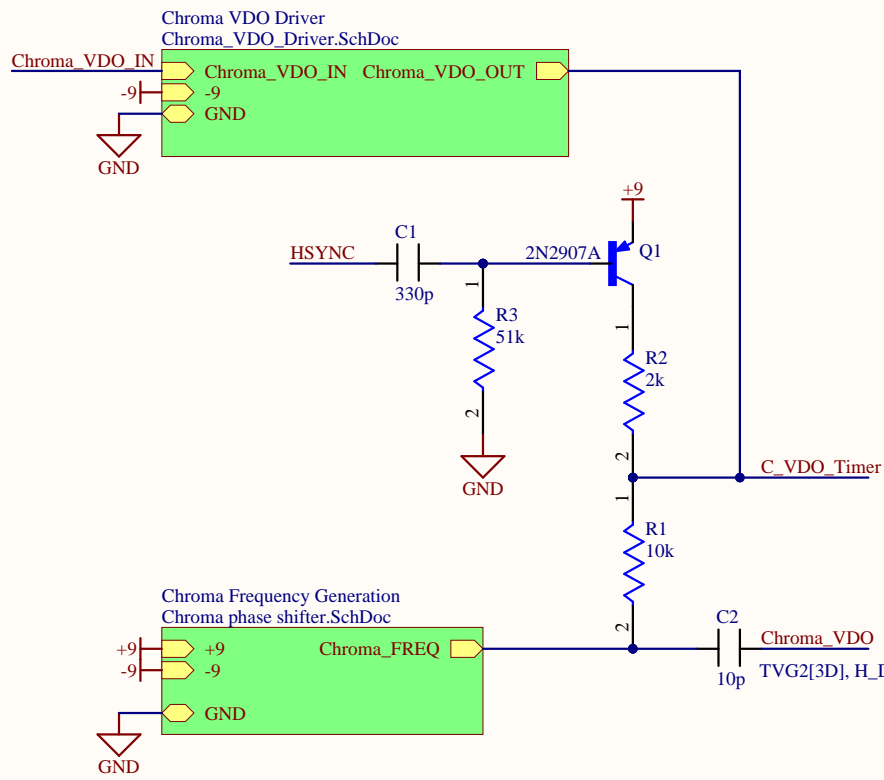
Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\..V_DMV_Mono.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\Magnavox_TV2\PCB\V_DMV_Sch	Drawn By:

A

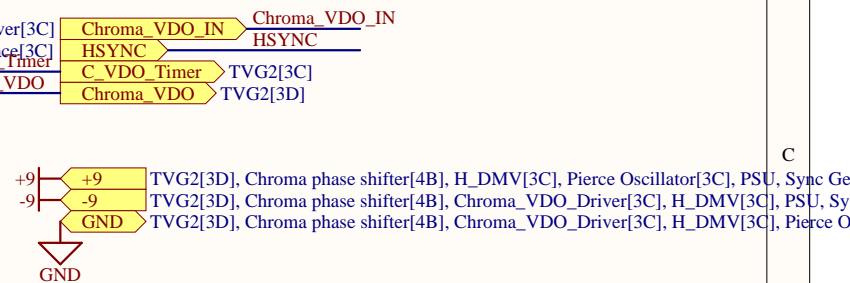
A



B

C

C

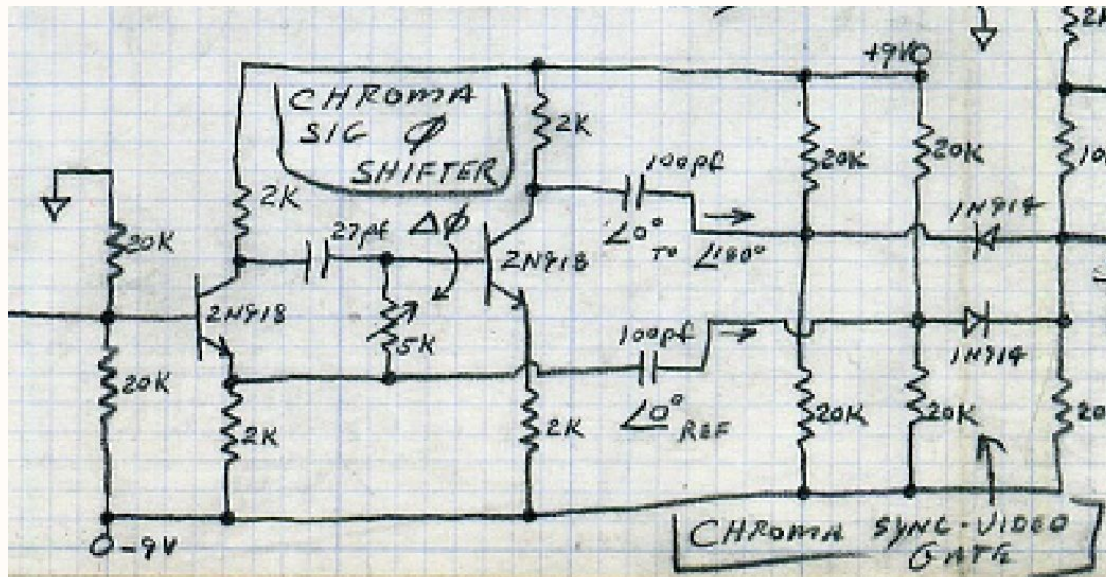
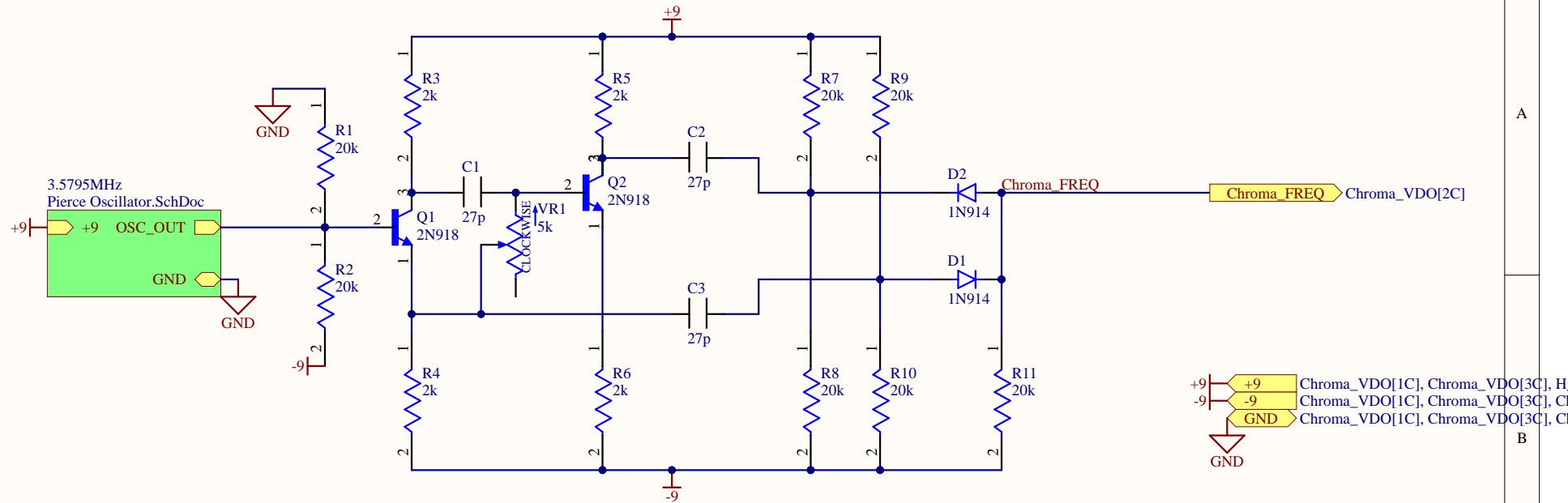


D

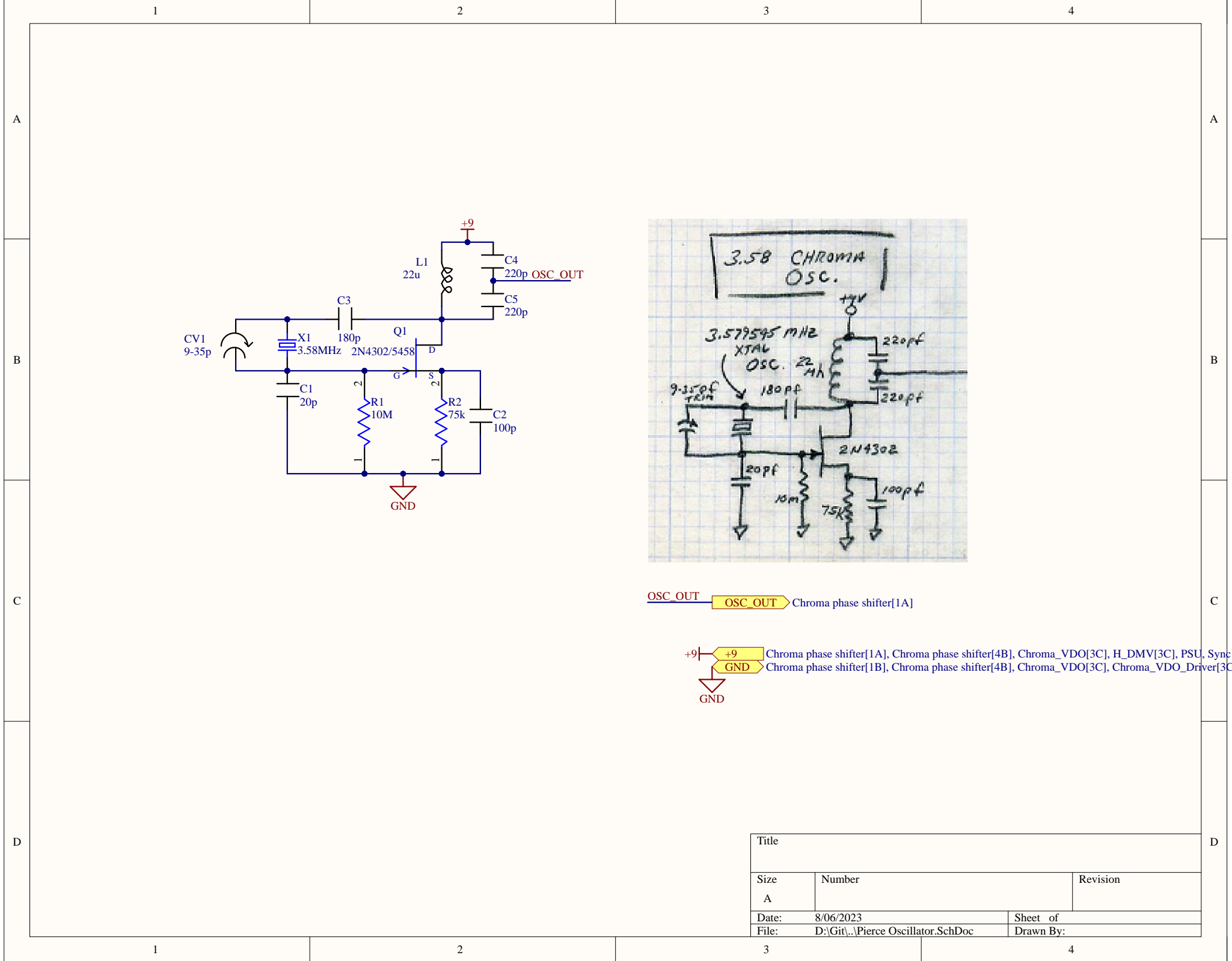
D

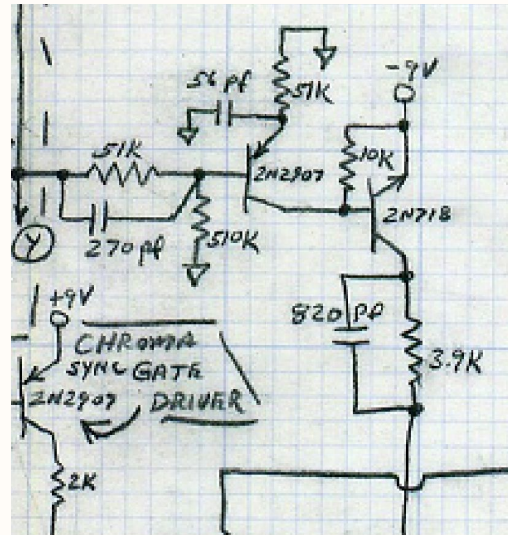
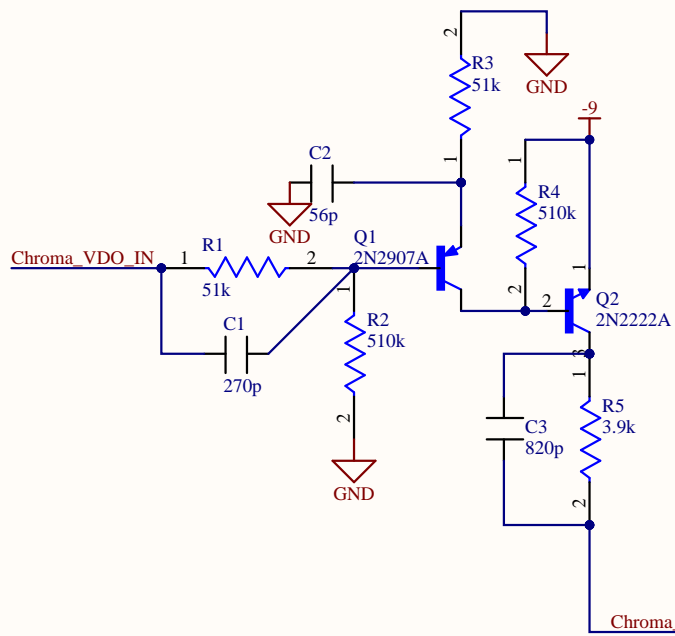
Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\...\Chroma_VDO.SchDoc	Drawn By:





Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\...\Chroma phase shifter.SchDoc	Drawn By:





Chroma\_VDO\_OUT Chroma\_VDO\_OUT Chroma\_VDO[2A]  
Chroma\_VDO[1A], Chroma\_VDO[3C] Chroma\_VDO\_IN Chroma\_VDO\_IN

-9V -9V Chroma\_VDO[1A], Chroma phase shifter[4B], Chroma\_VDO[3C], H\_DMV[3C], PSU, Sync Gen[3B], Retrace[3C], V  
GND GND Chroma\_VDO[1A], Chroma phase shifter[4B], Chroma\_VDO[3C], H\_DMV[3C], Pierce Oscillator[3C], PSU, Sync G

Title		
Size	Number	Revision
A		
Date:	8/06/2023	Sheet of
File:	D:\Git\...\Chroma_VDO_Driver.SchDoc	Drawn By: