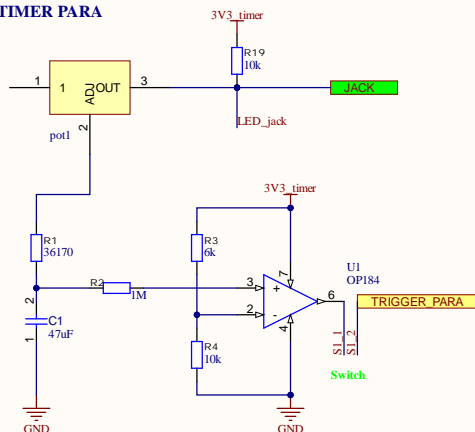
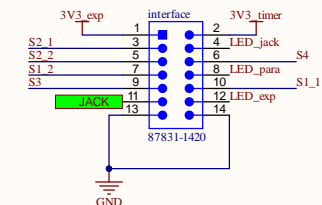
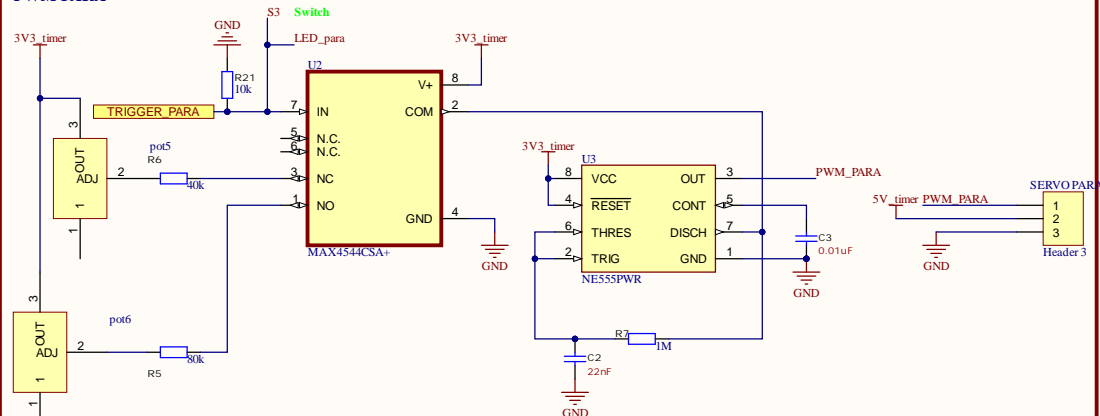


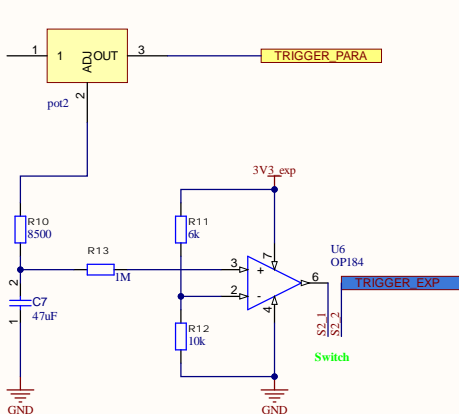
TIMER PARA



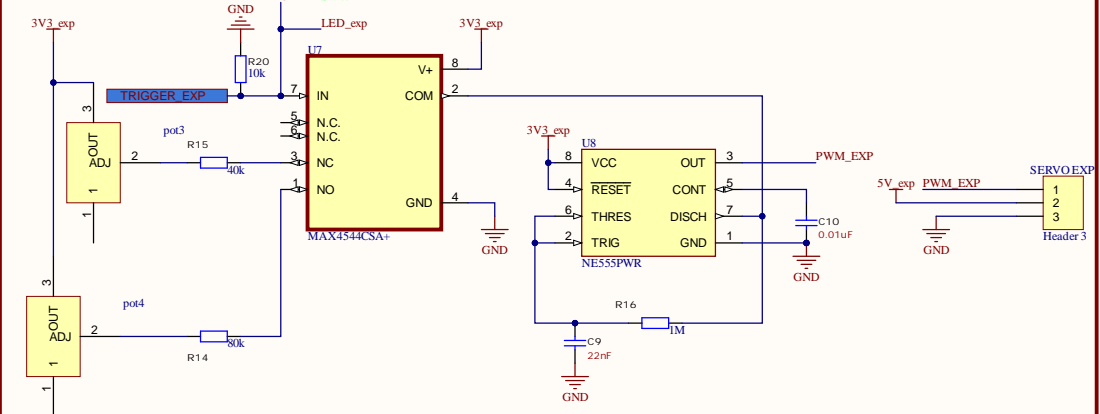
PWM PARA



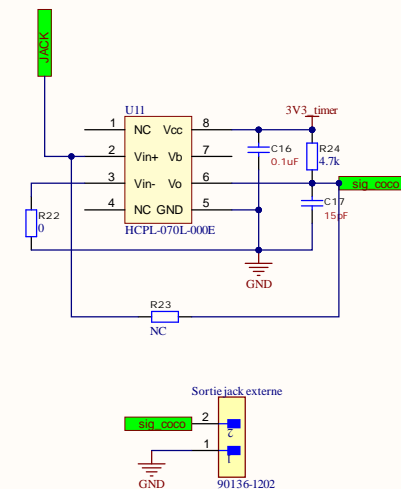
TIMER EXP



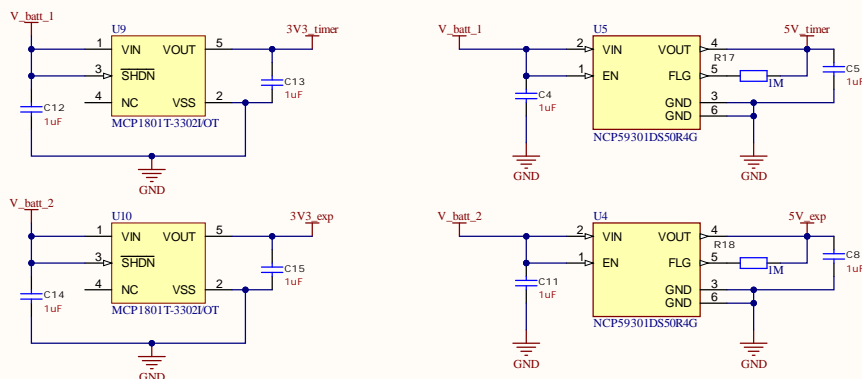
PWM EXP



OPTOCOUPLEUR



SUPPLY VOLTAGE



AO comparator :
Voltage bridge divider 6k/10k avec 3.3V
=> comparator voltage = 2V
=> when RC = 2V , trigger system

RC charge :
- the charge goes up to 3.3V in a known pattern.
- It takes t-Tau second to reach 0.632*3.3=2V=comparator voltage

AUTHORS		DATE		PROJECT	
		10/06/2022		timer_tigresse.PrjPcb	
PROJECT REVISION:		DOCUMENT REVISION:		LAST MODIFIED	
				10/06/2022	
REFERENCE DOCUMENTS		TITLE		sequencer tigresse 2022	
BOM:		SIZE		CAGE CODE	
ASSY DWG:		A3		DWG NO.	
FAB DWG:				REV	
PCB DWG:				*	
SCALE:		FILE NAME		timer_tigresse.SchDoc	
		SHEET		1 OF 1	

Comment	Description	Designator	Footprint	LibRef	Quantity
Capacitor 10 uF +/- 10% 16 V 0805	Chip Capacitor, 10 uF, +/- 10%, 16 V, 0805 (2012 Metric)	C1, C7	CAPC2013X135X45N1.0T25	CMP-001-00015-6	2
C0603C104K4PACTU	Cap Ceramic 0.1uF 16V X5R 10% SMD 0603 85°C Paper T/R	C2, C3, C4, C5, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17	0603c	CMP-001-00102-3	14
87831-1420	2.00mm Pitch Milli-Grid Header Vertical Through Hole Shrouded 14 Circuits 0.38	interface	MOLEX-87831-1420_V	CMP-002-00111-1	1
3386F-1-153LF	Trimmer, Cermet, 0.5W, 15000ohm, 10% +/-Tol, 100ppm/Cel, 1 Turn(s)	pot1, pot2, pot3, pot4, pot5, pot6	New footprint 1	CMP-009-00259-1	6
ERJ-P06F1003V	PANASONIC - Res Thick Film 0805 100K Ohm 1% 1/2W ±100ppm/C Molded SMD Punched Carrier T/R	R1, R2, R3, R4, R5, R6, R7, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24	0805	CMP-009-00217-1	22
Header 3	Header, 3-Pin	SERVO EXP, SERVO PARA	HDR1X3	Header 3	2
90136-1202	Conn Shrouded Header (4 Sides) HDR 2 POS 2.54mm Solder ST Thru-Hole C-Grid III™ Frame	Sortie jack externe	FP-90136-1202-MFG	CMP-002-00113-1	1
OP184	ANALOG DEVICES - Precision Rail-to-Rail Input and Output Operational Amplifiers - SOIC-8	U1, U6	OPA184-Footprint-1	CMP-001-0000-3	2
MAX4544CSA+	Low-Voltage, Single-Supply Dual SPST/SPDT Analog Switches	U2, U7	New footprint 1	CMP-015-00012-1	2
NE555PWR	Single Precision Timer 8-TSSOP 0 to 70	U3, U8	FP-PW0008A-MFG	CMP-00001-1	2
NCP59301DS50R4G	Régulateur LDO Tension fixe, entrée 2,24 V à 13,5 V, Dropout 400 mV, Sortie 5 V, 3 A, TO-263 5	U4, U5	New footprint 1	CMP-019-00058-1	2
MCP1801T-3302I/OT	IC, LINEAR REGULATOR; MAX. INPUT, 10V; OUTPUT, 3.3V; 150mA; ACCURACY, 0.4%; SOT23-5	U9, U10	SOT-23-OT5_N	CMP-019-00059-1	2
HCPL-070L-000E	Optocoupler DC-IN 1-CH Darlington With Base DC-OUT 8-Pin SOIC Tube	U11	HCPL-070L-000E-Footprint-1	CMP-015-00014-1	1