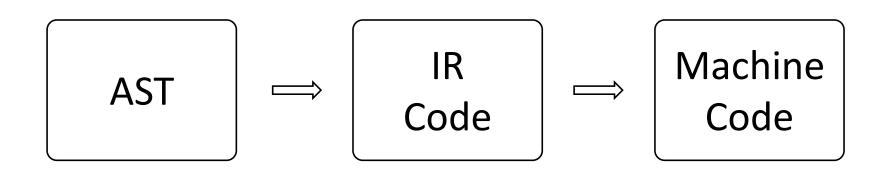
# Intermediate Representation

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#### Intermediate Representation

- Generic representation of instructions
  - Allows language and machine independent optimizations
  - Not executable



#### IR Language

- Temporary variables (IR registers)
  - t1, t2, ... (unlimited)
- Instructions
  - assignments, add, sub, call, return, ...
- Labels
  - label 1:

#### Constant assignment:

- <register> = <constant>
  - t1 = 7

#### Read from memory:

- <register> = <variable\_name>
  - t1 = x

#### Write to memory:

- <variable\_name> = <register>
  - y = t2

#### Arithmetic operations:

- <register> = op <register> <register> ...
  - t4 = add t1 t2
  - t0 = sub t0 t1

#### Branches:

- br <label>
  - br some\_label
- beq <register> [<constant> | <register>] <label>
  - beq t1 0 label\_1
  - beq t2 t3 label\_7

#### Functions:

- call <function\_name> <args>
  - call bar
  - call foo t1 t2
- <register> = call <function\_name> <args>
  - t8 = call foo t7
- return <register>
  - return t3

#### Arrays:

- <register> = new\_array <register>
  - t0 = new array t1
- <register> = array\_access <register> <register>
  - t0 = array\_access t1 t2
- array\_set <register> <register> <register>
  - array\_set t0 t1 t2

#### Classes:

- <register> = new\_class <type\_name>
  - t0 = new\_array Base
- <register> = field\_access <register> <field\_name>
  - t0 = field access t1 name
- field\_set <register> <field\_name> <register>
  - field set t0 name t2
- virtual\_call <register> <method\_name> <args>
  - virtual\_call t1 foo
- <register> = virtual\_call <register> <method\_name> <args>
  - t0 = virtual\_call t1 foo t20, t21

# IR Example

```
int foo(int x, int y) {
  int z = x + y;
  int w = z + 1;
  return w;
}
```

```
t1 = x
t2 = y
t3 = add t1, t2
z = t3
t4 = z
t5 = 1
t6 = add t4, t5
w = t6
```

# Translating AST to IR

- Input: AST
- Output: List of IR instructions
- Done using **AST visitor**

Basic algorithm:

```
visit(node):

instlist_1, reg_1 = visit(node.child_1)

...

instlist_n, reg_n = visit(node.child_n)

instlist = assemble(instlist_1, ..., instlist_n)

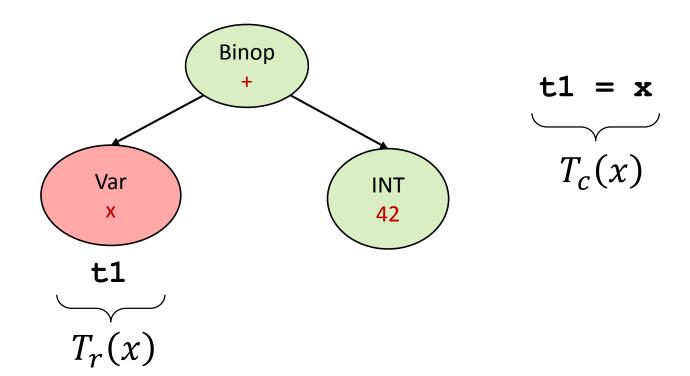
t_{new} = compute(t_1, ..., t_n)

return\ instlist, t_{new}
```

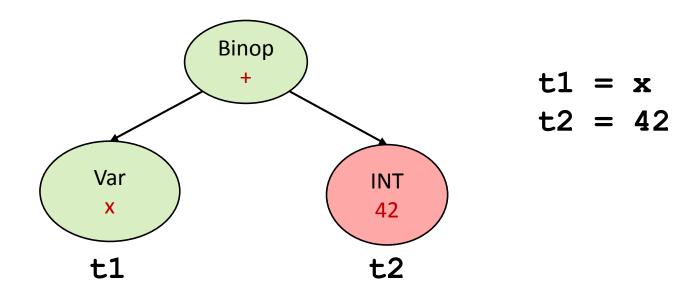
For an AST node *e* we define:

- $T_c(e)$ 
  - The generated instructions (code)
- $T_r(e)$ 
  - The register holding the result of the computation

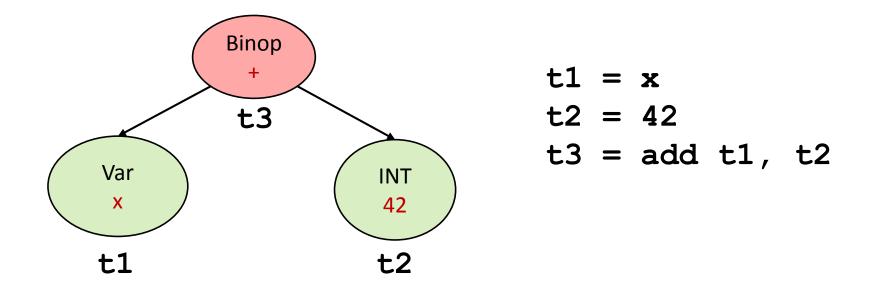
For x + 42:

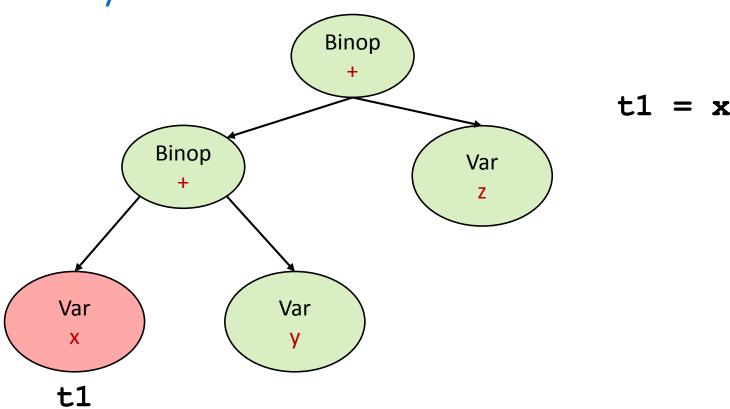


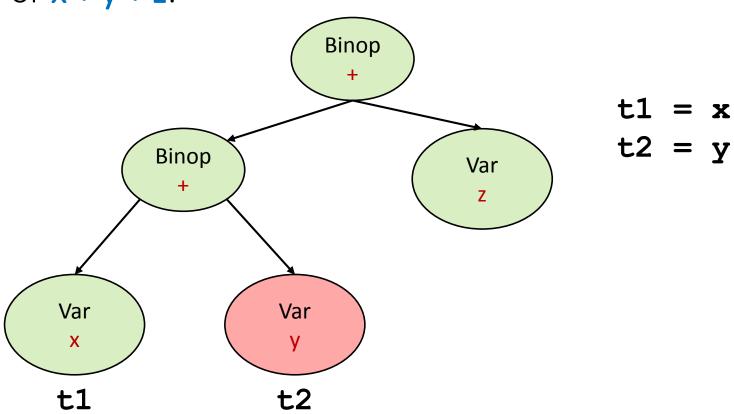
For x + 42:

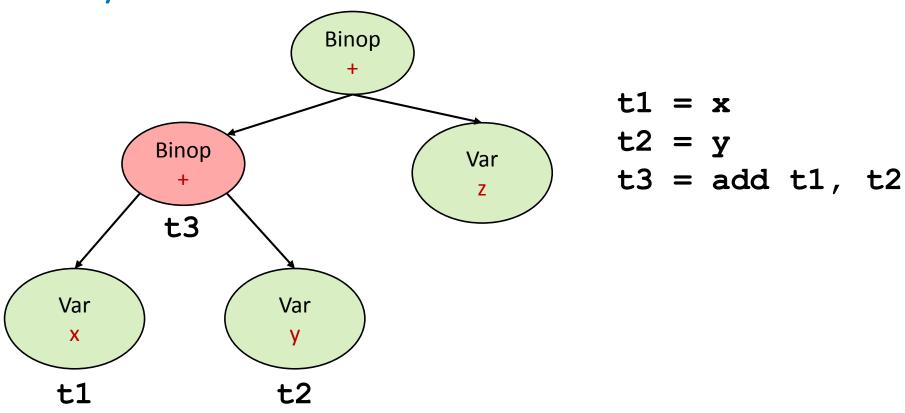


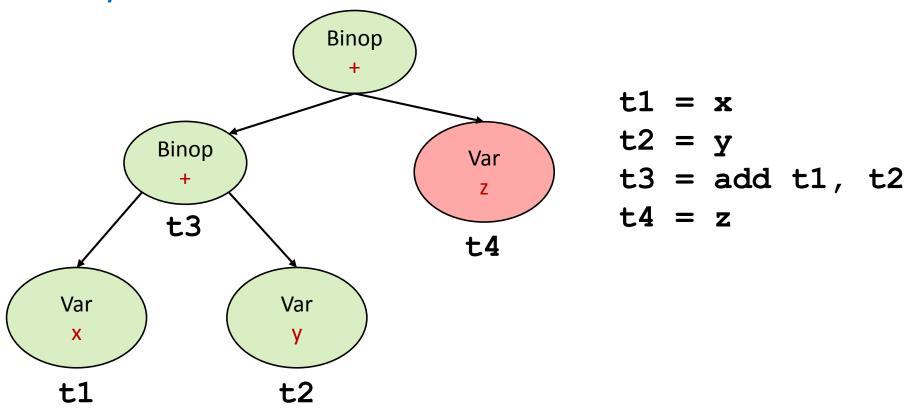
For x + 42:

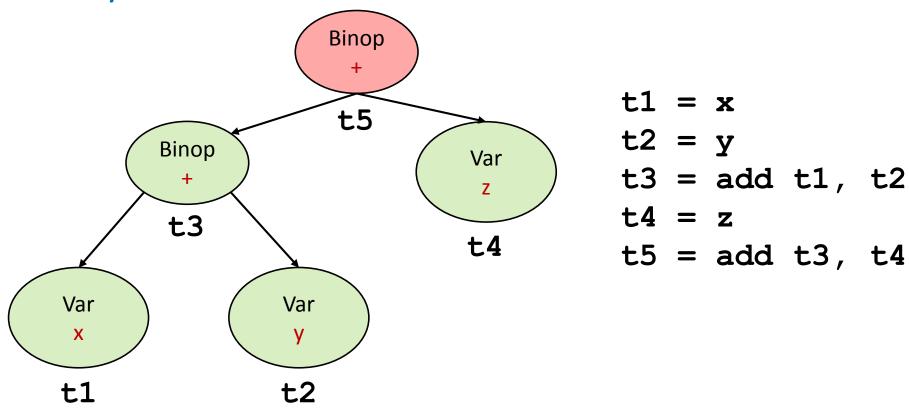












For  $e_1 == e_2$ :

$$T_c(e_1)$$
 { t1 = ...

 $T_c(e_2)$  { t2 = ...
 t3 = 1
 beq t1, t2, end\_label
 unique register t3 = 0
 end\_label:
 unique label

For a == b + 1:

```
t1 = a
t2 = b
t3 = 1
t4 = add t2, t3
t5 = 1
beq t1, t4, end_label
t5 = 0
end_label:
```

For  $e_1$  and  $e_2$ :

```
T_c(e_1) { t1 = ...
t3 = 0
T_r(e_1) beq t1, t3, end_label
T_c(e_2) { t2 = ...
t3 = and t1, t2
T_r(e_2) end label:
```

For  $e_1$  or  $e_2$ :

$$T_c(e_1)$$
 { t1 = ... t3 = 1 }  $T_r(e_1)$  beq t1, t3, end\_label  $T_c(e_2)$  { t2 = ... t3 = or t1, t2 }  $T_r(e_2)$  end\_label:

### Translating Statements

For  $f(e_1, e_2, ...)$ : (as rvalue)

```
T_c(e_1) = \dots
T_c(e_2) = \dots
t3 = call f, t1, t2, \dots
```

#### Translating Statements

For func(2, x + 1):

```
t1 = 2

t2 = x

t3 = 1

t4 = add t2, t3

t5 = call func, t1, t4
```

For  $new\ type[e]$ :

$$T_c(e)$$
 t1 = ...

 $T_r(e_1)$  t2 = new\_array t1

For new int[k+1]:

```
t1 = k
t2 = 1
t3 = add t1, t2
t4 = new_array t3
```

For  $e_1[e_2]$ :

$$T_c(e_1)$$
  $=$  ...

 $T_r(e_1)$ 
 $T_c(e_2)$   $=$  ...

 $T_c(e_2)$   $=$  ...

 $T_r(e_2)$   $=$  array\_access t1, t2

For **x**[**z+1**]:

```
t1 = x
t2 = z
t3 = 1
t4 = add t2, t3
t5 = array_access t1, t4
```

For *new type*:

```
t1 = new_class type
```

For new Point:

```
t1 = new_class Point
```

For e.f:

$$T_c(e)$$
 { t1 = ...  
 $T_r(e)$  t2 = field\_access t1, f

For **x**[3].foo:

```
t1 = x
t2 = 3
t3 = array_access t1, t2
t4 = field_access t3, foo
```

## Translating Basic Block

For  $s_1; s_2; ...:$ 

$$T_c(s_1) \\ T_c(s_2)$$

•••

For  $if(e) \{s\}$ :

```
T_c(e) = 1
T_r(e) = 1
T_r(e) = 1
beq t1, 0, end_label
T_c(s) = 1
end_label:
```

```
For if (x * y) \{ z = 0; \}:
```

```
t1 = x
t2 = y
t3 = mul t1, t2
beq t3, 0, end_label
t4 = 0
z = t4
end label:
```

For  $if(e) \{s_1\} else \{s_2\}$ :

```
T_c(e) = \underbrace{\begin{array}{c} \dots \\ \text{t1} = \dots \end{array}}
        /beq t1, 0, false_label
T_c(s_1) - \cdots
             br end label
              false label:
T_c(s_2)
             end label:
```

```
For if (w) \{z = 0; \} else \{z = 100; \}:
                t1 = w
               beq t1, 0, false label
                t2 = 0
                z = t2
                br end label
                false label:
                t3 = 100
                z = t3
                end label:
```

For while (e)  $\{s\}$ :

For while (z/x) { }:

```
cond_label:
t1 = z
t2 = x
t3 = div t1, t2
beq t3, 0, end_label
br cond_label
end_label:
```

For  $f(e_1, e_2, ...)$ :

```
T_c(e_1) = \dots
T_c(e_2) = \dots
call f, t1, t2, \dots
```

For func(2, x + 1):

```
t1 = 2
t2 = x
t3 = 1
t4 = add t2, t3
call func, t1, t4
```

For return e:

$$T_c(e) = \begin{cases} \vdots \\ \text{t1} = \ldots \end{cases}$$
return t1

For return w \* 3:

```
t1 = w
t2 = 3
t3 = mul t1, t2
return t3
```

For  $e_1[e_2] = e_3$ :

$$T_c(e_1) = \begin{cases} \vdots \\ t_1 \end{cases}$$

$$T_c(e_2) = \begin{cases} \vdots \\ t_2 = \end{cases}$$

$$T_c(e_3) = \begin{cases} \vdots \\ t_3 = \end{cases}$$

For arr[0] = x+1:

```
t1 = arr
t2 = 0
t3 = x
t4 = 1
t5 = add t3, t4
field_set t1, t2, t5
```

For o.f = e:

```
T_c(o) = \begin{cases} \vdots \\ \text{t1} \end{cases}
T_c(e) = \begin{cases} \vdots \\ \text{t2} = \vdots \\ \text{field\_set t1, f, t3} \end{cases}
```

For obj.flag = 7:

```
t1 = obj
t2 = 7
field_set t1, flag, t2
```

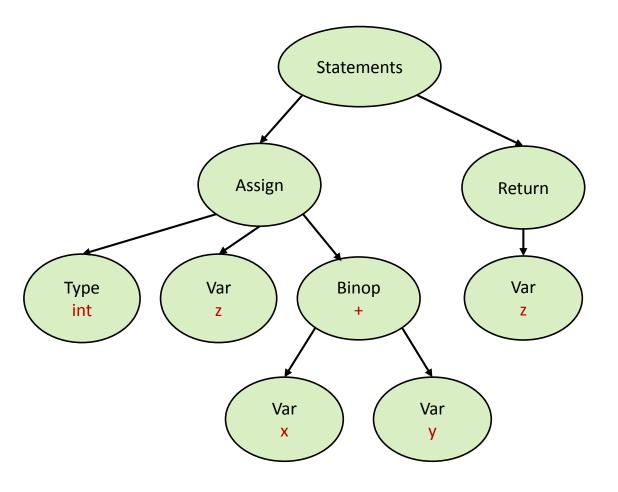
For  $o.f(e_1, e_2, ...)$ :

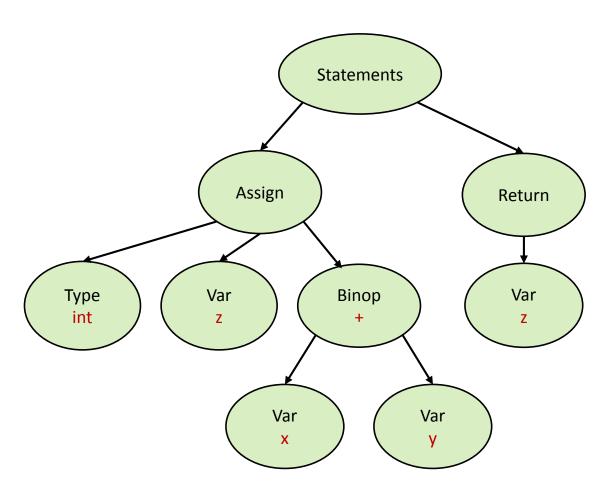
```
T_c(o) = \begin{cases} \vdots \\ \text{t1} \end{cases}
T_c(e_1) = \begin{cases} \vdots \\ \text{t2} = \ldots \end{cases}
T_c(e_2) = \begin{cases} \vdots \\ \text{t3} = \ldots \end{cases}
\vdots \\ \text{virtual call t1 f t2, t3, ...}
```

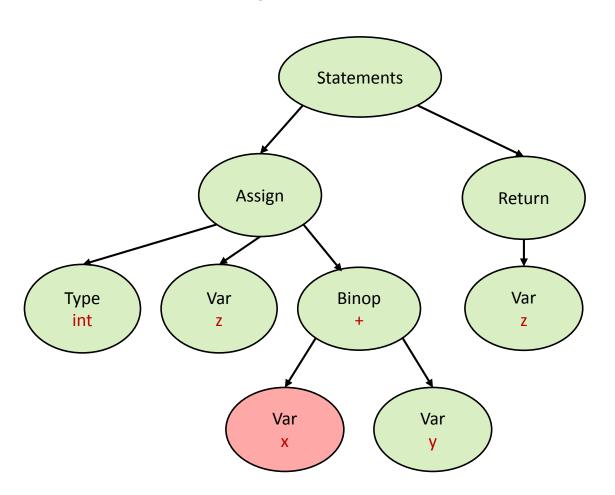
For obj.bar(2, x + 1):

```
t1 = obj
t2 = 2
t3 = x
t4 = 1
t5 = add t3, t4
virtual_call t1, bar, t2, t5
```

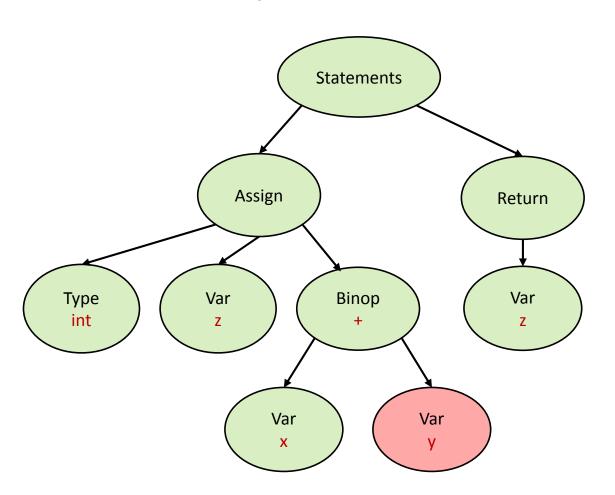
```
int f(int x, int y) {
  int z = x + y;
  return z;
}
```



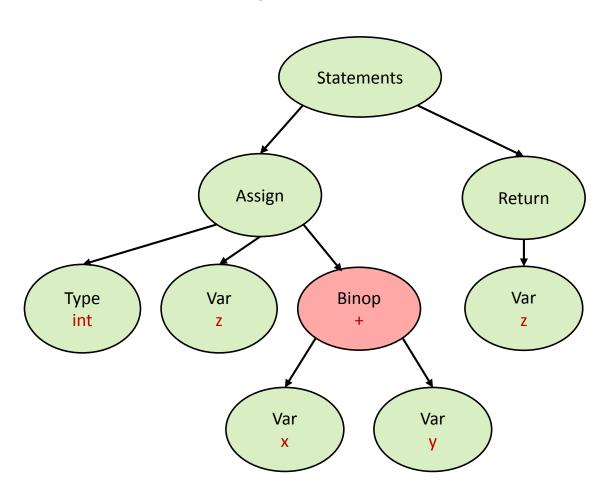




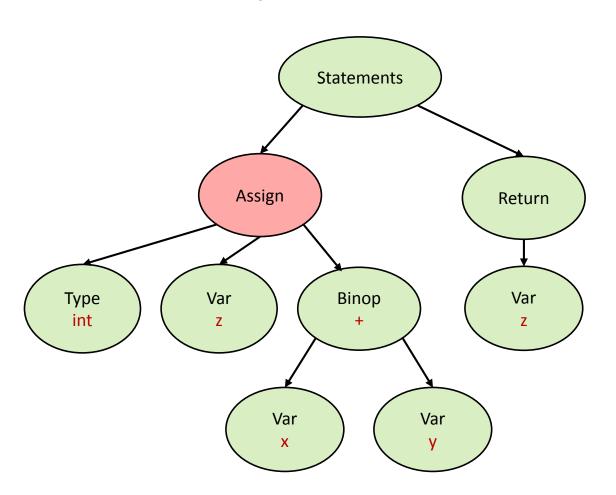
t1 = x

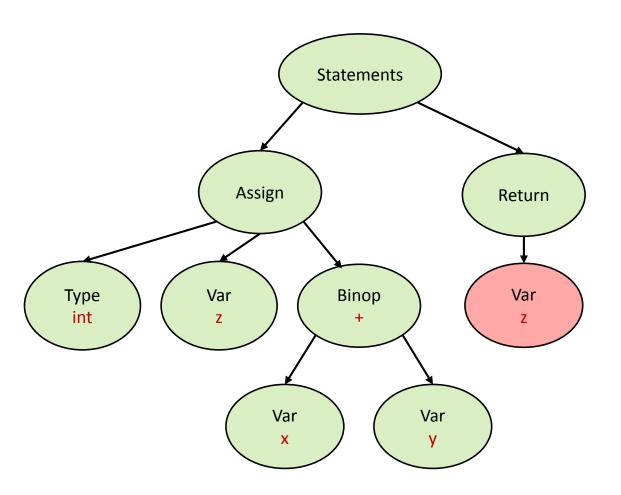


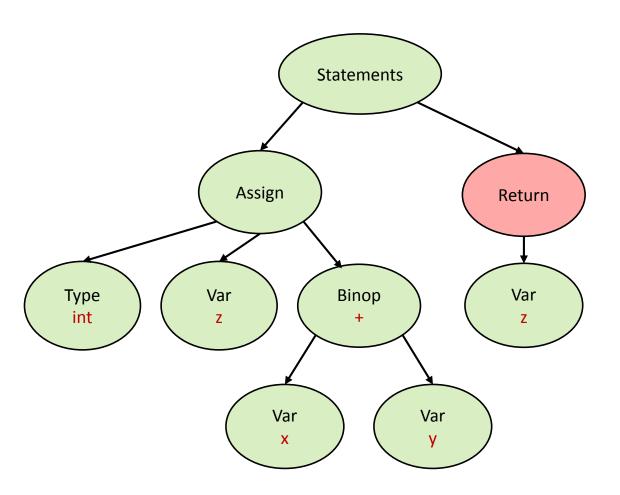
t1 = x t2 = y



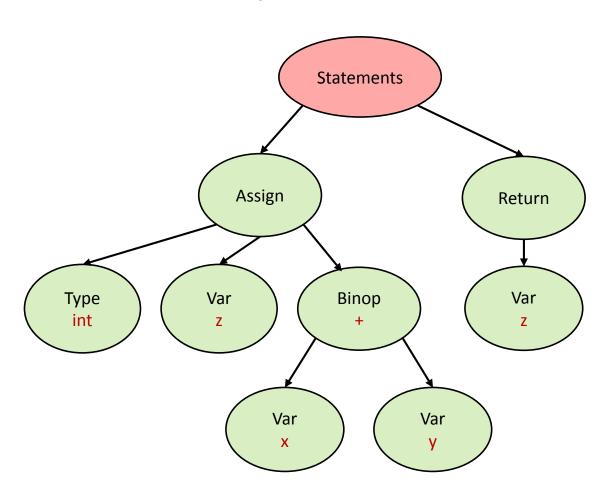
```
t1 = x
t2 = y
t3 = add t1, t2
```





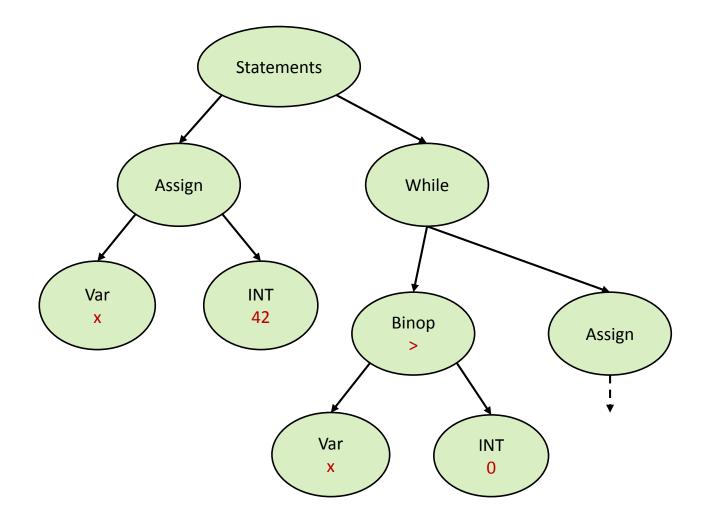


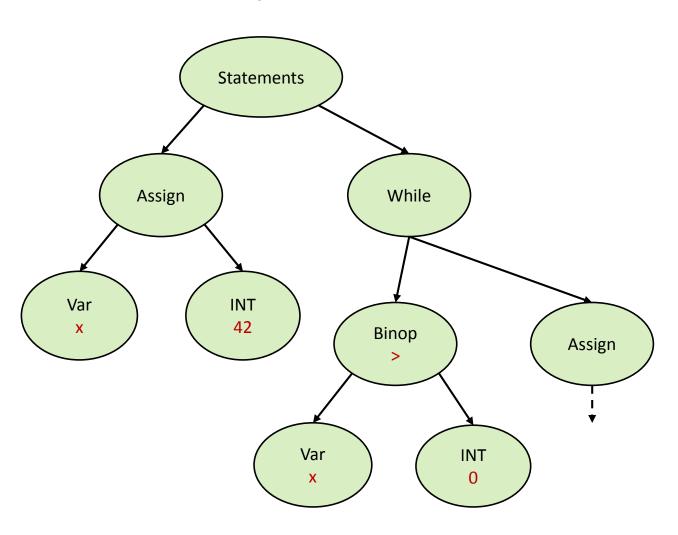
```
t1 = x
t2 = y
t3 = add t1, t2
z = t3
t4 = x
return t4
```

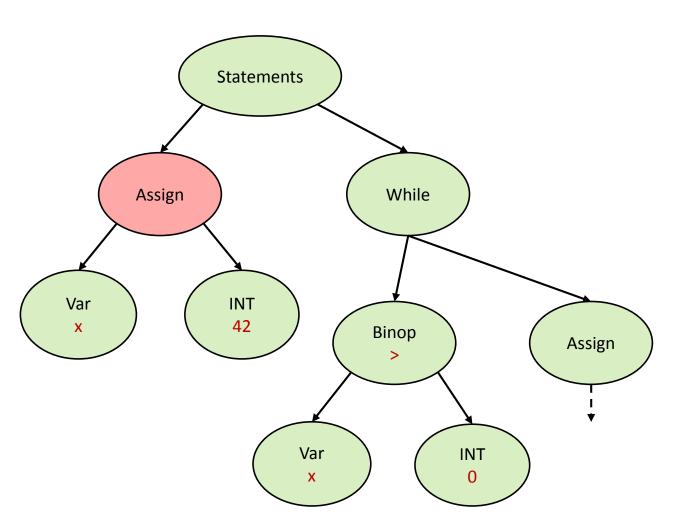


```
t1 = x
t2 = y
t3 = add t1, t2
z = t3
t4 = x
return t4
```

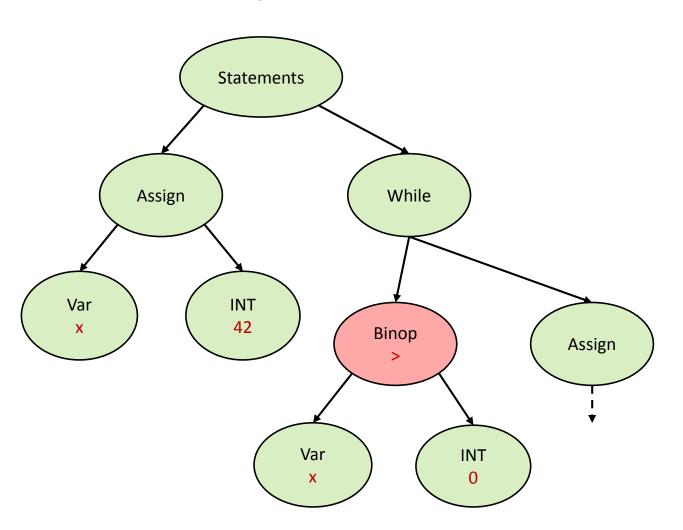
```
x = 42;
while (x > 0) {
  x = x - 1;
}
```

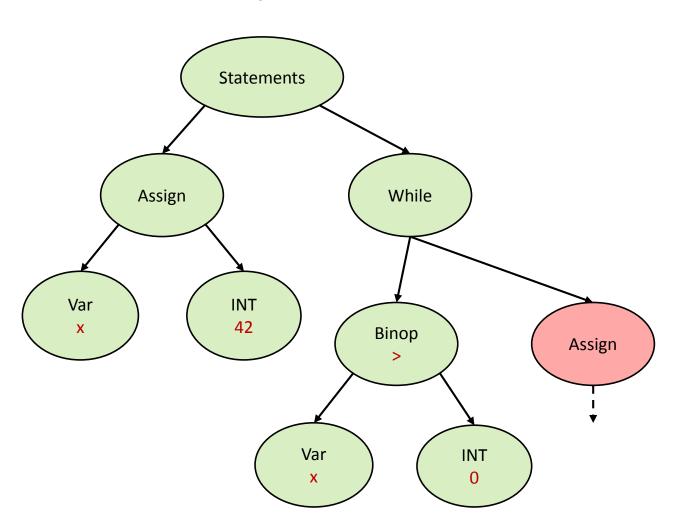


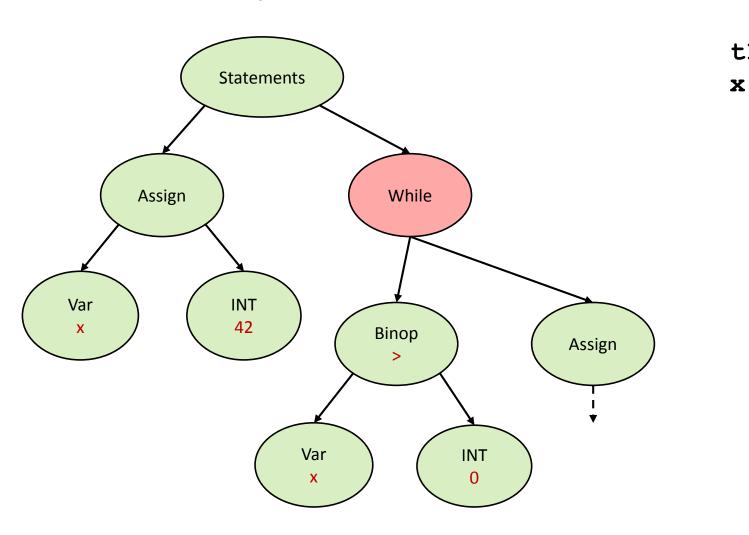




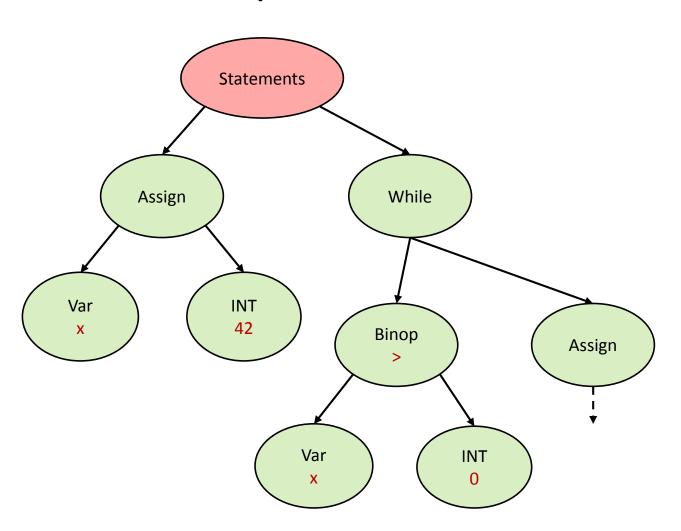
$$t1 = 42$$
$$x = t1$$







```
t1 = 42 cond_label:
x = t1
            t2 = x
           bgt t2, t3, cmp_label:
           cmp_label:
           beq t4, 0, end label
           t5 = x
           t7 = sub t5, t6
           x = t7
           br cond_label
            end_label:
```



```
t1 = 42
x = t1
cond label:
t2 = x
t3 = 0
t4 = 0
bgt t2, t3, cmp_label:
t4 = 1
cmp_label:
beq t4, 0, end_label
t5 = x
t6 = 1
t7 = sub t5, t6
x = t7
br cond_label
end_label:
```