

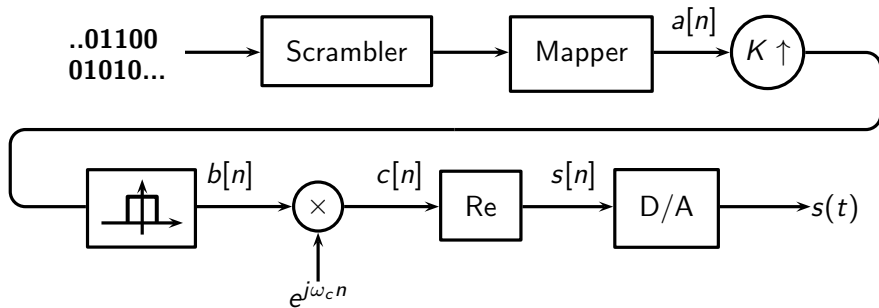
COM303: Digital Signal Processing

Lecture 23: Digital Communication Systems (II)

- ▶ QAM receiver design
- ▶ ADSL

receiver design

QAM transmitter, final design



It's a dirty job...

but a receiver has to do it:

- ▶ propagation delay
- ▶ channel effects
- ▶ interference
- ▶ clock drift

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- ▶ propagation delay → handshake and delay estimation
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- ▶ propagation delay → handshake and delay estimation
- ▶ channel effects → adaptive equalization
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- ▶ propagation delay → handshake and delay estimation
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- ▶ interference → line probing
- ▶ clock drift

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- ▶ propagation delay → handshake and delay estimation
- ▶ channel effects → adaptive equalization
- ▶ interference → line probing
- ▶ clock drift → timing recovery

A blast from the past

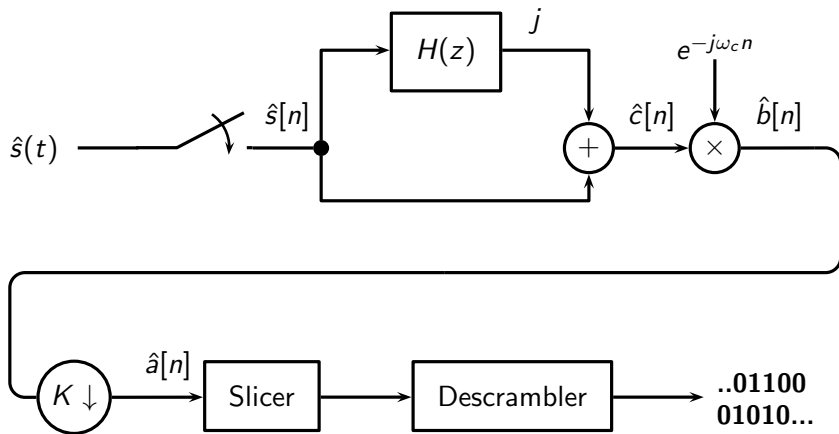
Play

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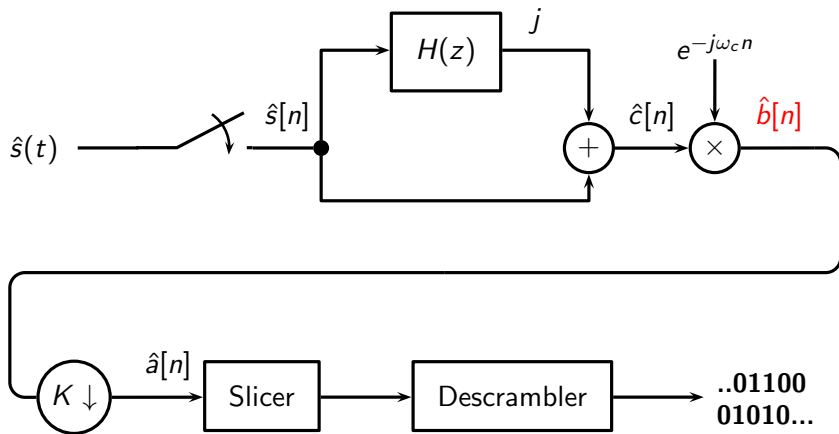
Play

- ▶ a sound familiar to anyone who's used a modem or a fax machine
- ▶ what's going on here?

Remember the (simplified) receiver



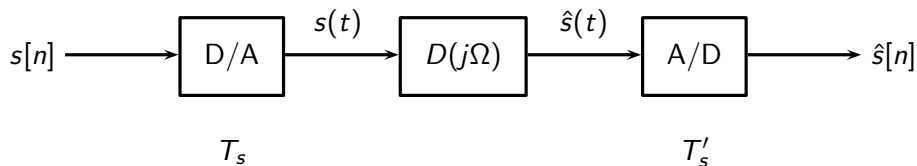
Remember the (simplified) receiver



Visually, in slow motion, plotting $b[n]$

if $\hat{s}[n] = \cos((\omega_c + \omega_0)n)$ then $\hat{b}[n] = e^{j\omega_0 n}$

The main problems



- ▶ noise
- ▶ propagation delay
- ▶ channel distortion $D(j\Omega)$
- ▶ different clocks ($T'_s \neq T_s$)

Delay compensation

Assume the channel is a simple delay: $\hat{s}(t) = s(t - d) \Rightarrow D(j\Omega) = e^{-j\Omega d}$

- ▶ channel introduces a delay of d seconds
- ▶ we can write $d = (L + \tau)T_s$ with $L \in \mathbb{N}$ and $|\tau| < 1/2$
- ▶ L is called the *bulk delay*
- ▶ τ is the fractional delay

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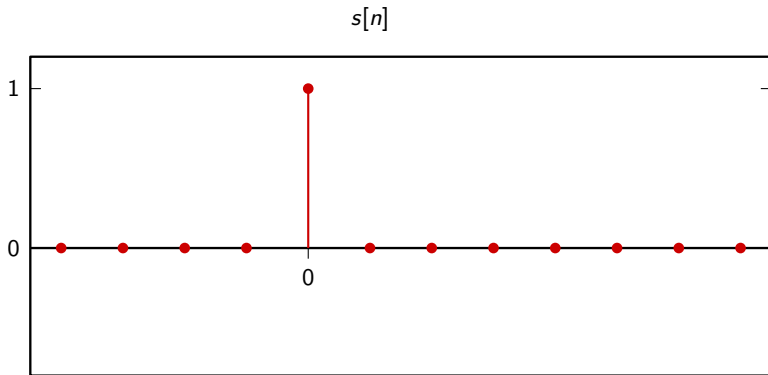
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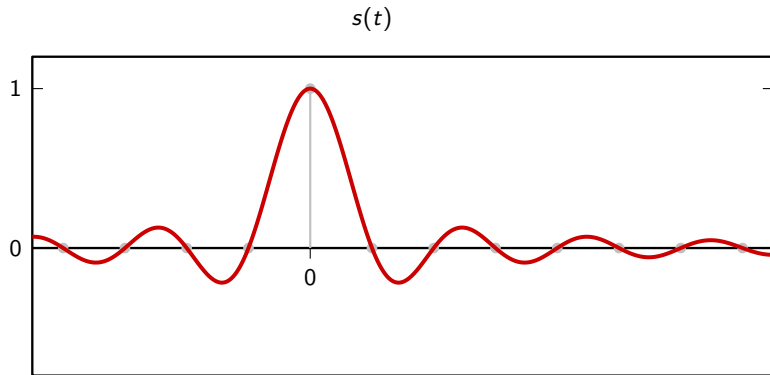
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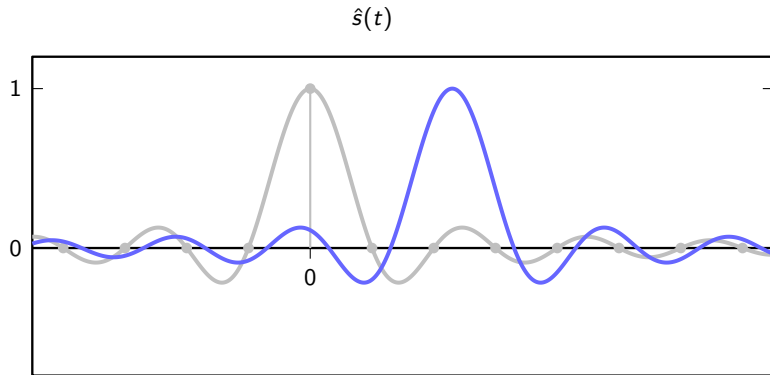
Estimating the bulk delay ($T_s = 1$)



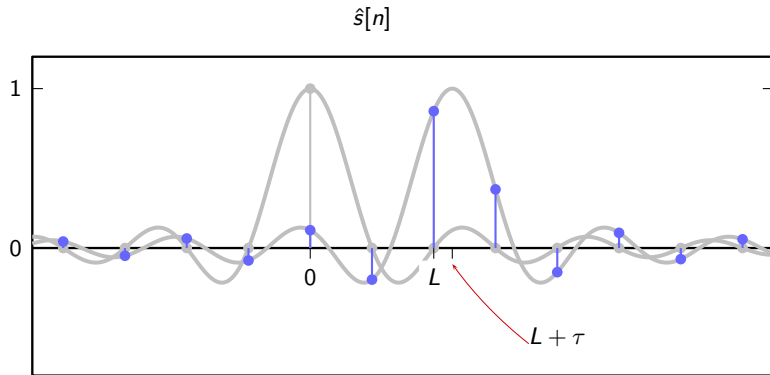
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Estimating the fractional delay

- ▶ transmit $b[n] = e^{j\omega_0 n}$ (i.e. $s[n] = \cos((\omega_c + \omega_0)n)$)
- ▶ receive $\hat{s}[n] = \cos((\omega_c + \omega_0)(n - L - \tau))$
- ▶ after demodulation and bulk delay offset:

$$\hat{b}[n] = e^{j\omega_0(n-\tau)}$$

- ▶ multiply by known frequency

$$\hat{b}[n] e^{-j\omega_0 n} = e^{-j\omega_0 \tau}$$

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Compensating for the fractional delay

- ▶ $\hat{s}[n] = s((n - \tau)T_s)$ (after offsetting bulk delay)
- ▶ we need to compute subsample values
- ▶ in theory, compensate with a sinc fractional delay $h[n] = \text{sinc}(n + \tau)$
- ▶ in practice, use local Lagrange approximation

$$x_L(n; t) = \sum_{k=-N}^N x[n - k] L_k^{(N)}(t)$$

$$L_k^{(N)}(t) = \prod_{\substack{i=-N \\ i \neq k}}^N \frac{t - i}{k - i} \quad k = -N, \dots, N$$

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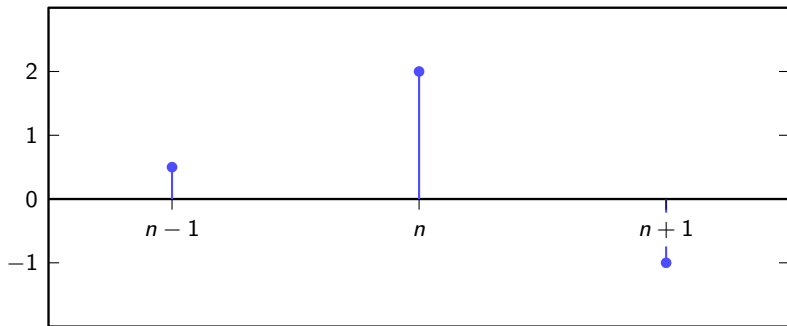
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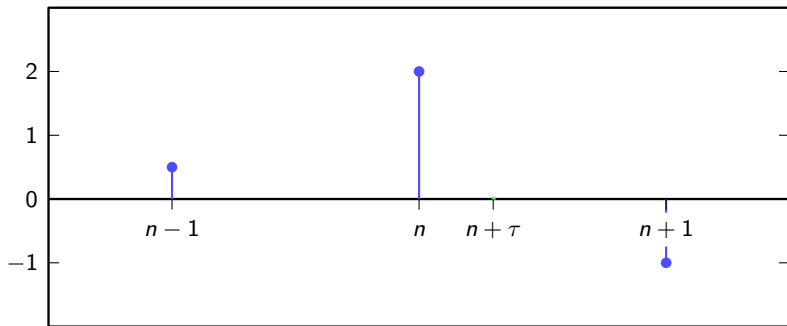
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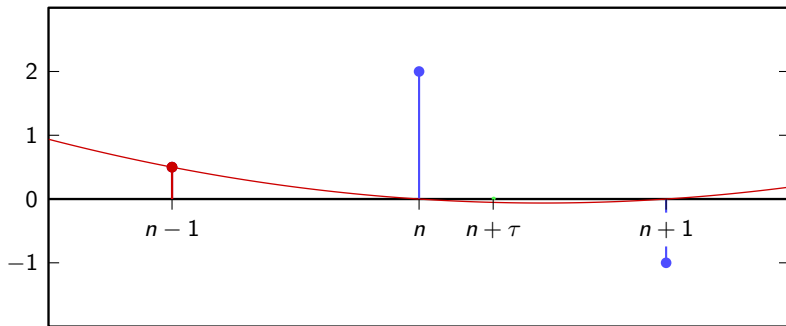
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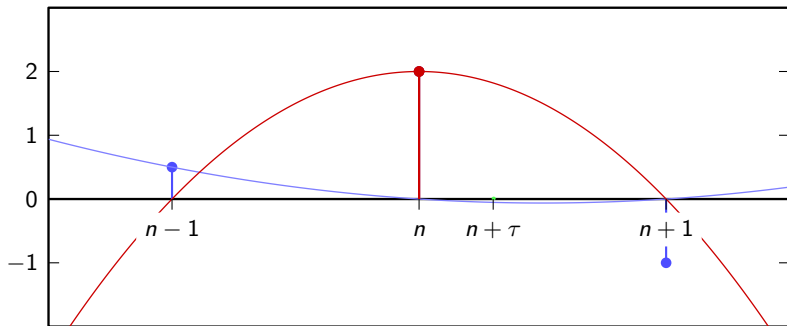
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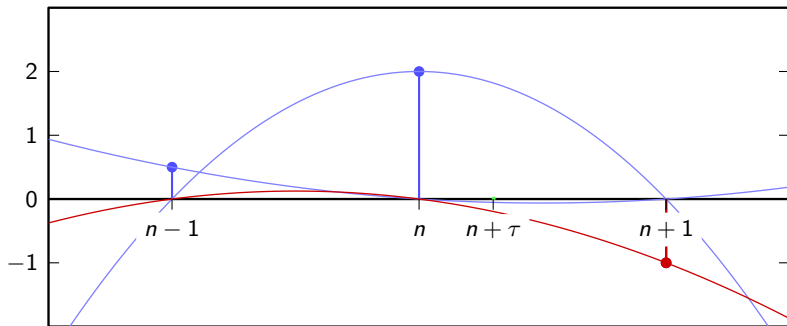
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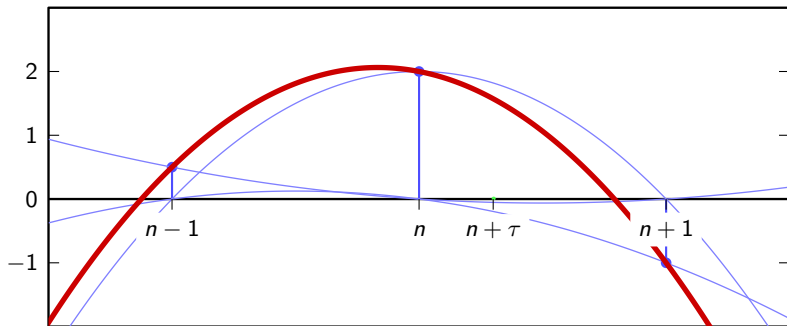
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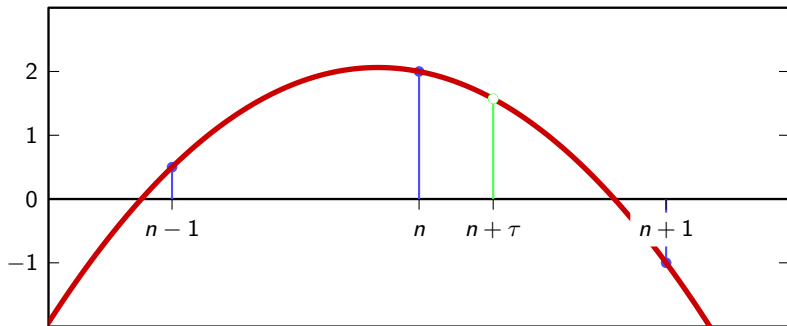
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Lagrange interpolation as an FIR

- ▶ $x(n + \tau) \approx x_L(n; \tau)$
- ▶ $x_L(n; \tau) = \sum_{k=-N}^N x[n - k] L_k^{(N)}(\tau)$
- ▶ define $d_\tau[k] = L_k^{(N)}(\tau)$, $k = -N, \dots, N$
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Example ($N = 1$, second order approximation)

$$L_{-1}^{(1)}(t) = t \frac{t-1}{2}$$

$$L_0^{(1)}(t) = (1-t)(1+t)$$

$$L_1^{(1)}(t) = t \frac{t+1}{2}$$

Example ($N = 1$, second order approximation)

$$d_{0.2}[n] = \begin{cases} -0.08 & n = -1 \\ 0.96 & n = 0 \\ 0.12 & n = 1 \\ 0 & \text{otherwise} \end{cases}$$

Delay compensation algorithm

- ▶ estimate the delay τ
- ▶ compute the $2N + 1$ Lagrangian coefficients
- ▶ filter with the resulting FIR

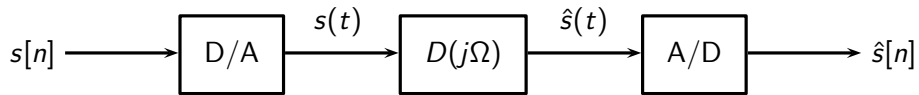
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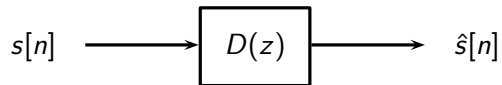
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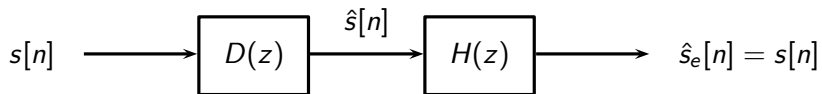
Compensating for the distortion



Compensating for the distortion



Example: adaptive equalization



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- ▶ in theory, $H(z) = 1/D(z)$
- ▶ but we don't know $D(z)$ in advance
- ▶ $D(z)$ may change over time

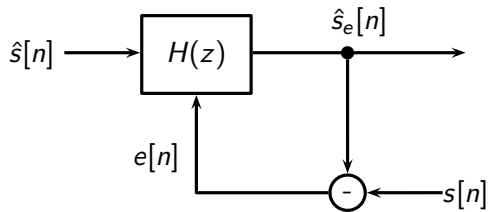
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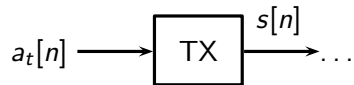
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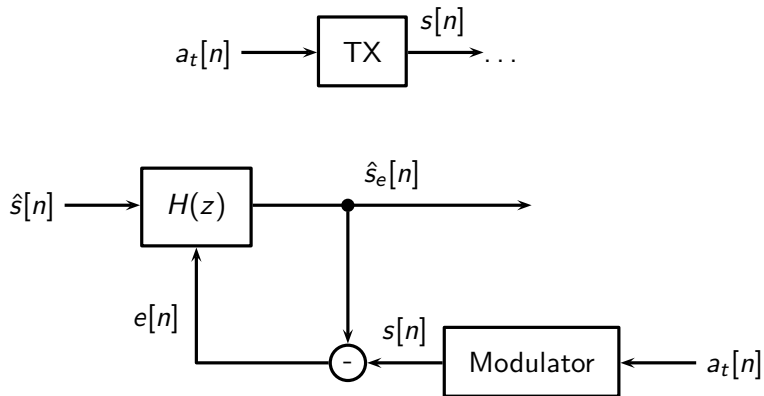
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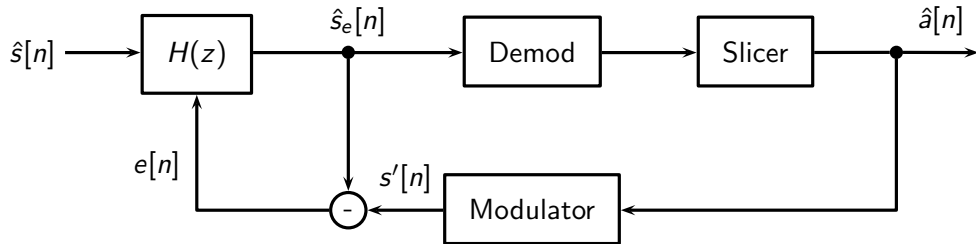
Adaptive equalization: bootstrapping via a training sequence



Adaptive equalization: bootstrapping via a training sequence



Adaptive equalization: online mode



Adaptive equalization: the LMS algorithm

FIR equalization:

$$\hat{s}_e[n] = \sum_{k=0}^{N-1} h_n[k] \hat{s}[n-k]$$

$$e[n] = \hat{s}_e[n] - s[n]$$

adapting the coefficients:

$$h_{n+1}[k] = h_n[k] + \alpha e[n] x[n-k], \quad k = 0, 1, \dots, N-1$$

So much more to do...

- ▶ how do we compensate for differences in clocks?
- ▶ how do we recover from interference?
- ▶ how do we improve resilience to noise?

advanced topics in communication system design

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advanced topics in communication system design

ADSL

Overview:

- ▶ Channel
- ▶ Signaling strategy
- ▶ Discrete Multitone Modulation (DMT)

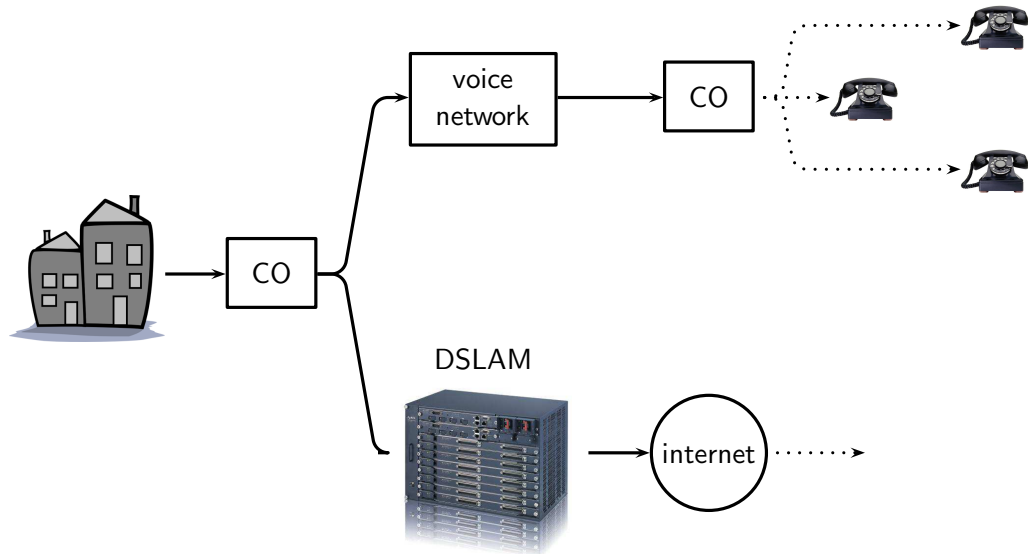
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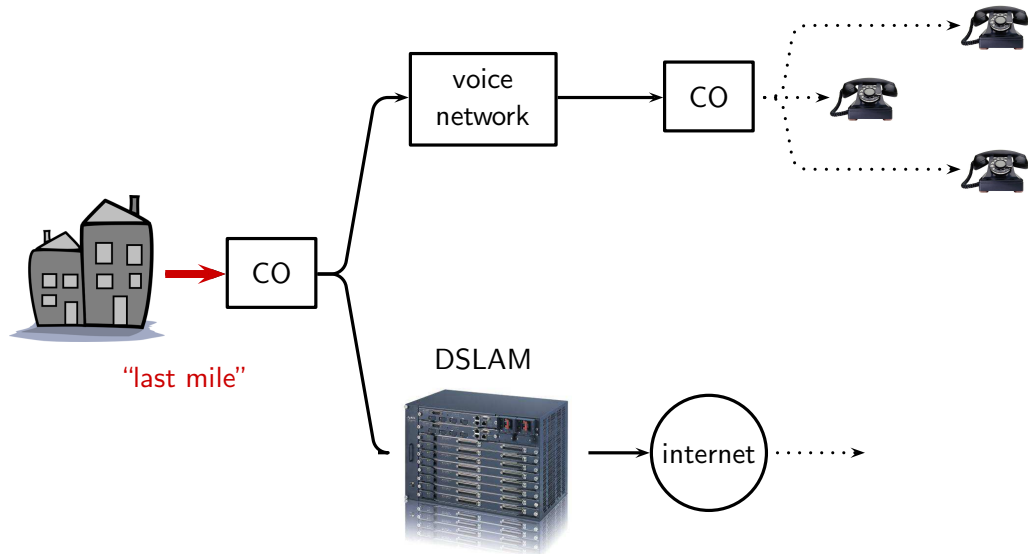
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The telephone network today



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The last mile

- ▶ copper wire (twisted pair) between home and nearest CO
- ▶ very large bandwidth (well over 1MHz)
- ▶ very uneven spectrum: noise, attenuation, interference, etc.

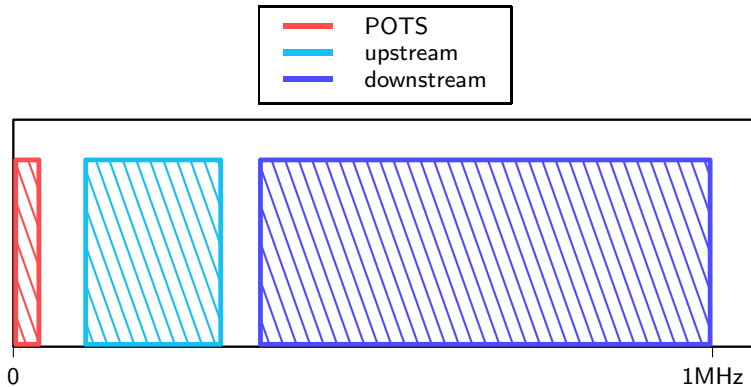
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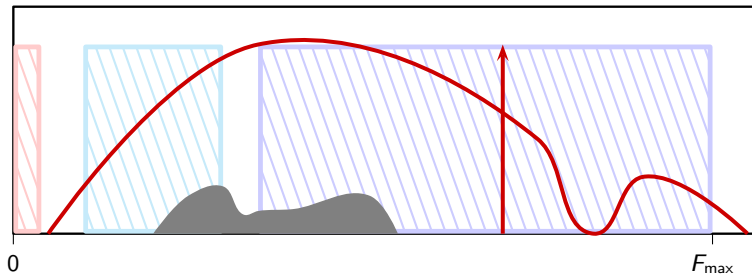
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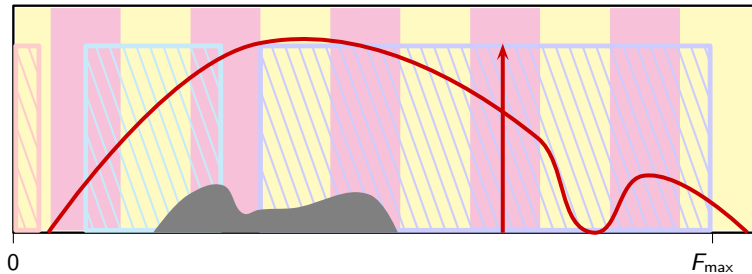
The ADSL channel



The ADSL channel



Idea: split the band into independent subchannels



Subchannel structure

- ▶ allocate N subchannels over the total positive bandwidth
- ▶ equal subchannel bandwidth $W = F_{\max}/N$
- ▶ equally spaced subchannels with center frequency kF_{\max}/N , $k = 0, \dots, N - 1$

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The digital design

- ▶ pick $F_s = 2F_{\max}$ ($F_s = 2NW = 2F_{\max}$. i.e. F_s multiple of W)
- ▶ center frequency for each subchannel $\omega_k = 2\pi \frac{kW}{F_s} = \frac{2\pi}{2N} k$
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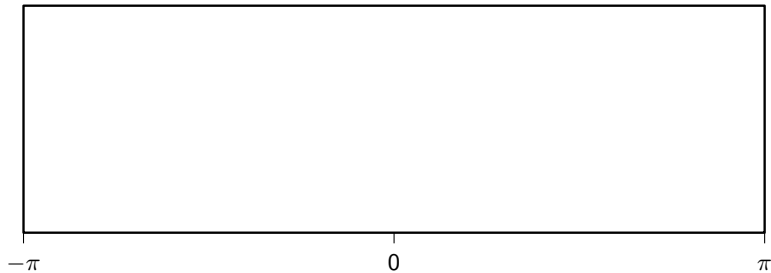
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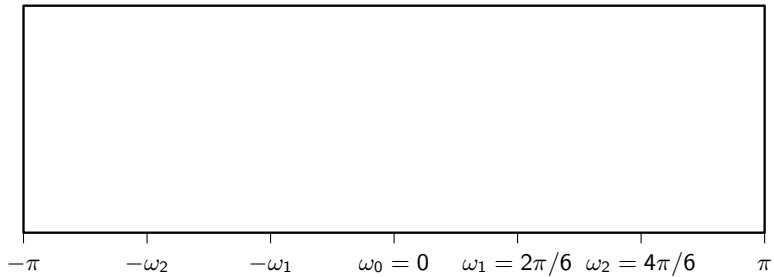
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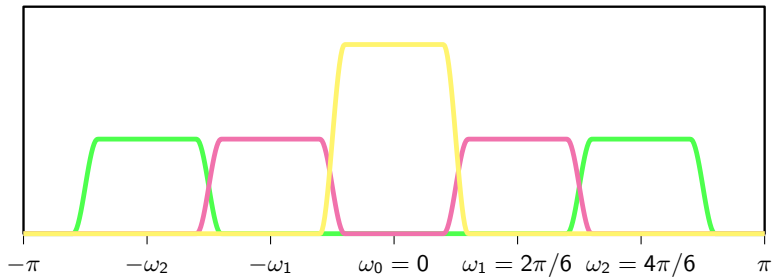
The digital design ($N = 3$)



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The digital design

- ▶ put a QAM modem on each channel
- ▶ decide on constellation size independently
- ▶ noisy or forbidden subchannels send zeros

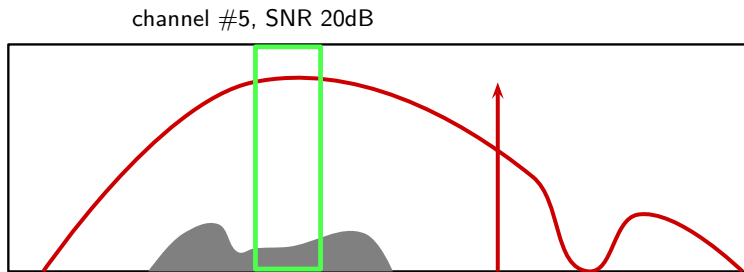
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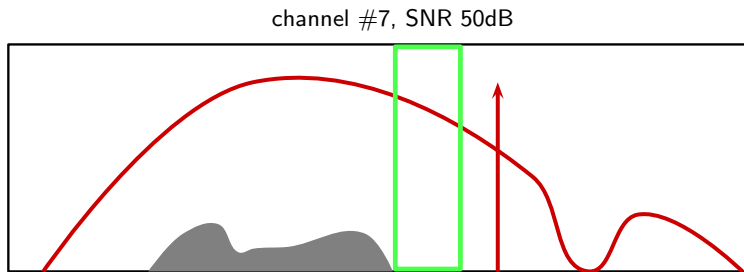
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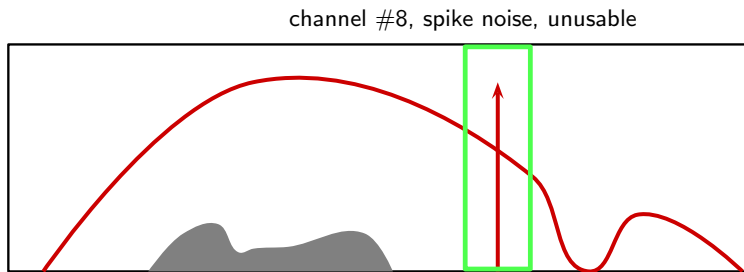
Idea: split the band into independent subchannels



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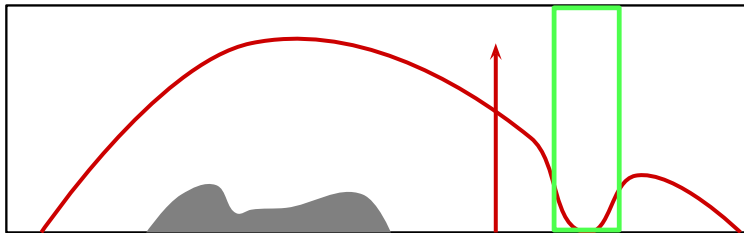


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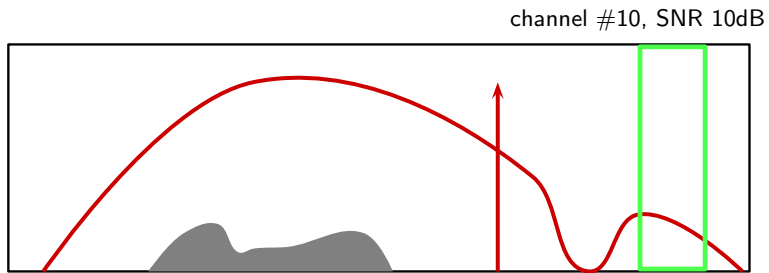


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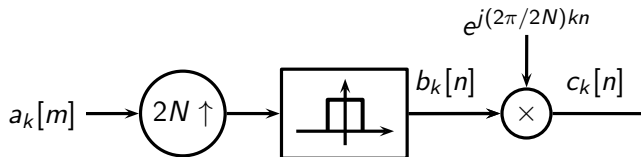
channel #9, too much attenuation, unusable



Idea: split the band into independent subchannels



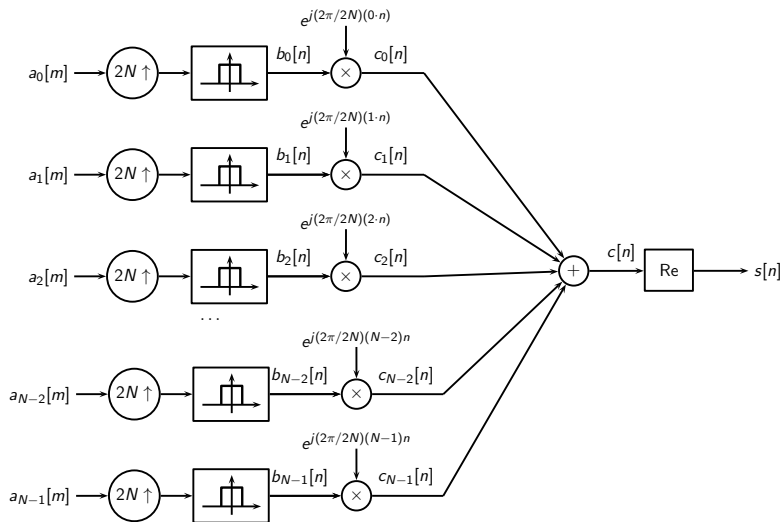
The subchannel modem



rate: W symbols/sec

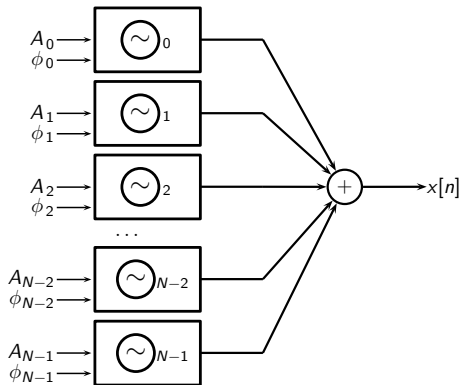
$2NW = F_s$ samples/sec

The bank of modems



If it looks familiar...

remember the DFT reconstruction formula?



- ▶ we will show that transmission can be implemented efficiently via an IFFT
- ▶ Discrete Multitone Modulation

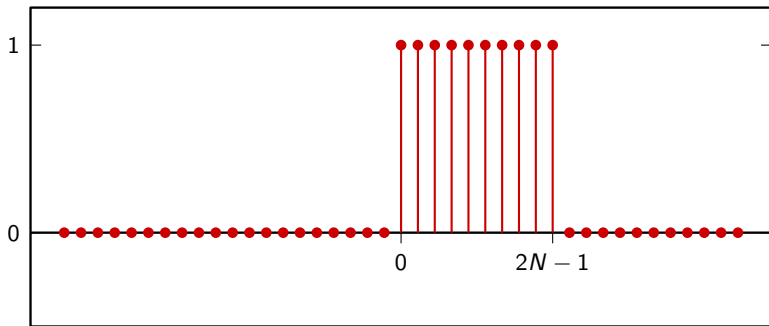
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The great ADSL trick

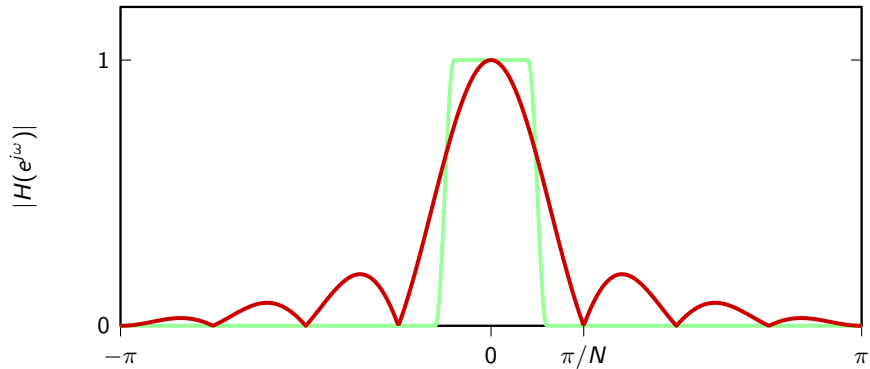
instead of using a “semi-ideal” digital interpolator (e.g. raised cosine), use a zero-order hold:

$$h[n] = \begin{cases} 1 & \text{for } 0 \leq n < 2N \\ 0 & \text{otherwise} \end{cases}$$

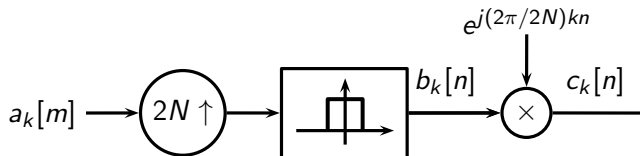
Digital ZOH: interval indicator signal



DTFT of interval signal



Back to the subchannel modem



rate: W symbols/sec

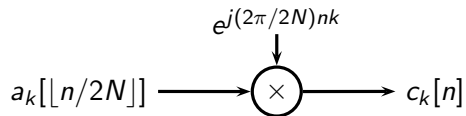
$2NW = F_s$ samples/sec

m changes every $1/W$ sec

n changes every $1/F_s = (1/W)/(2N)$

Back to the subchannel modem

by using the ZOH interpolator:

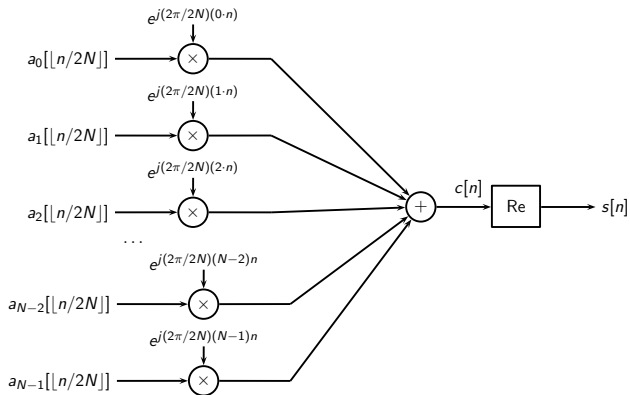


rate: W symbols/sec

$2NW = F_s$ samples/sec

n changes every $1/F_s$

The bank of modems, revisited



rate: $NW = F_{\max}$ symbols/sec

$2NW = F_s$ samples/sec

The complex output signal

$$c[n] = \sum_{k=0}^{N-1} a_k[\lfloor n/2N \rfloor] e^{j \frac{2\pi}{2N} nk}$$

for all $2N$ -long output chunks, i.e. for $2pN \leq n < 2(p+1)N$:

- ▶ $a_k[\lfloor n/2N \rfloor] = a_k[p]$ don't change
- ▶ $c[n] = 2N \cdot \text{IDFT}_{2N} \{ [a_0[p] \ a_1[p] \ \dots \ a_{N-1}[p] \ 0 \ 0 \ \dots \ 0] \} [n]$

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We can do even better!

► we are interested in $s[n] = \text{Re}\{c[n]\} = (c[n] + c^*[n])/2$

► it is easy to prove (exercise) that:

$$\text{IDFT} \{ [x_0 \ x_1 \ x_2 \ \dots \ x_{N-2} \ x_{N-1}] \}^* = \text{IDFT} \{ [x_0 \ x_{N-1} \ x_{N-2} \ \dots \ x_2 \ x_1]^* \}$$

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► therefore

$$s[n] = N \cdot \text{IDFT} \{ [2a_0[p] \ a_1[p] \ \dots \ a_{N-1}[p] \ a_{N-1}^*[p] \ a_{N-2}^*[p] \ \dots \ a_1^*[p]] \} [n]$$

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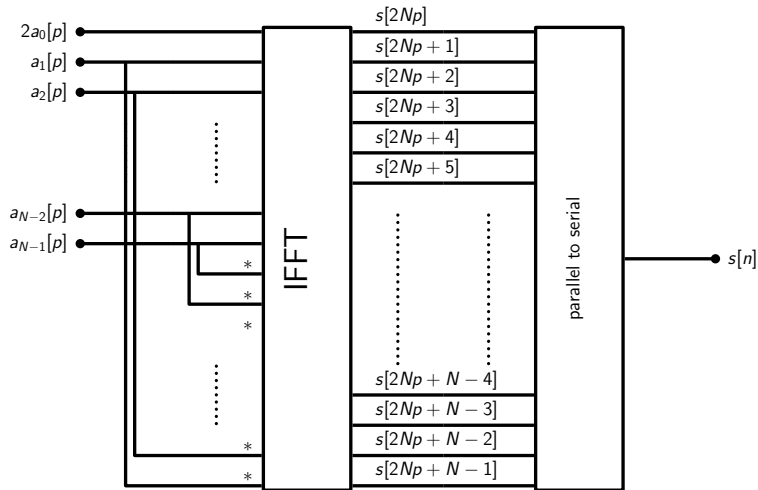
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ADSL transmitter



rate: NW symbols/sec

$2NW = F_s$ samples/sec

ADSL specs

- ▶ $F_{\max} = 1104\text{KHz}$
- ▶ $N = 256$
- ▶ each QAM can send from 0 to 15 bits per symbol
- ▶ forbidden channels: 0 to 7 (voice)
- ▶ channels 7 to 31: upstream data
- ▶ max theoretical throughput: 14.9Mbps (downstream)

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