Pre-Lab: Diodes (Lab 1) By: Danilo Zelenovic

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P1: Circuit from Figure 1 designed in Multisim

Diagram

Description automatically generated

Figure 1. Circuit from lab manual simulated in Multisim.

A picture containing graphical user interface

Description automatically generated

A screenshot of a computer

Description automatically generated with medium confidence

Chart

Description automatically generated

A screenshot of a computer

Description automatically generated

Graph P1(a). Simulated waveforms of the circuit of Figure 1.

All the above graphs plotted on the same graph:

Chart

Description automatically generated

Graphical user interface

Description automatically generated with medium confidence

Note: In the DC Sweep settings, the start value for the Vs was set to -12V, and the stop value was set to 12V, with increments of 0.01.

Graph P1(b). Voltage-current characteristic of the diode.