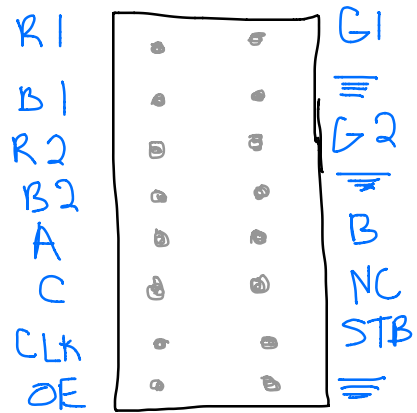
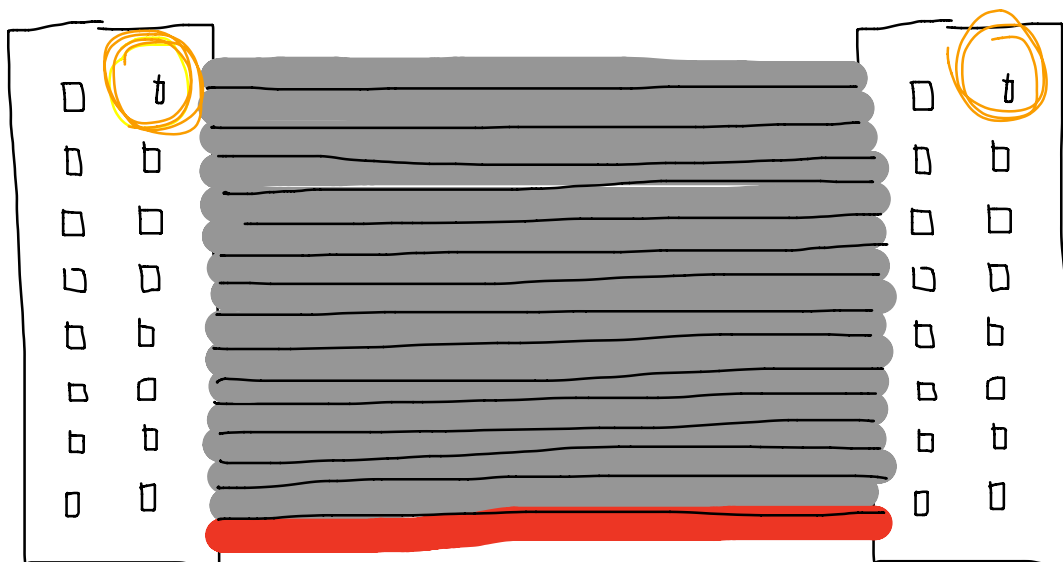


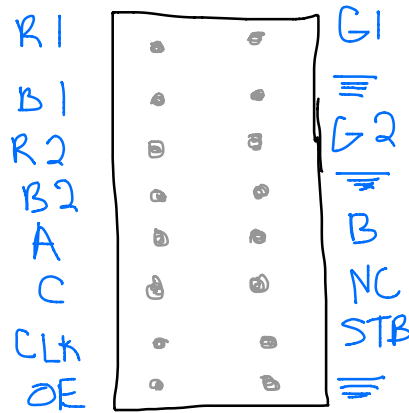
Pins  
on  
Board



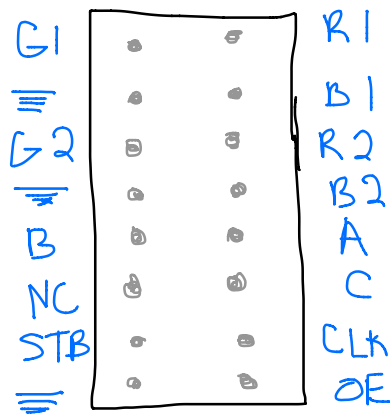
Pins on the cable  
(1:1 correlation)



Connected  
to  
board  
(facing  
down)



Exposed  
(Where we will  
be connecting)



• All  $\equiv$  must be connected to ground

• R1, G1, B1 deliver to top half of display

- R2, G2, B2 deliver to bottom half of display
- A, B, C select which two rows are lit
- CLK marks arrival of each bit of data
- OE switches LEDs off when transitioning from one row to the next

Highlighter color shows color of the wire connected to the board

Assignment Editor						
<<new>>		Filter on node names: *		Category: All		
	Status	From	To	Assignment Name	Value	Enabled
1	✓ Ok		lat	Location GPIO 033	PIN_B12	Yes
2	✓ Ok		r1	Location GPIO 018	PIN_E7	Yes
3	✓ Ok		b1	Location GPIO 020	PIN_E8	Yes
4	✓ Ok		r2	Location GPIO 022	PIN_F9	Yes
5	✓ Ok		b2	Location GPIO 024	PIN_C9	Yes
6	✓ Ok		a	Location GPIO 026	PIN_E11	Yes
7	✓ Ok		g1	Location GPIO 027	PIN_E10	Yes
8	✓ Ok		c	Location GPIO 028	PIN_C11	Yes
9	✓ Ok		g2	Location GPIO 029	PIN_B11	Yes
10	✓ Ok		clk_out	Location GPIO 030	PIN_A12	Yes
11	✓ Ok		b	Location GPIO 031	PIN_D11	Yes
12	✓ Ok		oe	Location GPIO 032	PIN_D12	Yes
13	✓ Ok		clk_in	Location CLK 50	PIN_R8	Yes
14	✓ Ok		rst_n	Location KEY 0	PIN_J15	Yes

# Parallel Ports

JP1      0x100000060

JP2      0x10000070

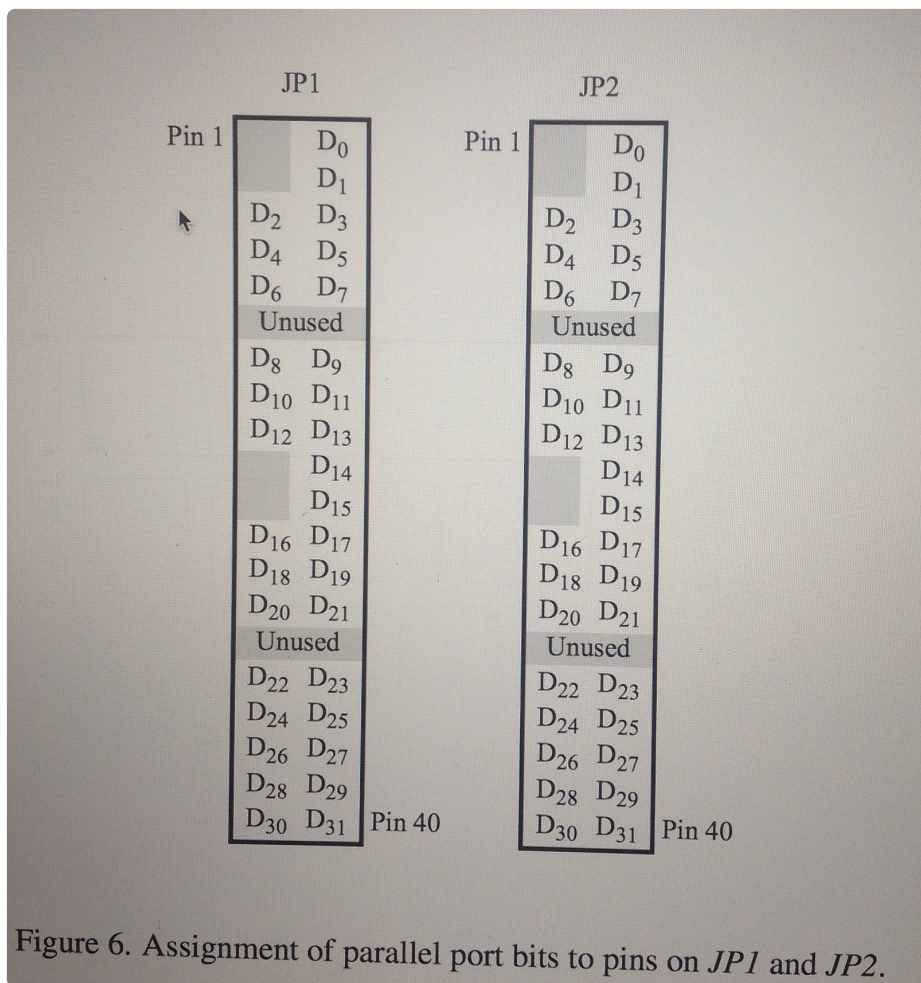


Figure 6. Assignment of parallel port bits to pins on JP1 and JP2.