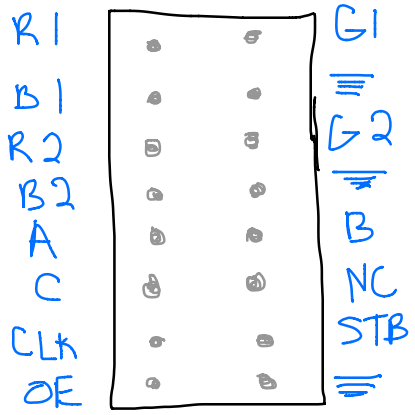
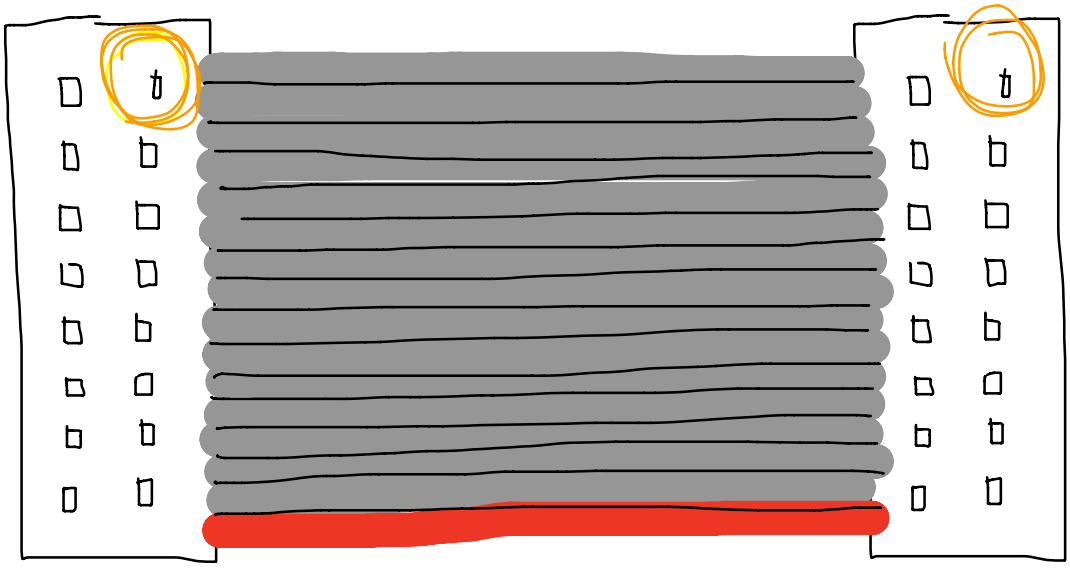
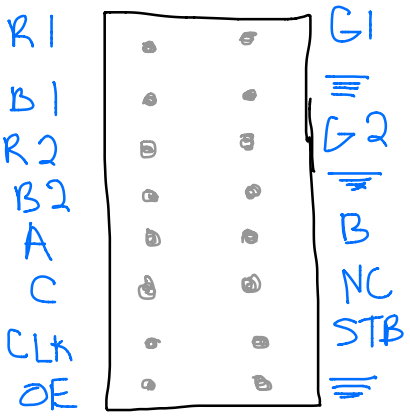
Pins on Board



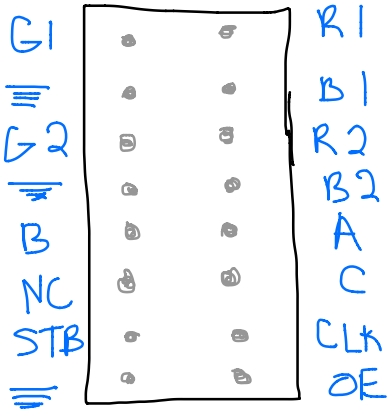
Pins on the cable (1:1 correlation)



Connected to board (facing down)



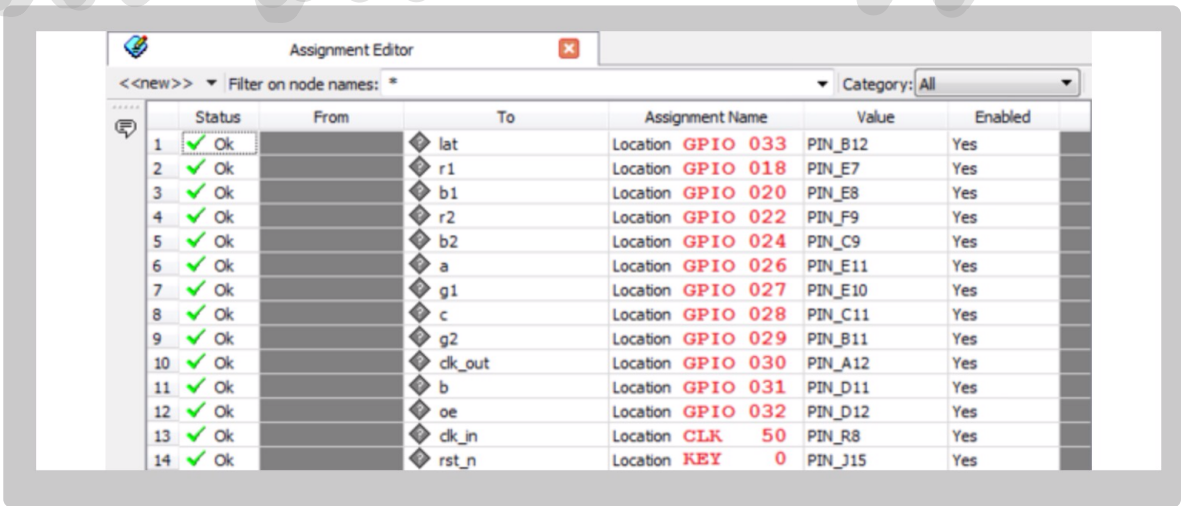
Exposed (Where we will be connecting)



- All ⏚ must be connected to ground
- R1, G1, B1 deliver to top half of display

- R2, G2, B2 deliver to bottom half of display
- A, B, C Select which two rows are lit
- CLK marks arrival of each bit of data
- OE switches LEDs off when transitioning from one row to the next

Highlighter color shows color of the wire connected to the board



Parallel Ports

JP1 0x10000060

JP2 0x10000070

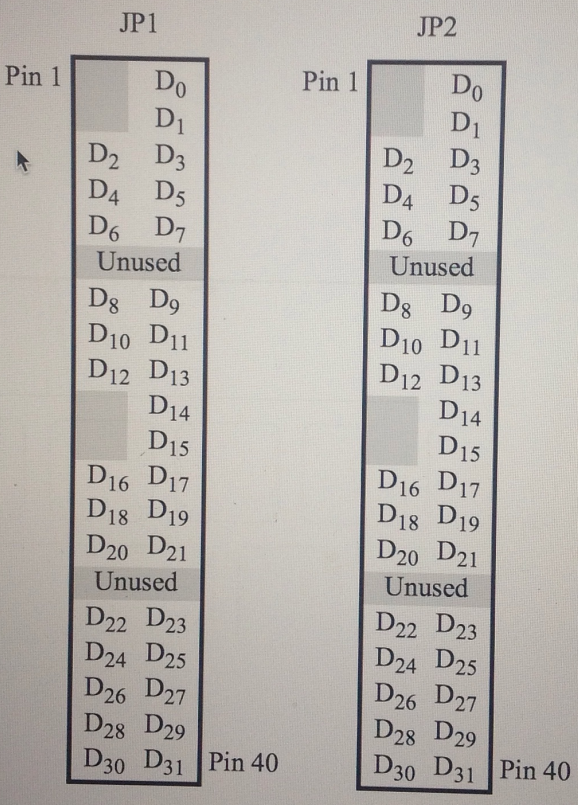


Figure 6. Assignment of parallel port bits to pins on *JP1* and *JP2*.