

MEC4126F: Integrated Embedded Systems

Prac 7 08 May 2025

Total marks: 51

#### <u>Instructions to students</u>

- 1. This template file contains space for the answers to the written questions of Prac 7.
- 2. Ensure that you copy-paste your answers inside the space allocated for each question.
- 3. Provide your numerical answers to TWO (2) significant decimal points, unless stated otherwise.

PeopleSoft ID: 1874813

#### Plagiarism Declaration

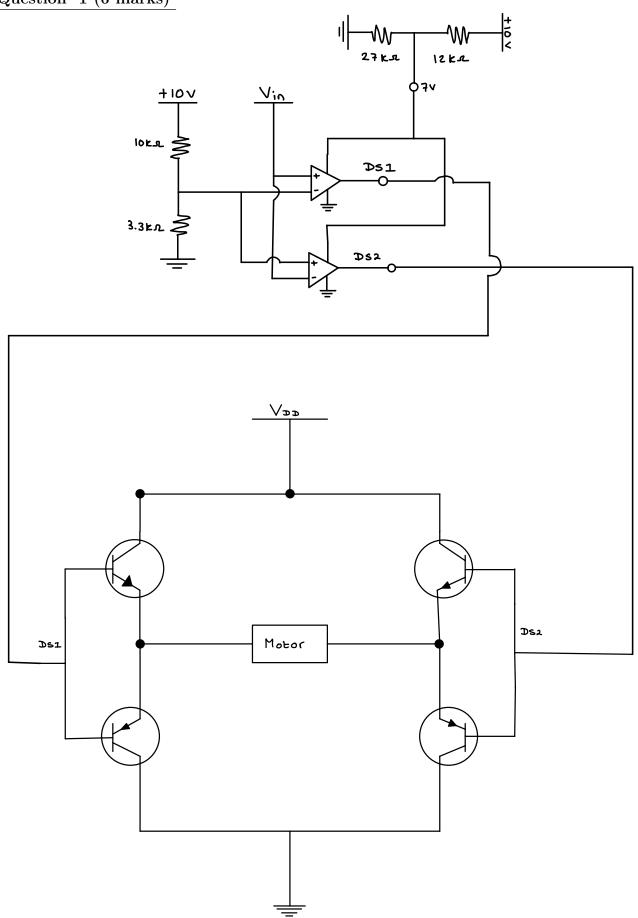
By demonstrating and submitting this practical I agree that:

- I know that plagiarism is a serious form of academic dishonesty.
- I have read the document about avoiding plagiarism, am familiar with its contents and have avoided all forms of plagiarism mentioned there.
- Where I have used the words of others, I have indicated this by the use of quotation marks.
- I have referenced all quotations and other ideas borrowed from others.
- I have not and shall not allow others to plagiarise my work.
- I have not used an AI language model to generate the code or answers submitted here.

Name: Daniele Squazzin Signature:

2 PTO

# Question 1 (6 marks)



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PTO

## Question 2 (3 marks)

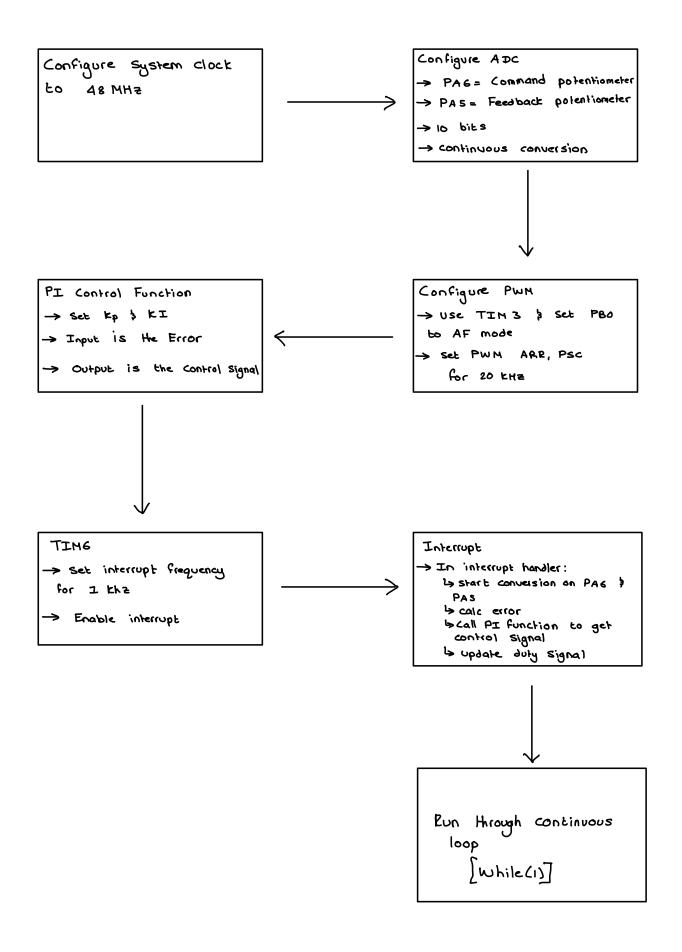
Vin	Comparator Output A (V)	Comparator Output B (U)
> Viet	8.4	0.7
< Vief	0.6	8.5

# Question 3 (3 marks)

Comparator Output A (V)	Comparator Output B (U)	H-bridge direction
~ 8. <i>5</i>	<b>≈</b> 0.6	Forward
≈ 0.5	≈ 8.4	Reverse

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### Question 4 (14 marks)



5 PTO

### Question 5 (6 marks)

Proportional gain

> Increase System response speed

4 Too high: overshoot

Integral gain

- Eliminates steady-state error

4 Too high: slow response

Sample Time

> Determines control loop frequency

4 Too long: poor control

> Too short: unnecessary processor Load

## Question 6 (10 marks)

(a) Slew rate 20.5 V/us

$$T_{pwn} > \frac{14 \text{ MS}}{0.01} = 1.4 \text{ mS}$$

: max freq = 714 Hz

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