Luleå University of Technology

Dept. of Computer Science, Electrical and Space Engineering

X7006E – Master Thesis Engineering Physics and Electrical Engineering

Project Evaluation of high peak power circuit board simulation versus physical tests (subject to change)

Author Brolin, Daniel

Supervisor
Johansson, Jonny

In cooperation with Grepit AB

June 5, 2019





Abstract

Summary of entire report

Contents

1	Acronyms	3
2	Glossary	3
3	Introduction 3.1 Goals 3.2 Hypothesis	2 2 2
4	Circuit Design 4.1 Schematic design	3 3 3
5	Evaluation Methods5.1 Physical measurements5.2 Simulations	4 4
6	Results 6.1 Measurements	5 5 5
7	Discussion	6
Re	eferences	7
Aı	ppendices	8
\mathbf{A}	Large Figures	8
В	Lists	9

1 Acronyms

1. PCB Printed Circuit Board 3, Glossary: PCB

2 Glossary

2. PCB A Printed Circuit Board (PCB) is the common acronym when referring to populated circuit boards. 3

3 Introduction

When designing electrical circuits the electromagnetic properties of the circuit board plays a role in parasitic behavior and interference that lowers the efficiency of the system; these properties are magnified proportionally to the frequency and power. While normally some parasitics are not that big of an issue, a system reliant on precision will lose effectiveness quickly when facing a surge of energy.

These parasitics are difficult to isolate, but it is believed simulations of the circuit layout could determine the source and possible courses of counter measures.

3.1 Goals

To find the source of the parasitic- and unexpected behavior, different methods to simulate the electromagnetic properties of the circuitry will be explored in the pre-study and should be validated by comparison against physical measurements. This will allow some insight in what losses are caused by the parasitics, where they originate from and what could be done to prevent them.

3.1.1 Reason

Similar circuits has been previously designed by the student, and the circuit design presented in this thesis is loosely based on the second version in production, giving some initial knowledge to the system. The first version worked as a proof of concept and suffered severe parasitic inductance. In the system version which prompted this thesis work repeated surges of approximately 2.5 kW for the duration of 1 ns are induced. The system simulated and analyzed in this thesis work is based loosely on this system, but different designs, different components and only a single channel. More about the system can be found in section 4.

3.1.2 Usecase

By measuring the circuitry it is simple to locate errors and disturbances, but locating disturbances is not identical to locating the source. The results of this thesis could act directly as a foundation for the third version, or simply as another area of expertise to customers, where the most accurate method can be used to simulate future systems ahead of production, saving a great deal of time and money.

3.2 Hypothesis

The parasitics are difficult to isolate, but hypothetically simulating full systems could pinpoint these and be used as reference designs to create ideal circuits ahead of time. Even if a source can not be found directly, a faulty circuit could be identified.

For this thesis to be successful a circuit needs to be successfully simulated with parasitics in concordance with a physically measured system. If this can be performed reliably and without to much effort, a system can be analyzed ahead of production saving time and money.

4 Circuit Design

Section that describes the tested system

4.1 Schematic design

Schematic of system

4.2 Component choices

Motivates component choices

4.3 PCB design

Describes the Printed Circuit Board (PCB) design that will be tested and simulated. Motivates design choices.

5 Evaluation Methods

Chapter that will describe testing methods and testing setup

5.1 Physical measurements

Will describe the measurement method for the system hardware

5.1.1 setup

Describes hardware measurement setup

5.1.2 tests

Describes the measurement method and what they test

5.2 Simulations

Will describe the testing method for the simulations

5.2.1 setup

Describes simulation setup

5.2.2 tests

describes the tests and what they mean

6 Results

This sections assembles results gathered from the tests in section 5.

6.1 Measurements

Will summarize measured results

6.2 Simulation

Will summarize simulated results

6.3 Comparison

Will be a short summary of the similarities and differences between simulated results and measured results

7 Discussion

Discusses the results from subsection 6.3. What do the similarities and differences mean? Why are they there?

Can this be used in the industry? Is this an useful product for customers? Compare against Hypothesis.

References

[1] https://www.autodesk.com/products/fusion-360/overview

Appendices

A Large Figures

insert pictures

Projec	t <i>Evaluatio</i>	on of	high	peak	power	circuit	board	simulation	versus	phy	sical
tests (s	subject to a	chang	(e)						Jun	e 5,	2019

B Lists

insert lists

 ${\bf LAST\ WORDS}$

Project Evaluation of high peak power circuit board tests (subject to change)	simulation versus physical June 5, 2019