**Design Documentation:**

A table with numbers and letters

Description automatically generated

We relied on the provided truth table to gain insights into the prioritization and wire assignments. This table proved invaluable in formulating our testbench design.

**Challenges Faced:**

We encountered a syntax error in the testbench code. The discrepancy arose from our coding approach, particularly in how we handled (sw), which differed from the provided example.

**Key Takeaways from Implementation:**

During testbench simulation, we initially expected to obtain 0s and 1s but observed values of 2 and 3. To address this, we incorporated an additional LED as a precautionary measure when implementing with LEDs.