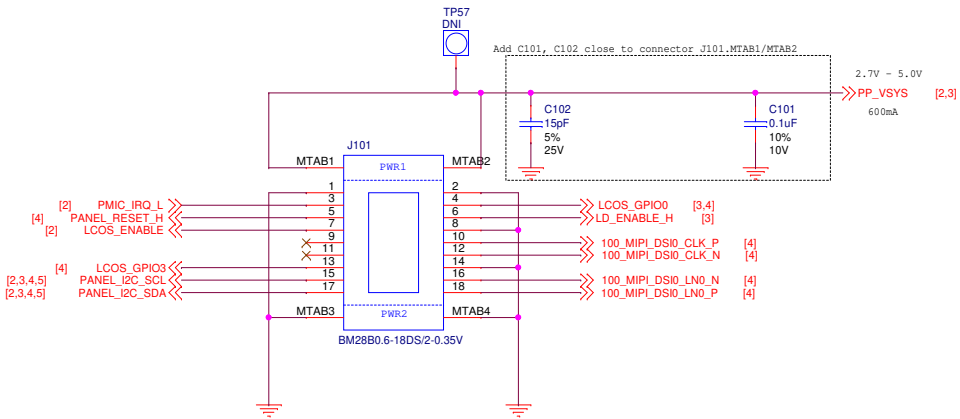


# Connector to Superflex

I2C Address Table

LCOS :	0x64
PMIC:	0x40
LED Driver:	0x29
Temp Sensor driver board:	0x48
Temp sensor RB LED:	0x49
Temp Sensor G LED :	0x4A
NVRAM:	0x50 and 0x58



845-03106-01-1p3



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**TITLE**  
846-02196-01, SCH, DISPLAY DRIVER BOARD, PICTOR

**SHEET DESCRIPTION**  
01-SuperFlex\_Connector

**ENGINEER**  
Suhas Gupta, Kevin Shia

**APPROVED**  
<APPROVER>

**DATE**  
Monday, March 06, 2023

**SIZE**  
B

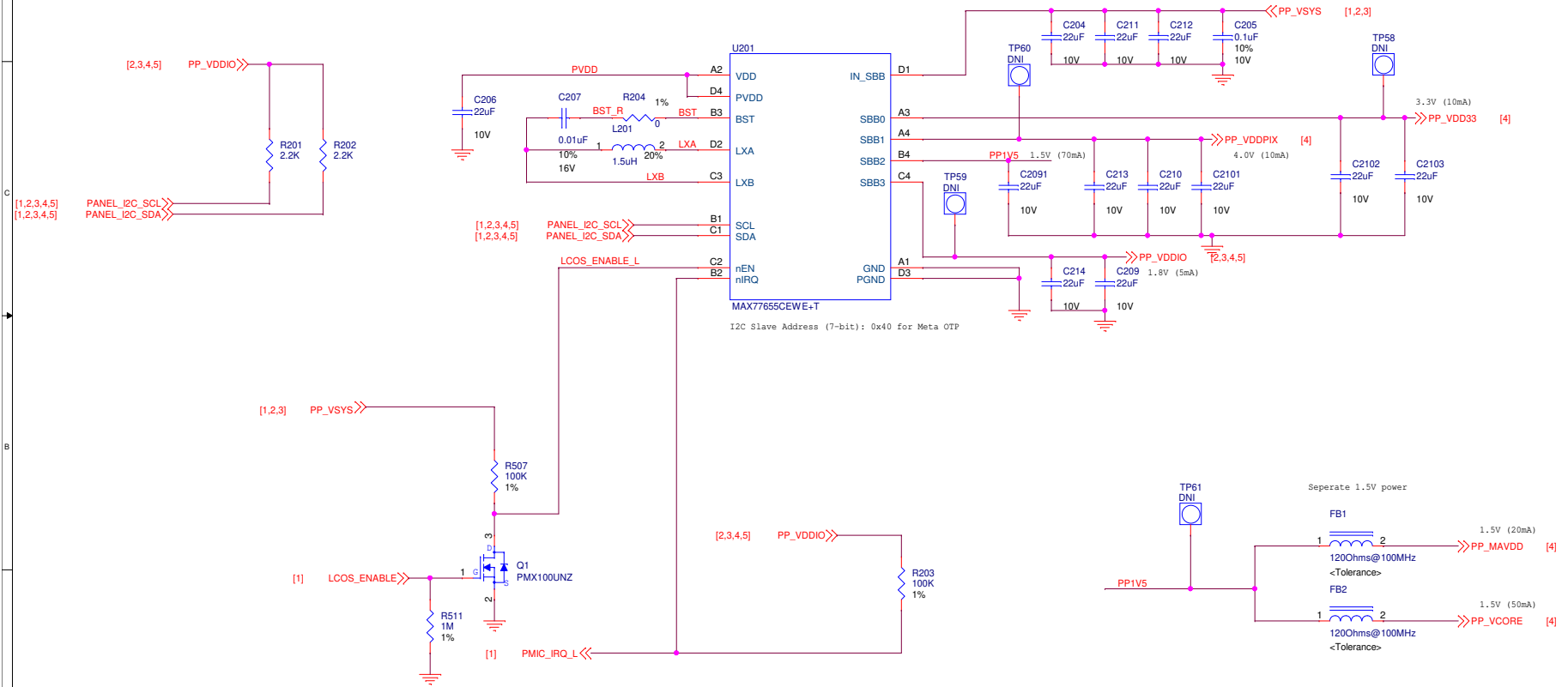
**TYPE**  
SCH

**PART NUMBER**  
846-02196-01

**REVISION**  
1.0.3

**SHEET**  
1 of 6

Display PMIC



845-03106-01=1p3



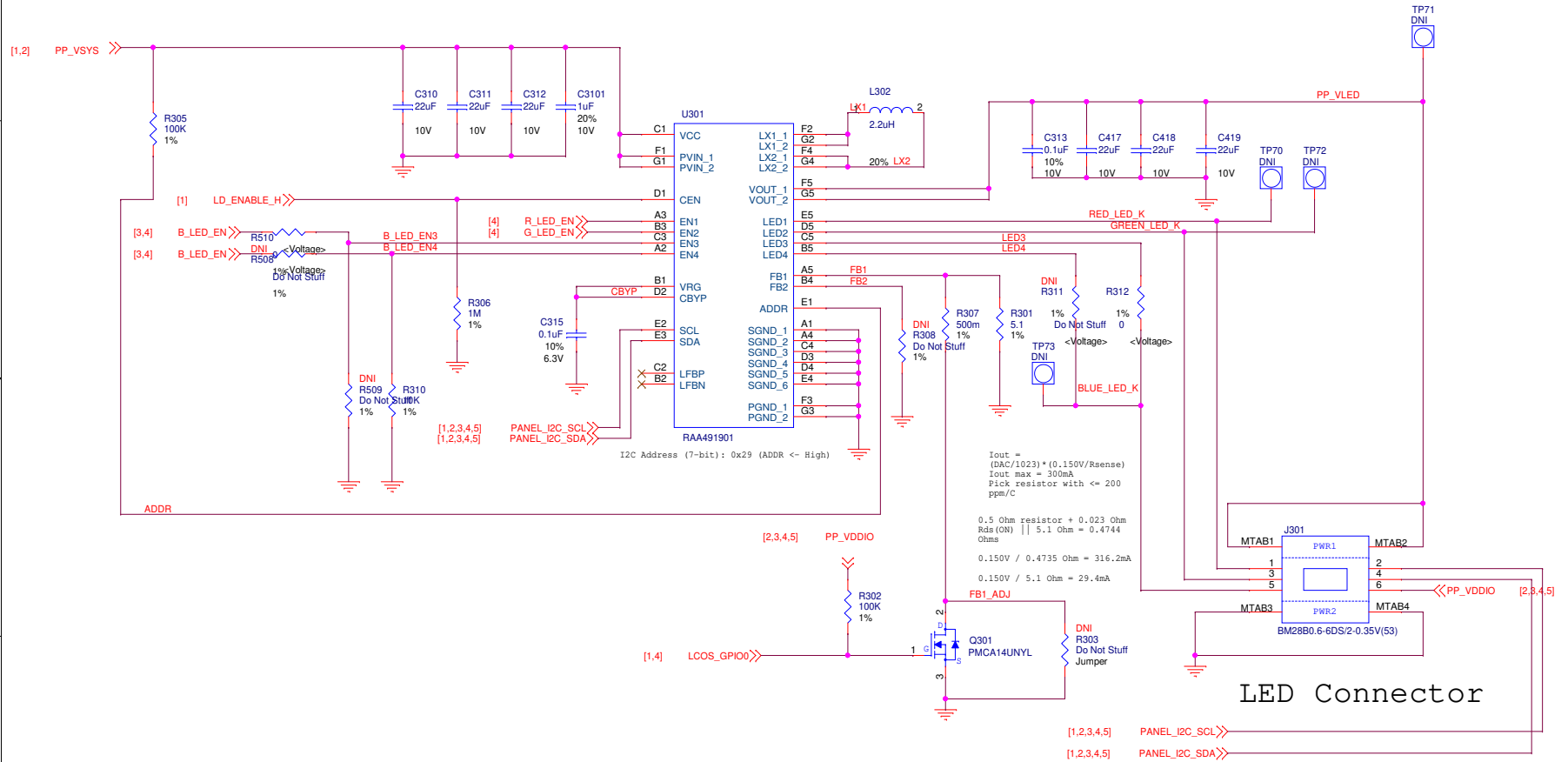
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846-02196-01, SCH, DISPLAY DRIVER BOARD, PICTOR

**SHEET DESCRIPTION**  
02-PMIC

<b>ENGINEER</b> Suhas Gupta, Kevin Shia		<b>APPROVED</b> <APPROVER>	<b>DATE</b> Monday, March 06, 2023	
<b>SIZE</b> B	<b>TYPE</b> SCH	<b>PART NUMBER</b> 846-02196-01	<b>REVISION</b> 1.0.3	<b>SHEET</b> 2 of 6

# LED Driver



845-03106-01=1p3



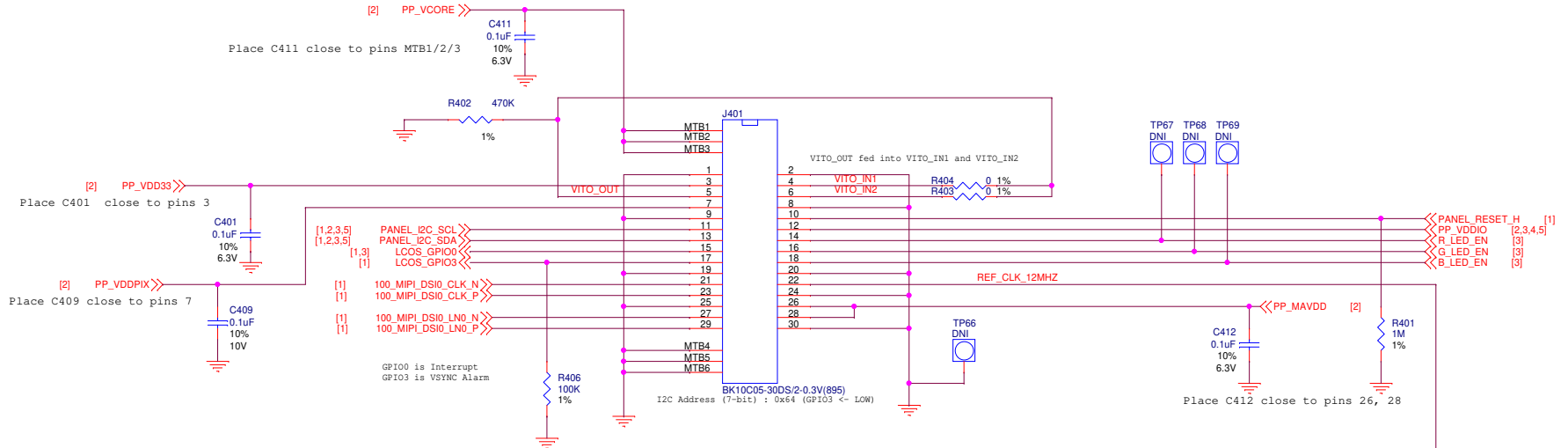
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**TITLE**  
846-02196-01, SCH, DISPLAY DRIVER BOARD, PICTOR

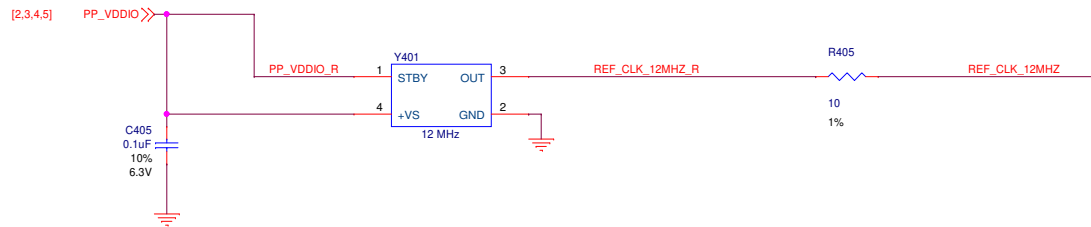
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03-LED\_Driver

ENGINEER		APPROVED	DATE	
Suhas Gupta, Kevin Shia		<APPROVER>	Monday, March 06, 2023	
SIZE	TYPE	PART NUMBER	REVISION	SHEET
B	SCH	846-02196-01	1.0.3	3 of 6

# LCOS Connector



## Reference clock generator for LCoS ASIC



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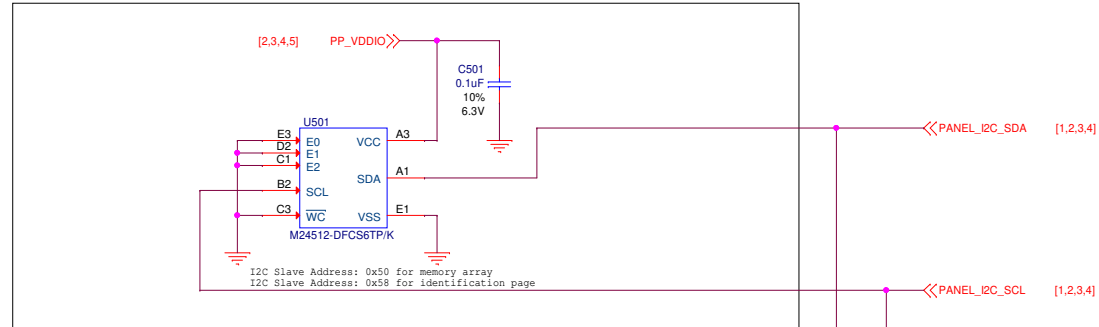
**TITLE**  
846-02196-01, SCH, DISPLAY DRIVER BOARD, PICTOR

**SHEET DESCRIPTION**  
04-LCOS\_Connector

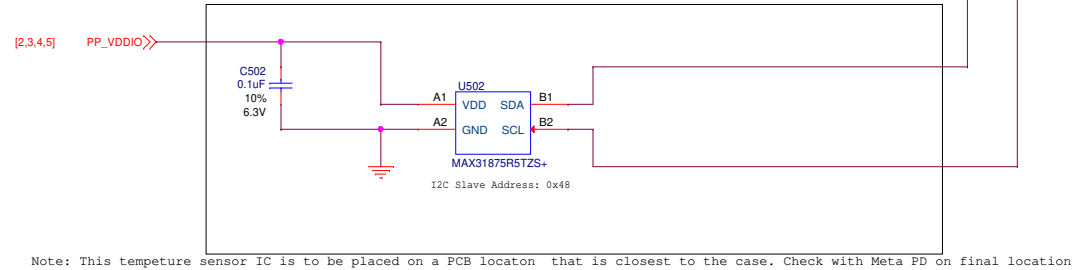
<b>ENGINEER</b> Suhas Gupta, Kevin Shia		<b>APPROVED</b> <APPROVER>	<b>DATE</b> Monday, March 06, 2023	
<b>SIZE</b> B	<b>TYPE</b> SCH	<b>PART NUMBER</b> 846-02196-01	<b>REVISION</b> 1.0.3	<b>SHEET</b> 4 of 6

# Miscellaneous Circuits

## 512 kbit NVRAM



## Temperature Sensor for Projector Case



Note: This temperture sensor IC is to be placed on a PCB locaton that is closest to the case. Check with Meta PD on final location

845-03106-01-1p3



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846-02196-01, SCH, DISPLAY DRIVER BOARD, PICTOR

**SHEET DESCRIPTION**  
05-Misc

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<b>SIZE</b> B	<b>TYPE</b> SCH	<b>PART NUMBER</b> 846-02196-01	<b>REVISION</b> 1.0.3	<b>SHEET</b> 5 of 6

## Change List

V2

1. Change EEPROM U501 from 24CW640T-I/CS0668 to M24512-DFCS6TP/K
2. Remove zero ohm resistors: R407, R408, R502, R503, R505, R506 (I2C zero ohm on SDA/SCL)
3. Add MOSFET PMCA14UNYL in series with R307
4. Add 5 ohm resistor (R301) in parallel with R307 / MOSFET PMCA14UNYL (Q301)
5. Remove R309 (ADDR pull down)
6. Load C418 and add C419 to account for cap & IC corners
7. Change R511 from 200k to 1M
8. R303 jumper in parallel with MOSFET Q301 (To bypass the part)
9. Added C102 15pF desense cap to J101 VSYS voltage rail
10. Add C103 22uF capacitor close to superflex connector
11. Add C419 22uF capacitor to the PP\_VLED output (DNI)
12. Add TPs to match GTK test point setup

V3.

1. Remove C103 (22uF) capacitor near superflex connector
2. Add 2.2k I2C pullup resistors powered by PMIC 1.8V rail
3. Rename LD\_I2C\_Sxx as PANEL\_I2C\_Sxx and disconnect pin 9 and 11 on the superflex connector
4. Remove R205 (PMIC\_IRQ\_L) zero ohm resistor
5. Remove R512 (LCOS\_GPIO0) zero ohm resistor
6. R306 (LD\_ENABLE\_H) now PD instead of PU and populated
7. R203 changed from 0201 to 01005 for testing
8. R401 1M PD on PANEL\_RESET\_H near J401
9. Updated L201 to DFE252007F-1R5M=P2

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TITLE  
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SHEET DESCRIPTION  
06-Change\_List

ENGINEER  
Suhas Gupta, Kevin Shie

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<APPROVER>

DATE  
Monday, March 06, 2023

SIZE  
C

TYPE  
SCH

PART NUMBER  
846-02196-01

REVISION  
1.03

SHEET  
6 of 6