BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE PILANI, HYDERABAD CAMPUS

CS F303 COMPUTER NETWORKS ASSIGNMENT-2 PHASE-2

PERFORMANCE PLOTS

Team Details

Vatsayayn Binay: 2018A7PS0305H

Satvik Arora: 2017B5A71671H

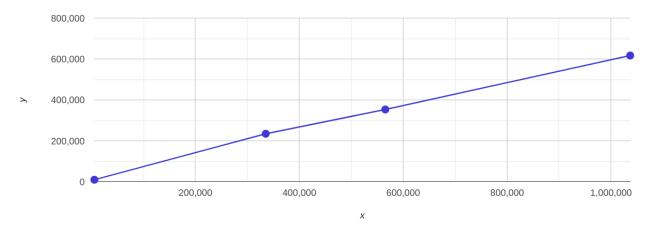
Hardik Jain: 2017B3A71113H Viraj Singh: 2017B3A71099H

Shivali Ratra: 2017B4A71707H

THROUGHPUT v/s TIME

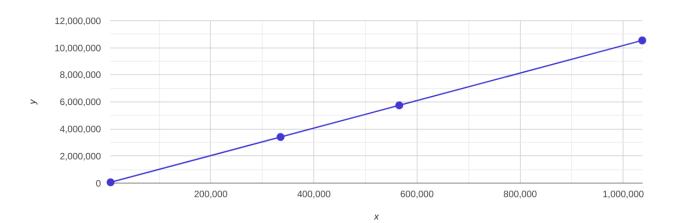
1. No delay, No loss and No throttle

File Size (bytes)	Time (microsec)
5592	9788
335327	234534
565519	353302
1036851	617061



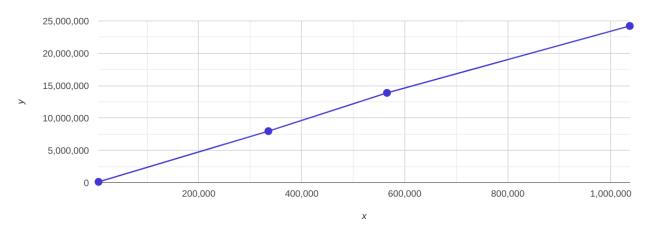
2. <u>10 ms delay</u>

File Size (bytes)	Time (microsec)
5592	61688
335327	3408706
565519	5746306
1036851	10541488



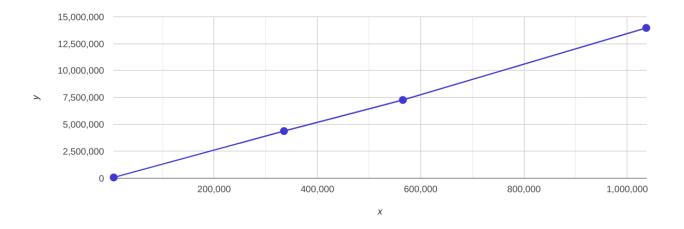
3. <u>10 ms delay, 60% loss, 50% corrupt</u>

File Size (bytes)	Time (microsec)
5592	138539
335327	7970367
565519	13887363
1036851	24221261



4. 10 ms delay, 40% drop, 30% duplicate

File Size (bytes)	Time (microsec)
5592	72733
335327	4380525
565519	7268448
1036851	13967565



5. 10 ms delay, 30% duplicate, 50% reorder

File Size (bytes)	Time (microsec)
5592	18081
335327	1441520
565519	2250328
1036851	4263772

