# $Q1\text{-}14\ Answers: \textbf{SampleExam1Questions\_SelectedFromQuestionsActuallyWrittenInThePast}$

Question 1	6			
Question 2	11			
~	-4			
	-5			
	-3			
Question 3	NR			
2	1011			
	1100			
	1100			
Question 4	1100 1010 1111 1110			
Question 5	3A.A8			
Question 6	[-2 <sup>5</sup> , 2 <sup>5</sup> - 1] or [-32, 31]			
Question 7	((((((((((((((((((((((((((((((((((((			
Question 8	((((((((((((((((((((((((((((((((((((			

# Question 9

10101001 11010110 (+) -----0111111 overflow occurred: [V] N]

sum of 2 negative values cannot be positive

#### Question 11

address (in decimal)	Contents in hex (if little endian)	Contents in hex (if big endian)
1000	21	AB
1001	43	CD
1002	CD	43
1003	AB	21

## Question 12 6<sup>th</sup>

## Question 13

- 3 of the following (or something to the effect):
- CPU, memory and I/O devices (interconnected through bus) are basic components
- data and instructions both stored in memory
- single, sequentially addressed memory that's accessed by address regardless of what's stored
- instructions are executed sequentially unless explicitly altered

#### Question 14

In the instruction set, the only instructions that access memory are load and store instructions.