



FILE NAME: **PCB\_V0.5.pdsprj**

DESIGN TITLE: **PCB\_V0.5.pdsprj**

PATH: E:\Estudios-estudies-Revana\Bizerba\PCB\_V0.5.pdsprj

BY: @AUTHOR

BOARD: @BOARD

DATE:  
**17/10/2025**

1 of 1  
TIME: 09:53:44 p. n.

REV:@REV