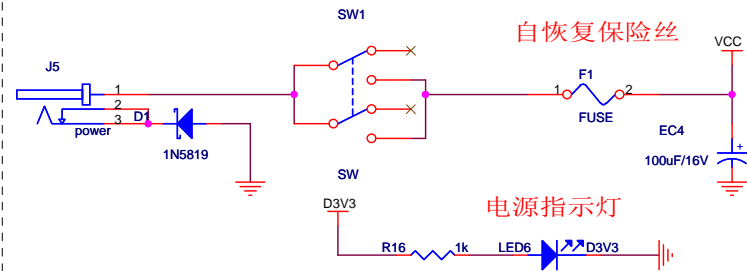


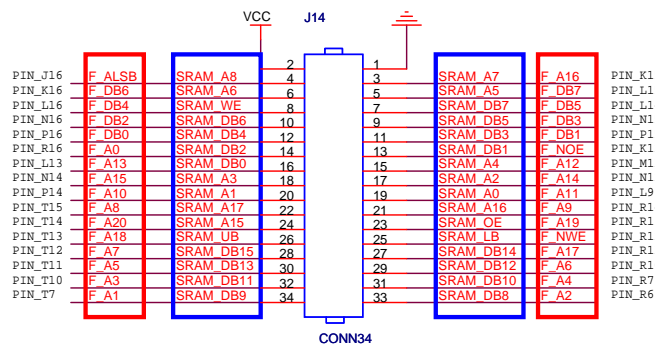
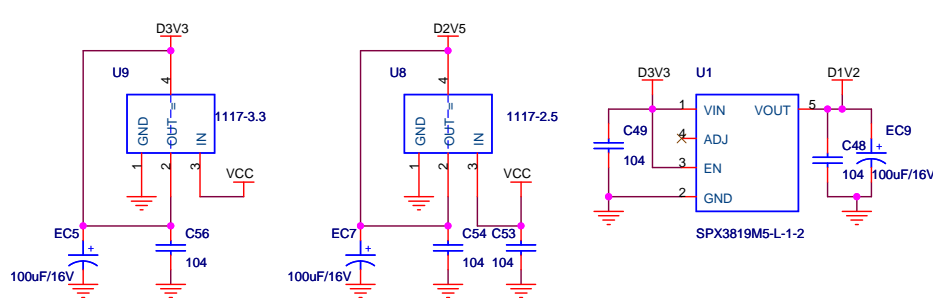
页码	描述	页码	描述
1	1_POWER	6	
2	2_INTERFACE	7	
3	3_FPGA	8	
4	4_FLASH_SDRAM	9	
5		10	

FPGA黑金开发板核心板 型号：DB4CE15			
2010-2011 @ 黑金动力社区 Http://www.heijin.org			
创建日期	2011.12.1	版本	3.0

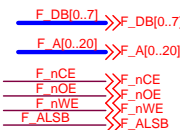
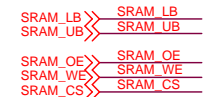
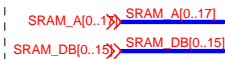
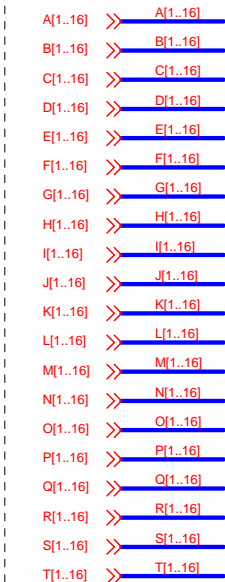
电源入口（输入电压为5v，接口为内正外负）



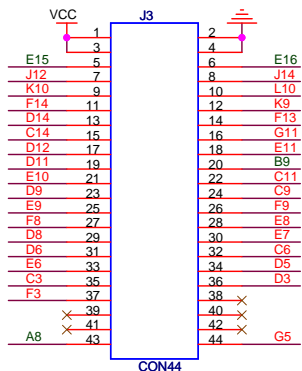
电源转换



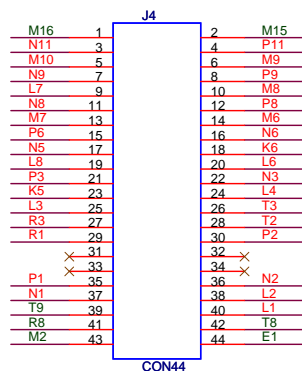
注：34针标准扩展口，
与FLASH, SRAM部分引脚共用，
红框内是FLASH引脚，
蓝框内是SRAM引脚；
使用时将F_NCE, SRAM_CS两个引脚拉高



向下2.0mm扩展口

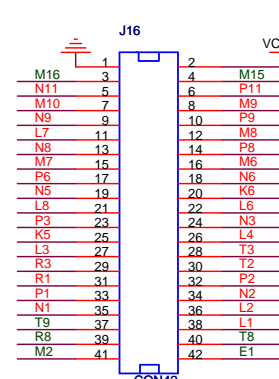
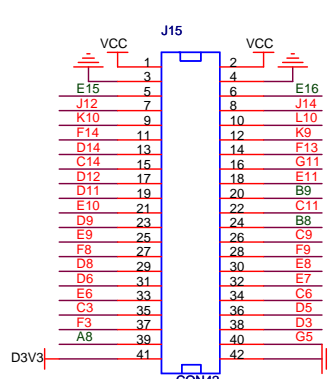


对应底板的接插件J7



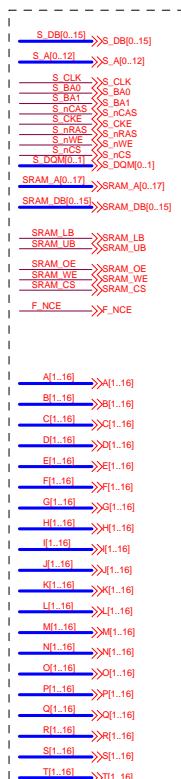
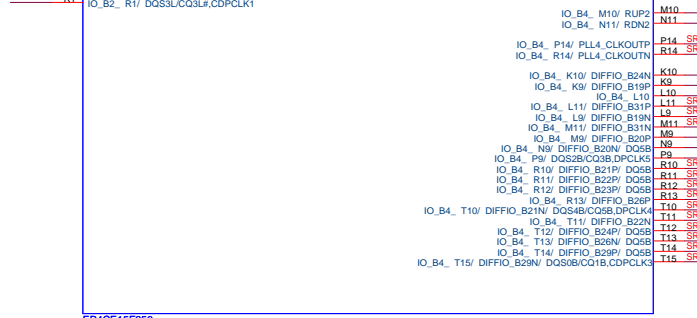
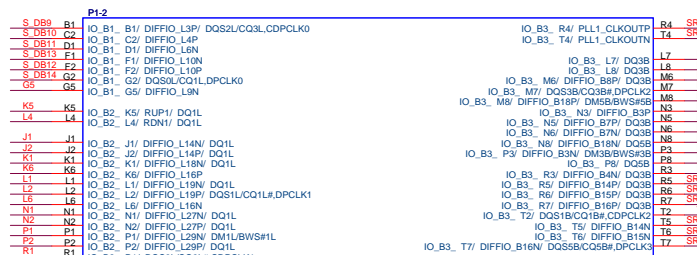
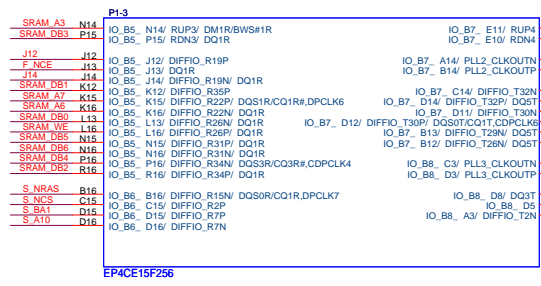
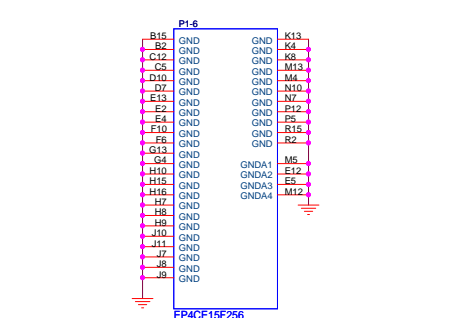
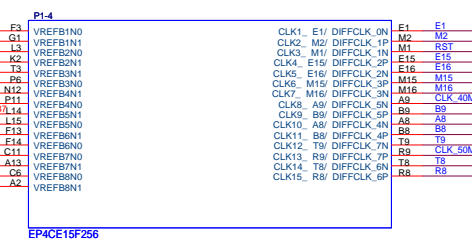
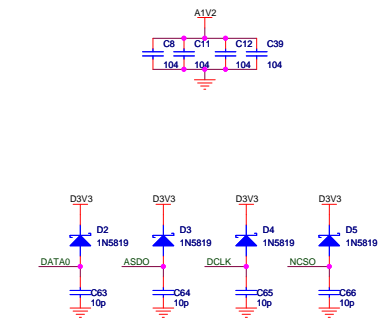
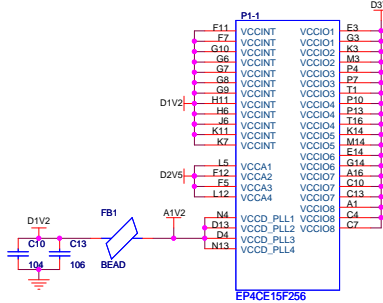
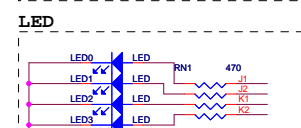
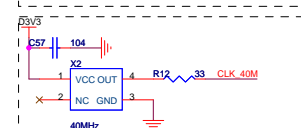
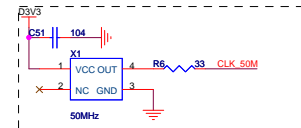
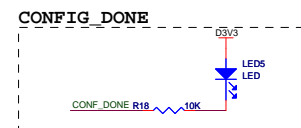
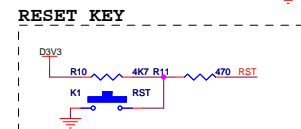
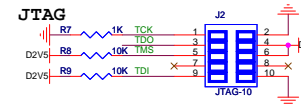
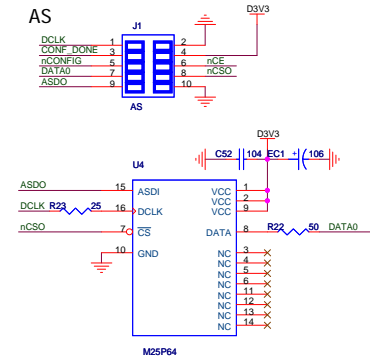
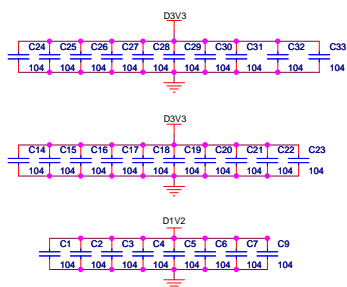
对应底板的接插件J6

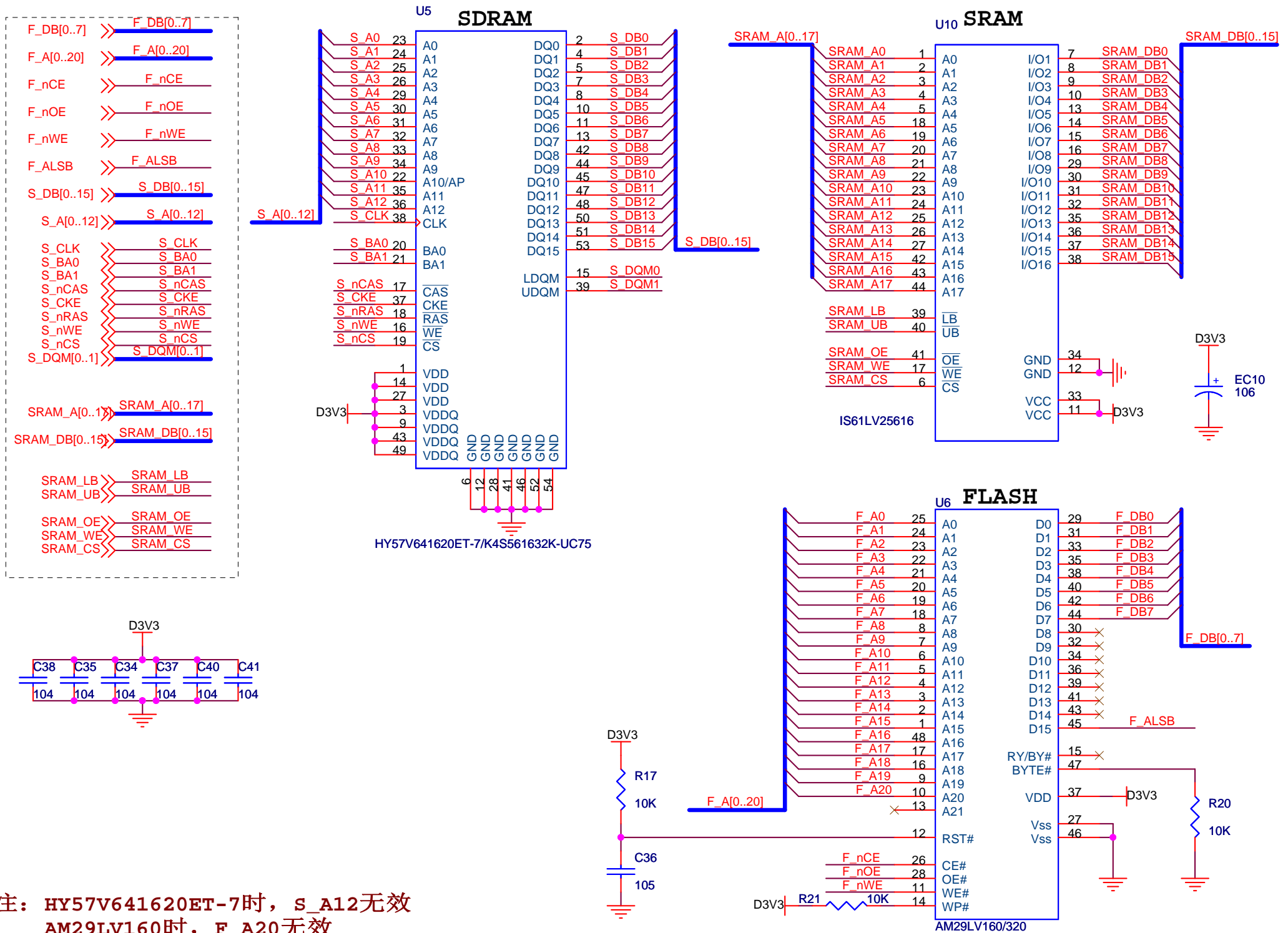
预留2.54mm扩展口



注：引脚中绿色的部分只能作为输入引脚使用，不能作为输出引脚使用

Title		
1_Power		
Size	Document Number	Rev
B	Http://www.heijin.org	3.0
Date:	Thursday, October 20, 2011	Sheet 1 of 4





注: HY57V641620ET-7时, s_A12无效
AM29LV160时, F_A20无效