

| 页码 | 描述 | 页码 | 描述 |
|----|----|----|----|
| 1 | | 6 | |
| 2 | | 7 | |
| 3 | | 8 | |
| 4 | | 9 | |
| 5 | | 10 | |

| | | | |
|---|-----------|----|-------|
| <p>FPGA黑金开发板底板 2010-2011@黑金动力社区 <u>Http://www.heijin.org</u></p> | | | |
| 创建日期 | 2011.9.12 | 版本 | 1.0.0 |

按键

KEY_UP >>> KEY_UP
KEY_DOWN >>> KEY_DOWN
KEY_LEFT >>> KEY_LEFT
KEY_RIGHT >>> KEY_RIGHT
KEY_ENTER >>> KEY_ENTER

数码管

DIG[0..7] >>> DIG[0..7]
SEL[0..5] >>> SEL[0..5]

LCD

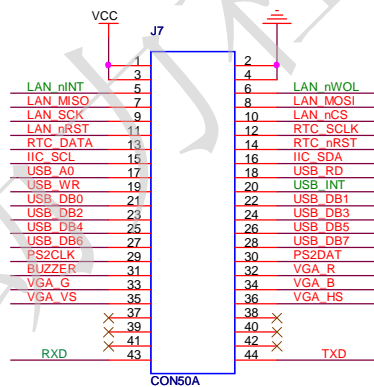
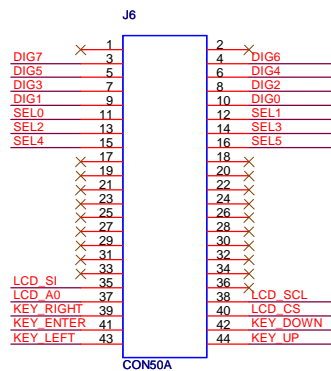
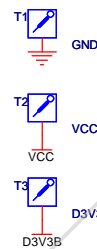
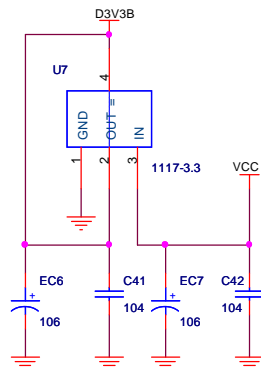
LCD_CS >>> LCD_CS
LCD_A0 >>> LCD_A0
LCD_SCL >>> LCD_SCL
LCD_SI >>> LCD_SI

VGA

VGA_R >>> VGA_R
VGA_G >>> VGA_G
VGA_B >>> VGA_B
VGA_HS >>> VGA_HS
VGA_VS >>> VGA_VS

PS2

PS2DAT >>> PS2DAT
PS2CLK >>> PS2CLK



TO NET

LAN_nINT >>> LAN_nINT
LAN_MISO >>> LAN_MISO
LAN_MOSI >>> LAN_MOSI
LAN_SCK >>> LAN_SCK
LAN_nCS >>> LAN_nCS
LAN_nRST >>> LAN_nRST
LAN_nWOL >>> LAN_nWOL

TO RTC_422_232

RTC_SCLK >>> RTC_SCLK
RTC_nRST >>> RTC_nRST
RTC_DATA >>> RTC_DATA
RXD >>> RXD
TXD >>> TXD
IIC_SCL >>> IIC_SCL
IIC_SDA >>> IIC_SDA

TO USB

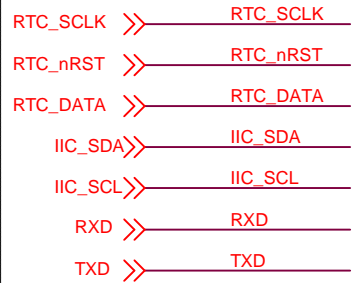
USB_DB[0..7] >>> USB_DB[0..7]
USB_INT >>> USB_INT
USB_WR >>> USB_WR
USB_RD >>> USB_RD
USB_A0 >>> USB_A0

蜂鸣器

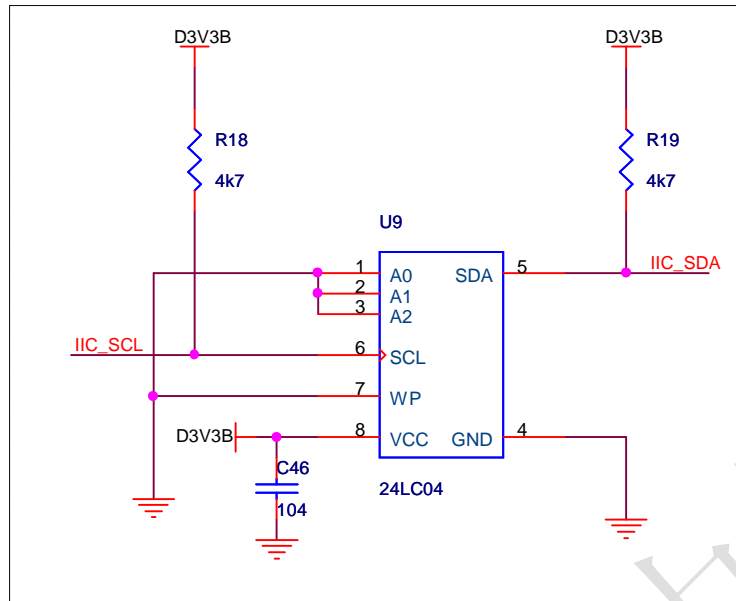
BUZZER >>> BUZZER

| | | |
|---------|---|---------------|
| Title | | |
| 4_power | | |
| Size | Document Number | Rev |
| B | Http://www.heijin.org | 1.0.0 |
| Date: | Monday, September 12, 2011 | Sheet 5 of 10 |

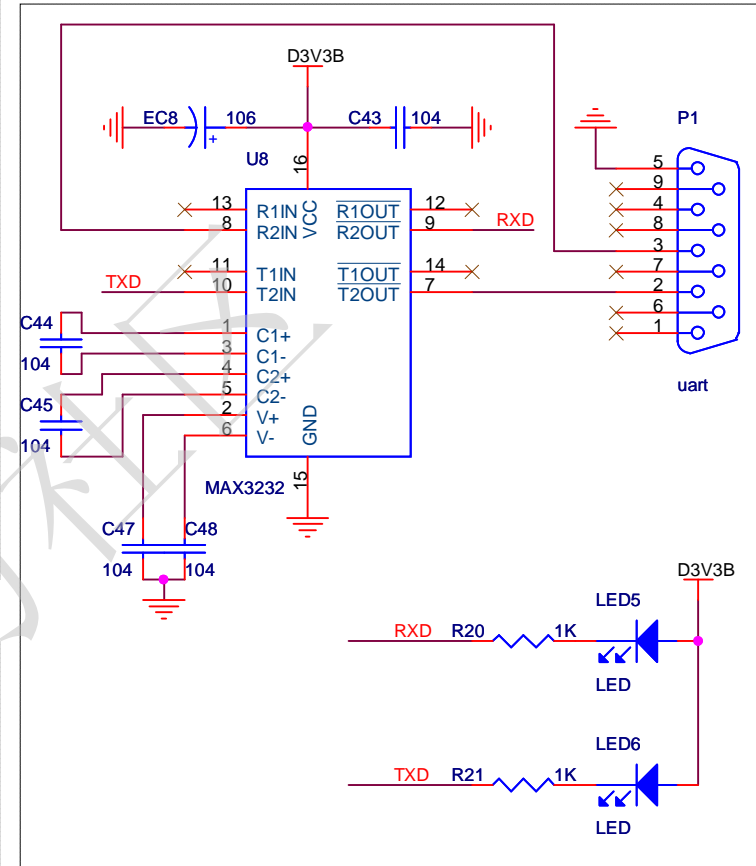
FROM FPGA



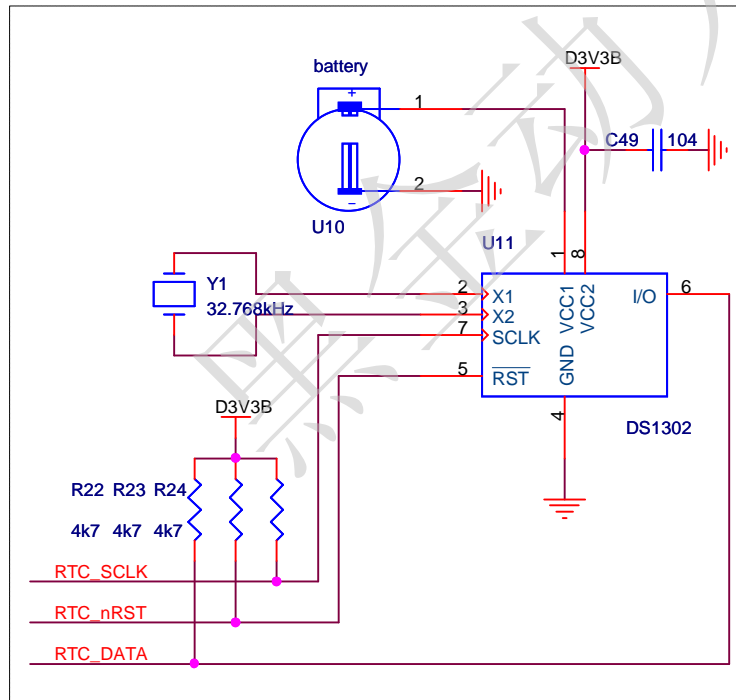
EEPROM



UART



RTC



Title

4_rtc_eeprom_uart

Size

Document Number

[Http://www.heijin.org](http://www.heijin.org)

Rev

1.0.0

Date:

Monday, September 12, 2011

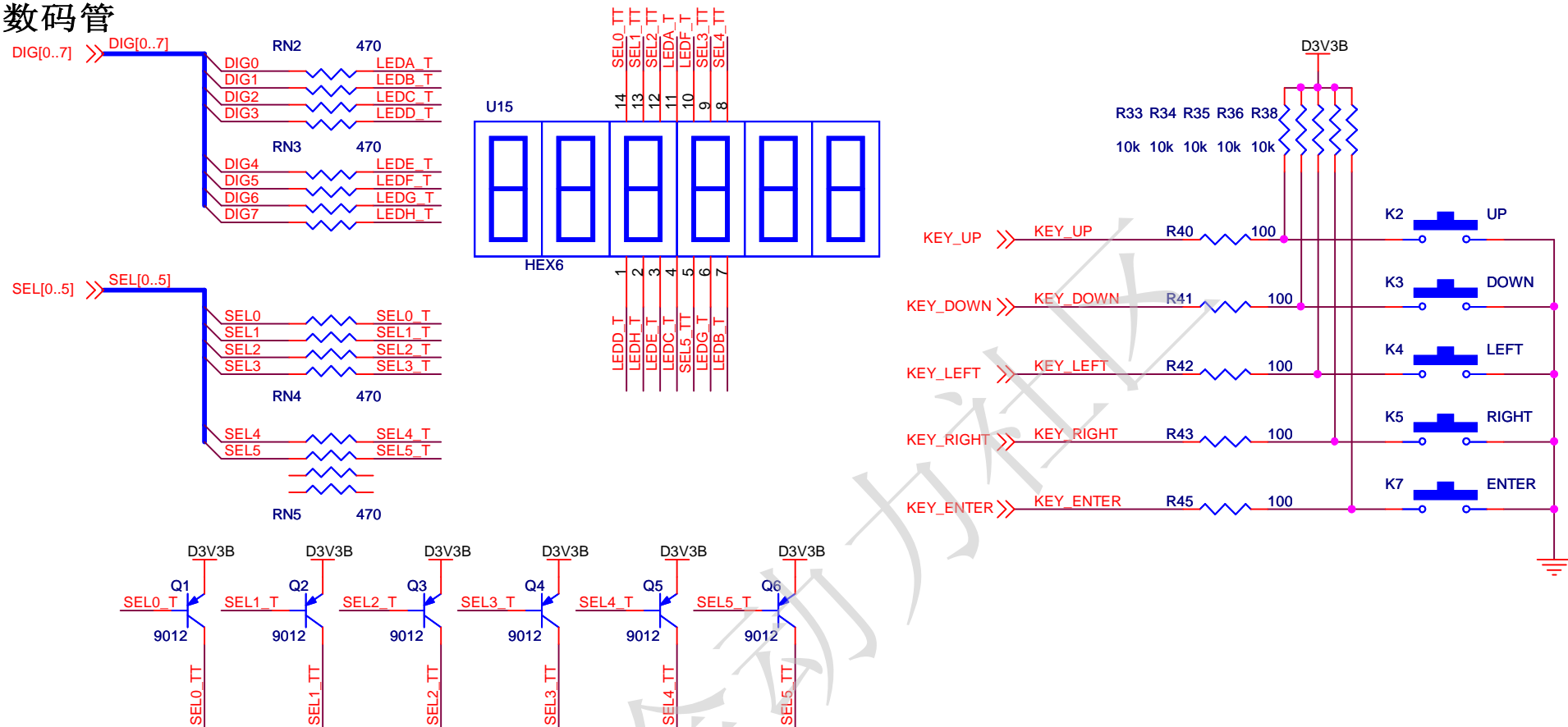
Sheet

6

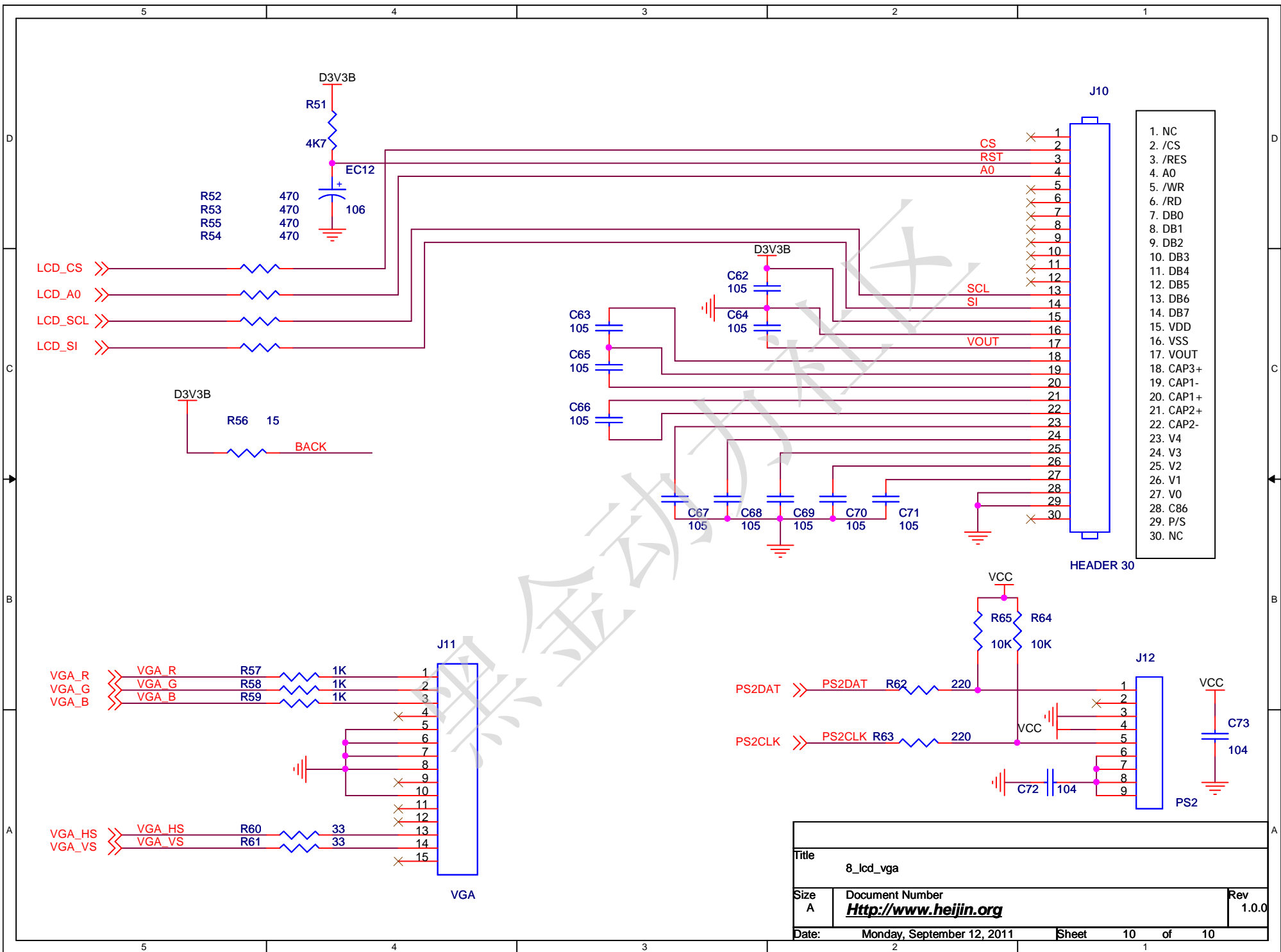
of

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数码管



| | | |
|-----------------|---|---------------|
| Title | | |
| 7_led_key_digit | | |
| Size | Document Number | Rev |
| A | Http://www.heijin.org | 1.0.0 |
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| | | |
|-----------|---|----------------|
| Title | | |
| 8_lcd_vga | | |
| Size | Document Number | Rev |
| A | Http://www.heijin.org | 1.0.0 |
| Date: | Monday, September 12, 2011 | Sheet 10 of 10 |