0x00 PAGE\_CFG Register P0\_R0

0x01 SW\_RESET Register P0\_R1

0x02 SLEEP\_CFG Register P0\_R2

0x05 SHDN\_CFG Register P0\_R5

0x07 ASI\_CFG0 Register P0\_R7

0x08 ASI\_CFG1 Register P0\_R8

0x09 ASI\_CFG2 Register P0\_R9

0x0B ASI\_CH1 Register P0\_R11

0x0C ASI\_CH2 Register P0\_R12

0x0D ASI\_CH3 Register P0\_R13

0x0E ASI\_CH4 Register P0\_R14

0x0F ASI\_CH5 Register P0\_R15

0x10 ASI\_CH6 Register P0\_R16

0x13 MST\_CFG0 Register P0\_R19

0x14 MST\_CFG1 Register P0\_R20

0x15 ASI\_STS Register P0\_R21

0x16 CLK\_SRC Register P0\_R22

0x21 GPIO\_CFG0 Register P0\_R33

0x22 GPIO\_CFG1 Register P0\_R34

0x23 GPIO\_CFG2 Register P0\_R35

0x24 GPI\_CFG0 Register P0\_R36

0x25 GPI\_CFG1 Register P0\_R37

0x26 GPIO\_VAL Register P0\_R38

0x27 GPIO\_MON Register P0\_R39

0x28 INT\_CFG Register P0\_R40

0x29 INT\_MASK0 Register P0\_R41

0x2A INT\_MASK1 Register P0\_R42

0x2B INT\_MASK2 Register P0\_R43

0x2C INT\_LTCH0 Register P0\_R44

0x2D CHx\_LTCH Register P0\_R45

0x2E CH1\_LTCH Register P0\_R46

0x2F CH2\_LTCH Register P0\_R47

0x30 CH3\_LTCH Register P0\_R48

0x31 CH4\_LTCH Register P0\_R49

0x32 CH5\_LTCH Register P0\_R50

0x33 CH6\_LTCH Register P0\_R51

0x34 INT\_MASK3 Register P0\_R52

0x35 INT\_LTCH1 Register P0\_R53

0x36 INT\_LTCH2 Register P0\_R54

0x37 INT\_LTCH3 Register P0\_R55

0x38 MBDIAG\_CFG0 Register P0\_R56

0x39 MBDIAG\_CFG1 Register P0\_R57

0x3A MBDIAG\_CFG2 Register P0\_R58

0x3B BIAS\_CFG Register P0\_R59

0x3C CH1\_CFG0 Register P0\_R60

0x3D CH1\_CFG1 Register P0\_R61

0x3E CH1\_CFG2 Register P0\_R62

0x3F CH1\_CFG3 Register P0\_R63

0x40 CH1\_CFG4 Register P0\_R64

0x41 CH2\_CFG0 Register P0\_R65

0x42 CH2\_CFG1 Register P0\_R66

0x43 CH2\_CFG2 Register P0\_R67

0x44 CH2\_CFG3 Register P0\_R68

0x45 CH2\_CFG4 Register P0\_R69

0x46 CH3\_CFG0 Register P0\_R70

0x47 CH3\_CFG1 Register P0\_R71

0x48 CH3\_CFG2 Register P0\_R72

0x49 CH3\_CFG3 Register P0\_R73

0x4A CH3\_CFG4 Register P0\_R74

0x4B CH4\_CFG0 Register P0\_R75

0x4C CH4\_CFG1 Register P0\_R76

0x4D CH4\_CFG2 Register P0\_R77

0x4E CH4\_CFG3 Register P0\_R78

0x4F CH4\_CFG4 Register P0\_R79

0x50 CH5\_CFG0 Register P0\_R80

0x51 CH5\_CFG1 Register P0\_R81

0x52 CH5\_CFG2 Register P0\_R82

0x53 CH5\_CFG3 Register P0\_R83

0x54 CH5\_CFG4 Register P0\_R84

0x55 CH6\_CFG0 Register P0\_R85

0x56 CH6\_CFG1 Register P0\_R86

0x57 CH6\_CFG2 Register P0\_R87

0x58 CH6\_CFG3 Register P0\_R88

0x59 CH6\_CFG4 Register P0\_R89

0x64 DIAG\_CFG0 Register P0\_R100

0x65 DIAG\_CFG1 Register P0\_R101

0x66 DIAG\_CFG2 Register P0\_R102

0x67 DIAG\_CFG3 Register P0\_R103

0x68 DIAG\_CFG4 Register P0\_R104

0x6B DSP\_CFG0 Register P0\_R107

0x6C DSP\_CFG1 Register P0\_R108

0x70 AGC\_CFG0 Register P0\_R112

0x73 IN\_CH\_EN Register P0\_R115

0x74 ASI\_OUT\_CH\_EN Register P0\_R116

0x75 PWR\_CFG Register P0\_R117

0x76 DEV\_STS0 Register P0\_R118

0x77 DEV\_STS1 Register P0\_R119

0x7E I2C\_CKSUM Register P0\_R126