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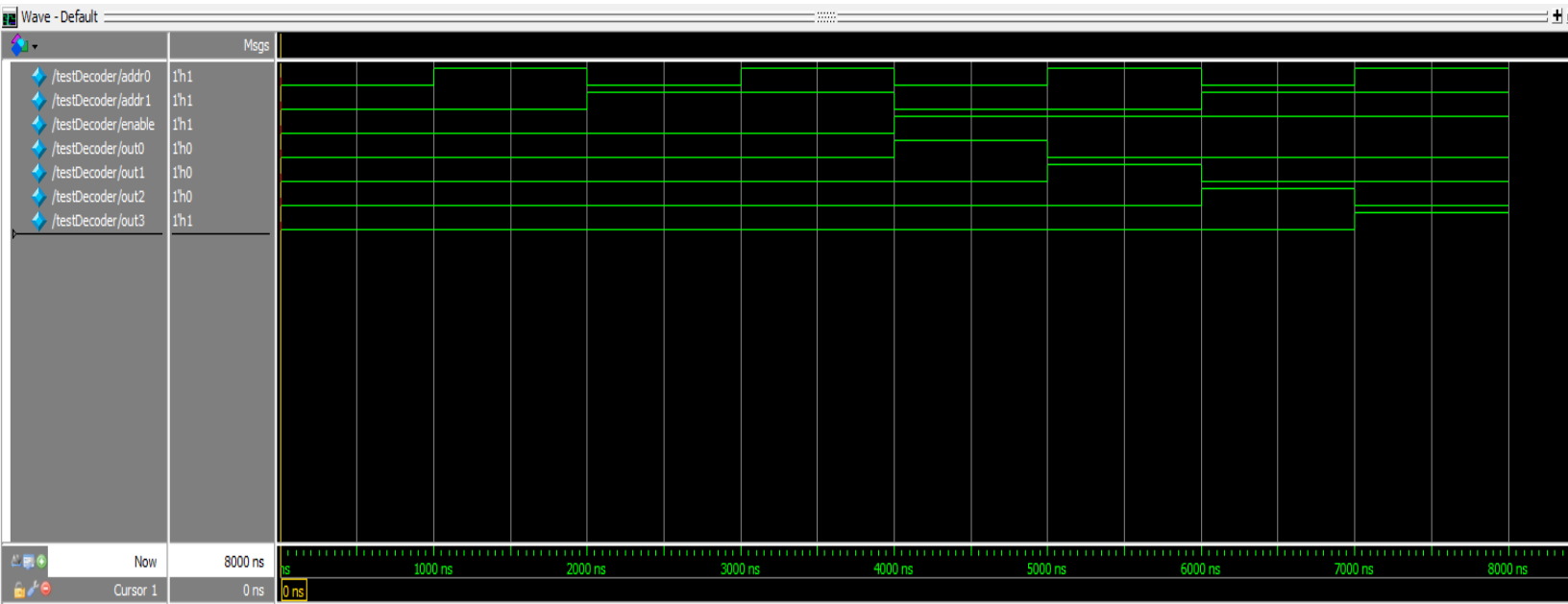
HW 2

Decoder:

Results

Enabled	A0	A1	Out 0	Out 1	Out 2	Out 3	Expected			
0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	1	0	0	0
1	1	0	0	1	0	0	0	1	0	0
1	0	1	0	0	1	0	0	0	1	0
1	1	1	0	0	0	1	0	0	0	1

Waveform

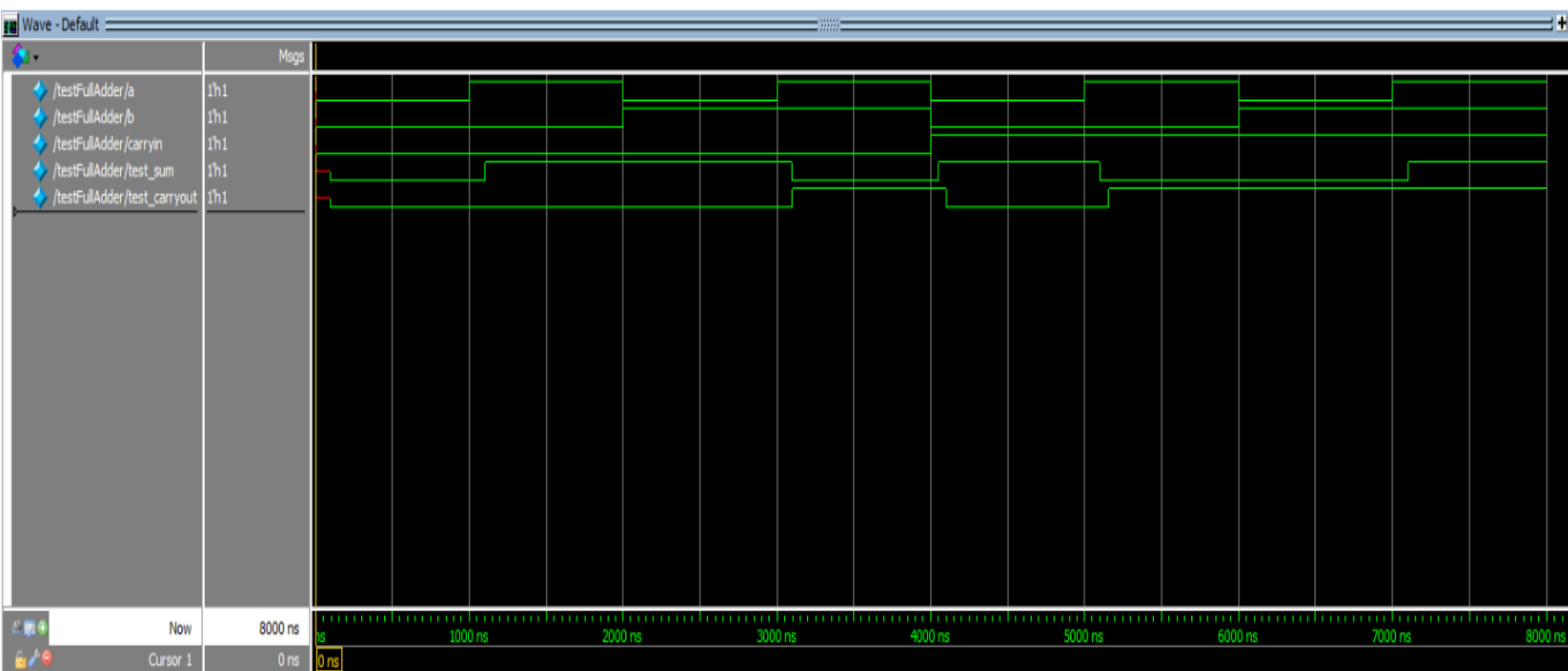


Adder:

Results

Carry In	A	B	Sum	Carry Out	Expected	
0	0	0	0	0	0	0
0	1	0	1	0	1	0
0	0	1	1	0	1	0
0	1	1	0	1	0	1
1	0	0	1	0	1	0
1	1	0	0	1	0	1
1	0	1	0	1	0	1
1	1	1	1	1	1	1

Waveform



Multiplexer:

Results

Address 0	Address 1	In 0	In 1	In 2	In 3	Out	Expected
0	0	1	0	0	0	1	1
0	0	0	1	1	1	0	0
1	0	0	1	0	0	1	1
1	0	1	0	1	1	0	0
0	1	0	0	1	0	1	1
0	1	1	1	0	1	0	0
1	1	0	0	0	1	1	1
1	1	1	1	1	0	0	0

Waveform

