STM32MP15XXAC_1DDR3Lx16

Schematics not complete, only for layout example

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U_STM32MP15XXAC_1DDR3Lx16-Top STM32MP15XXAC_1DDR3Lx16-Top.SchDoc

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

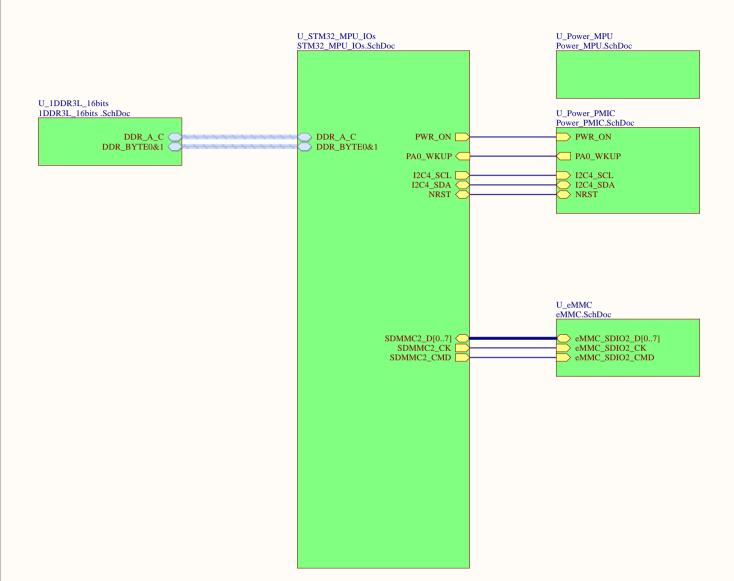
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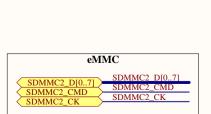
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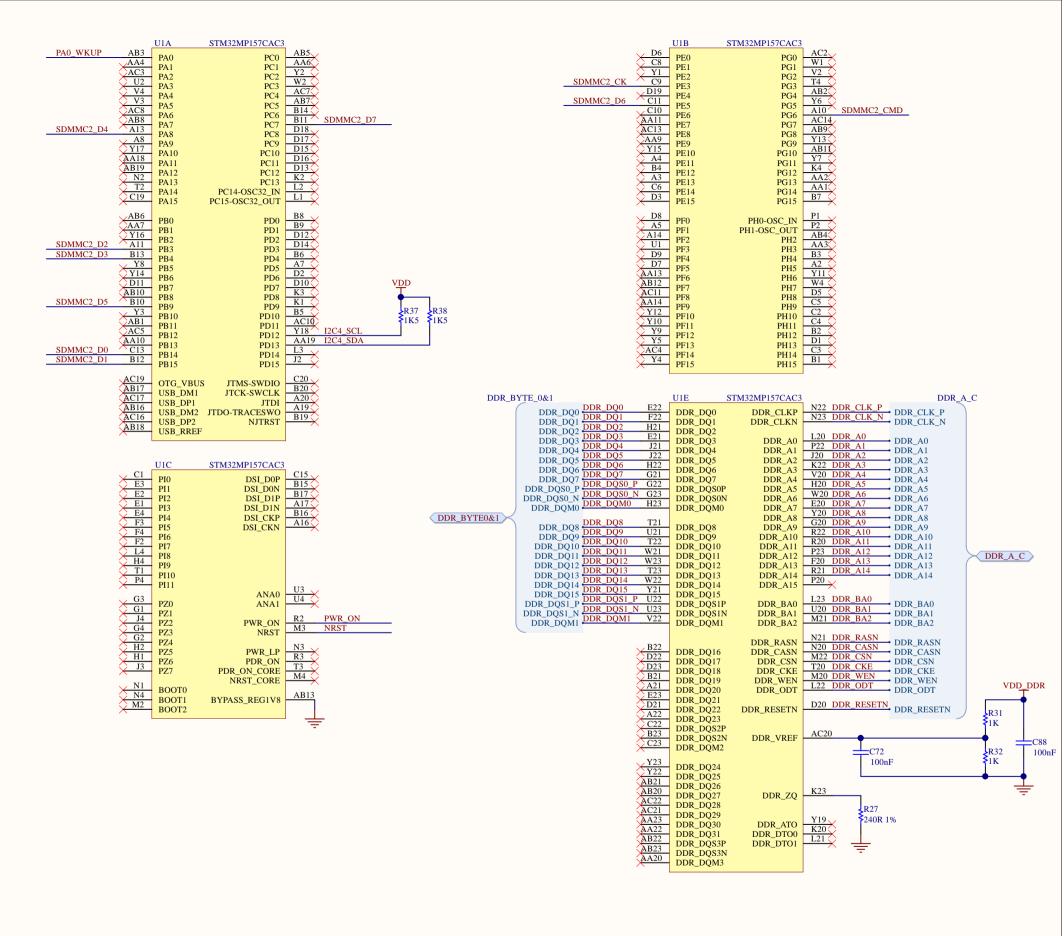
	32MP15XXAC_1DDR3		
roject: STN			
ariant: Exa			
evision: A-01		Reference:	life.augmented
ize: A4	Date: 06-JUN-19	Sheet: 1 of 7	mie.augmented



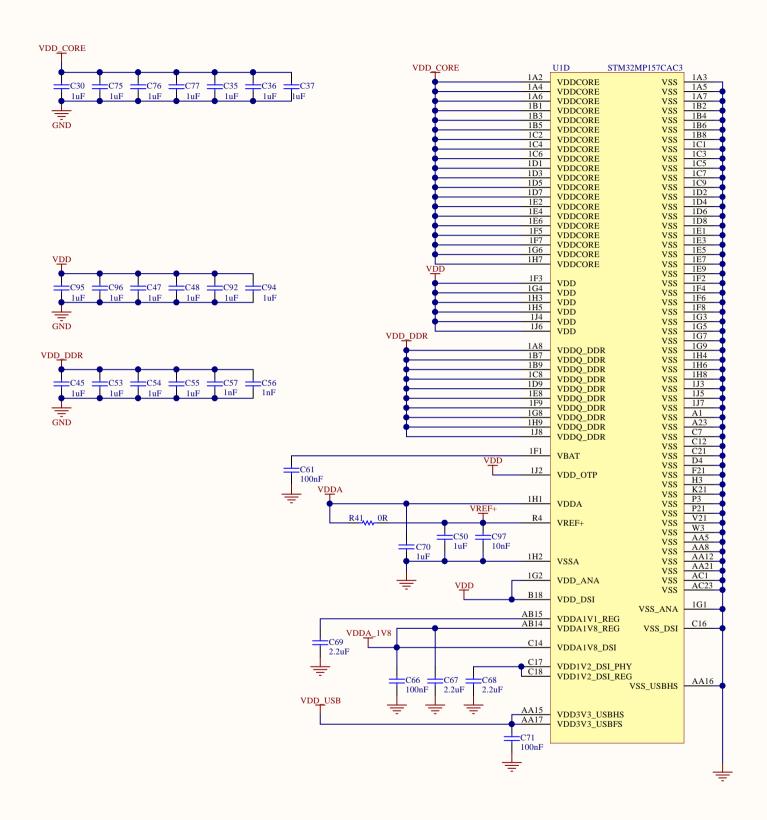
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Project: ST	M32MP15XXAC_1DD	R3Lx16	
Variant: Exa	mple		
Revision: A	01	Reference:	
Size: A4	Date: 06-ILIN-10	Sheet: 2 of 7	life.augmente



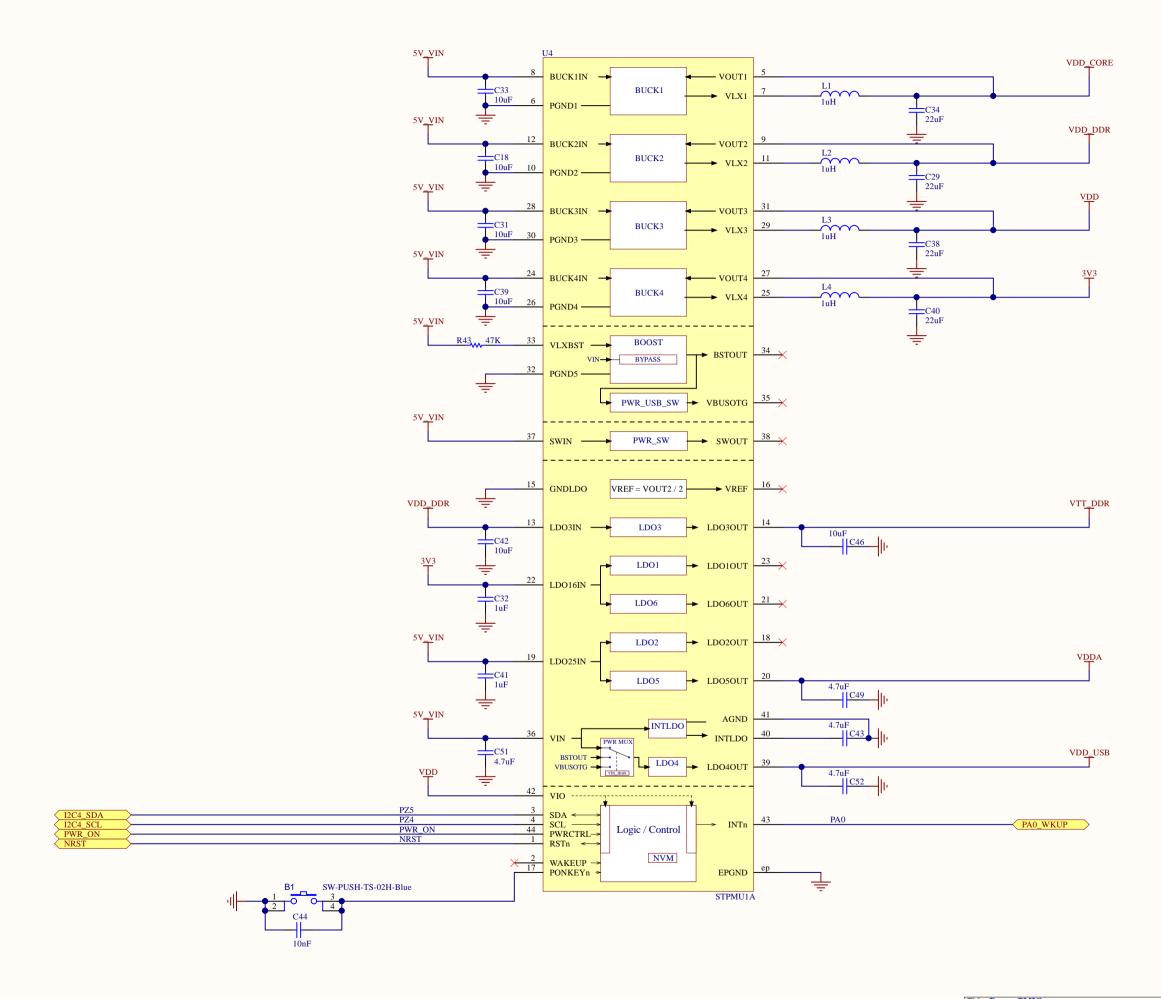
POWER	
PWR ON	PWR_ON
NRST	NRST
INK31	
PAO WKUP	PA0_WKUP
I2C4 SDA	I2C4_SDA
12C4_SDA 12C4_SCL	I2C4_SCL
12C4_SCL	



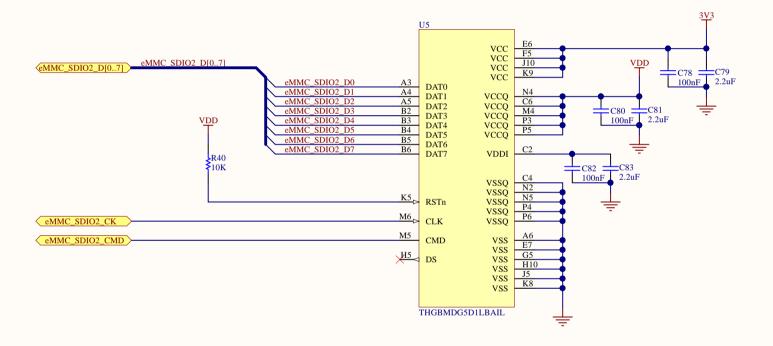
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Variant: Exa	mple		
Revision: A-01		Reference:	126-
Size: A3	Date: 06-JUN-19	Sheet: 3 of 7	life.augmente



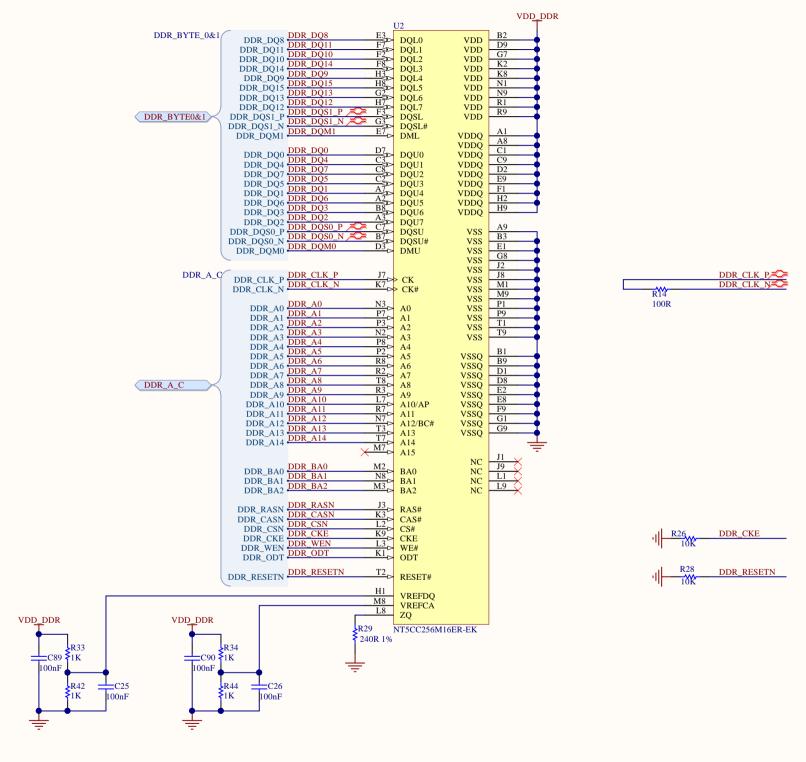
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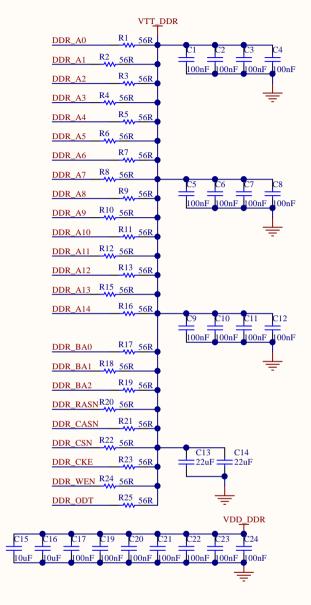


Title: Power	PMIC		
Project: STN	432MP15XXAC_1DI	OR3Lx16	
Variant: Exa	mple		
Revision: A	-01	Reference:	
Size: A3	Date: 06-JUN-19	Sheet: 5 of 7	life.augme



Title: eMM			
Project: STN			
Variant: Exa	mple		
Revision: A	-01	Reference:	1//
Size: A3	Date: 06-JUN-19	Sheet: 6 of 7	life.augmented







Title: 1DDF	R3L_16bits		
Project: STI	M32MP15XXAC_1DD	R3Lx16	
Variant: Exa	mple		
Revision: A	01	Reference:	
Size: A3	Date: 06-JUN-19	Sheet: 7 of 7	life.augmen