

Block Diagram:

Daniel Mestres, Natanael Santiago, Francisco Rosario

Base Components

Pipeline Registers

Control Unit

Changes

Enables -

- 1 - ID_shift_imm
- 2 - ID_ALU_op
- 3 - ID_load_instr
- 4 - ID_RF_enable
- 5 - EX_RF_enable
- 6 - MEM_RF_enable
- 7 - WB_RF_enable

Data Hazards by Load inst. -

- 8 - Disable PC
- 9 - Nop insertion selection
- 10 - EX_load_instr
- 11 - EX_Rd

Forwarding Operand -

- 12 - MEM_load_instr

Data Hazards -

- 13 - Control_Hazard
- 14 - ID_B_instr
- 15 - Condition Codes
- 16 - Instruction Condition

