

# Prosthesis control system using STM32

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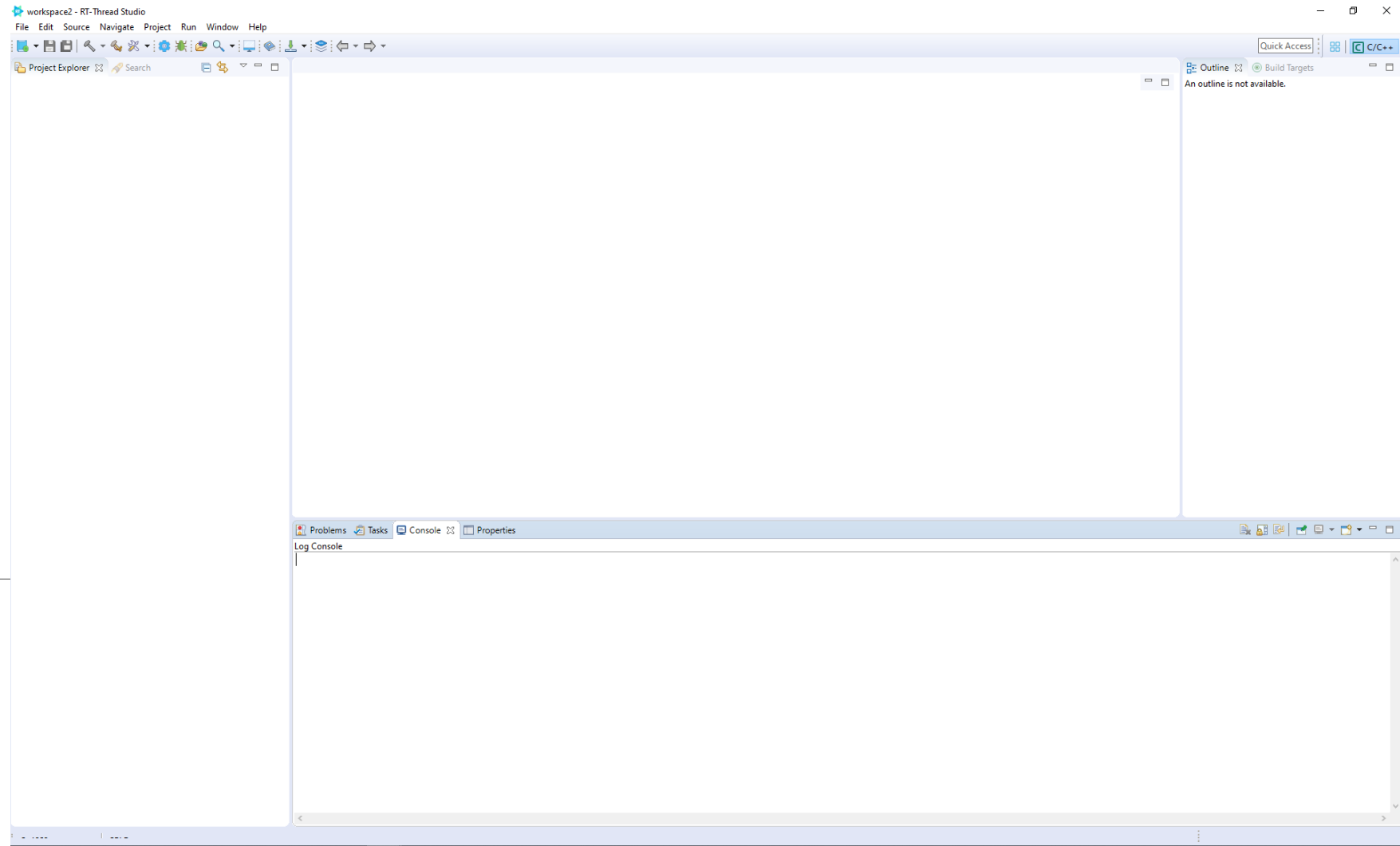
## Development Guide

### Authors:

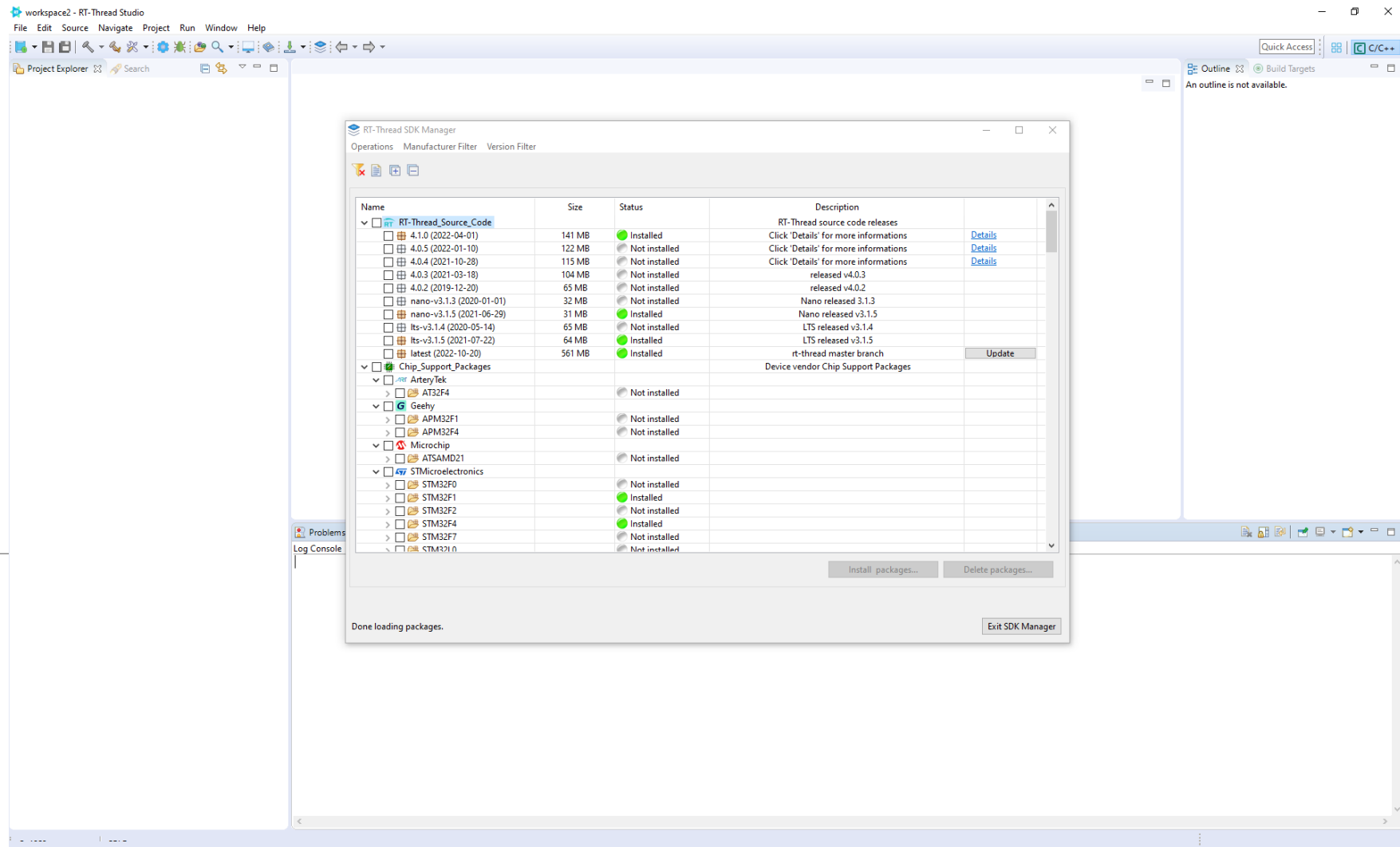
- Ph.D. Victor Asanza
- Daniel Montoya



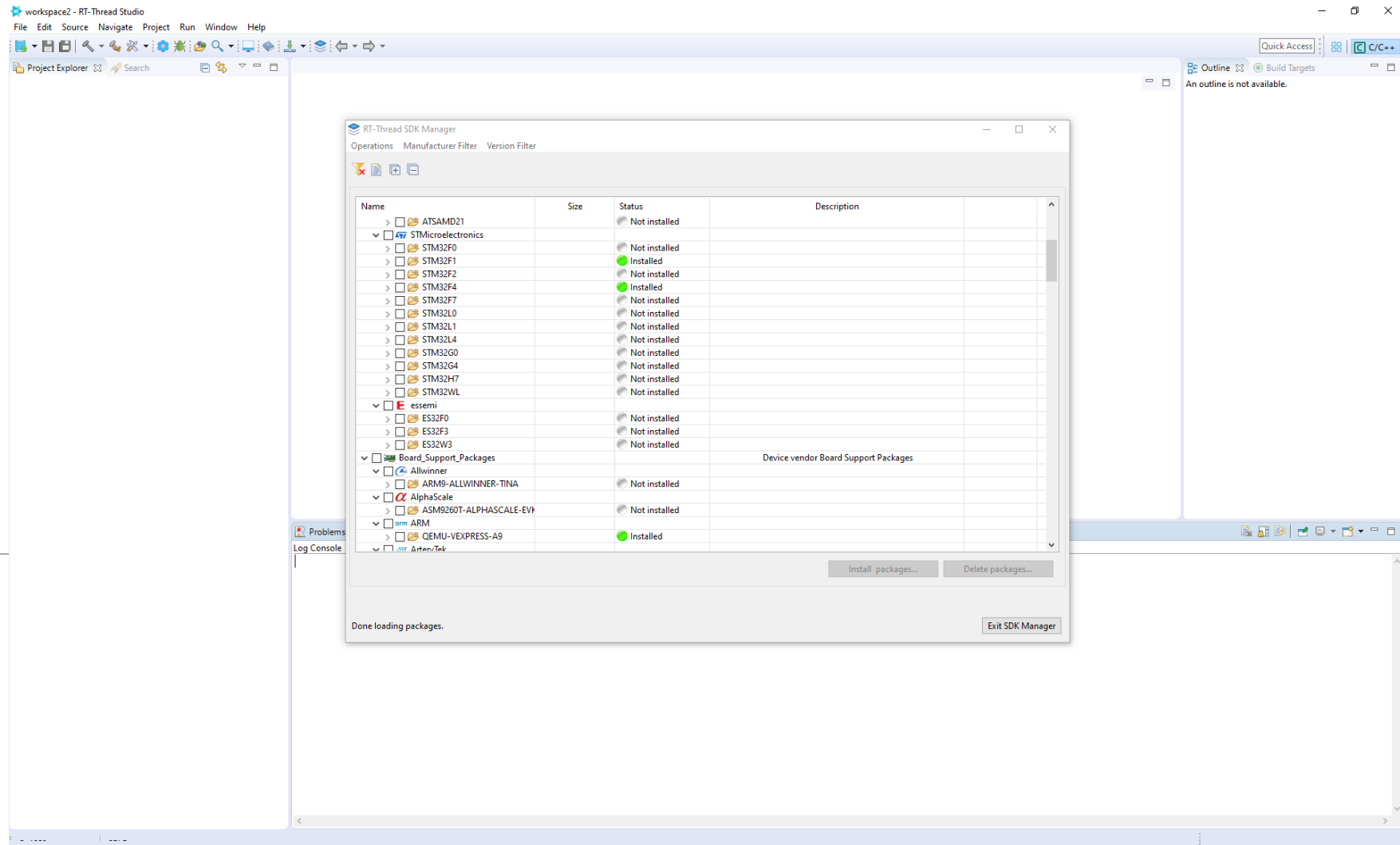
# 1. Start RT-Thread Studio



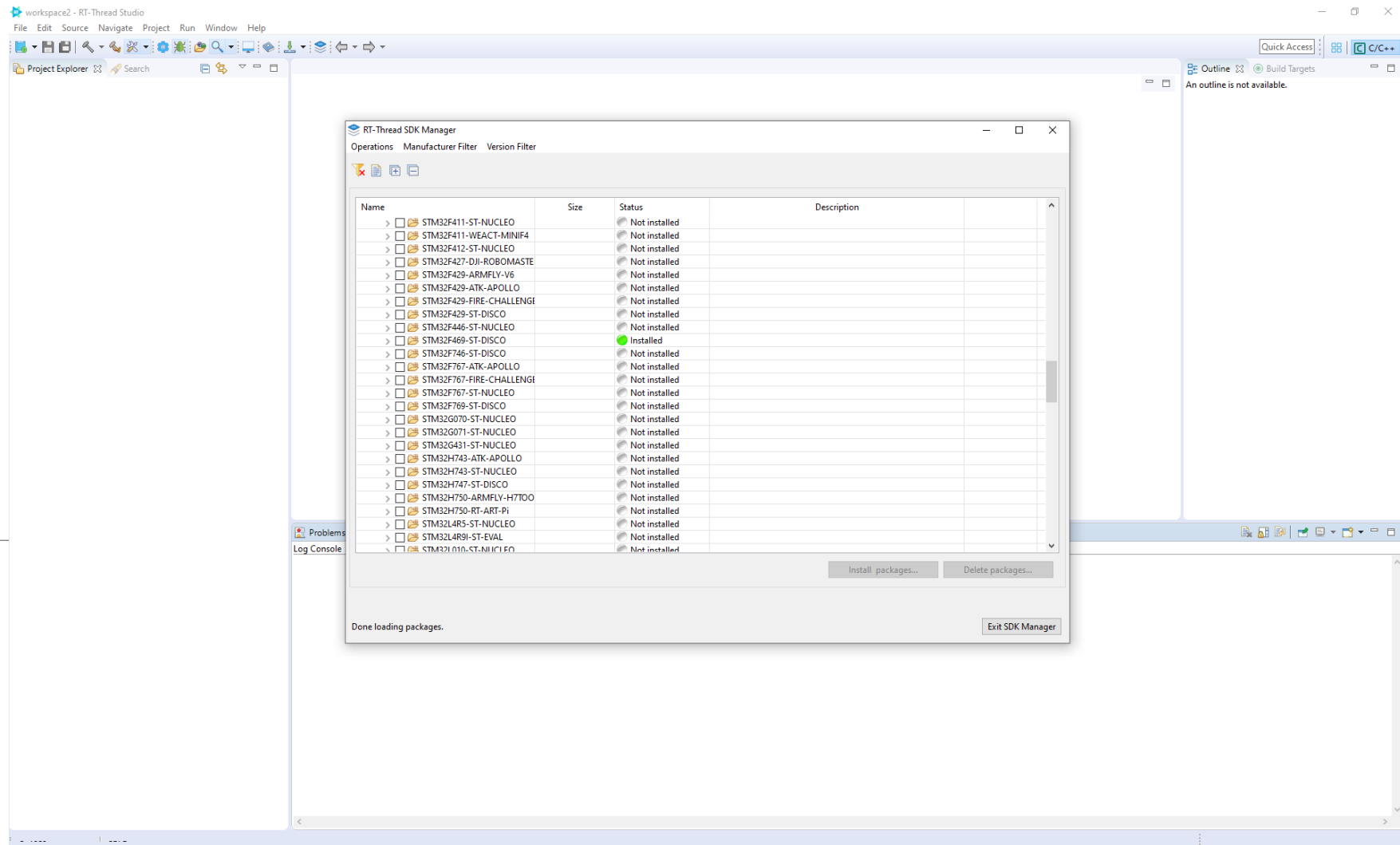
## 2. Check if the RT-Thread packages are updated



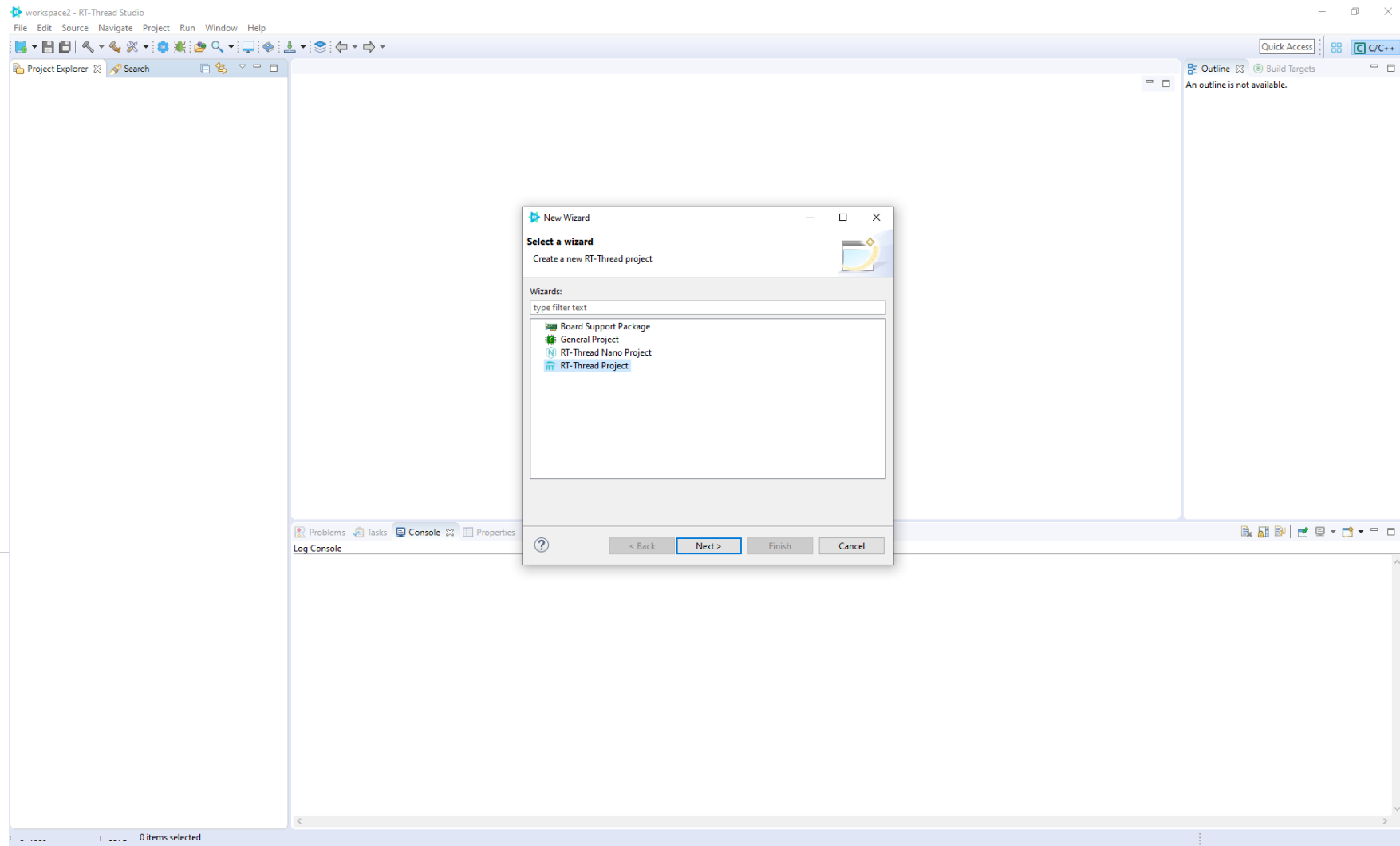
### 3. Verify or install the packages for the microcontroller to use



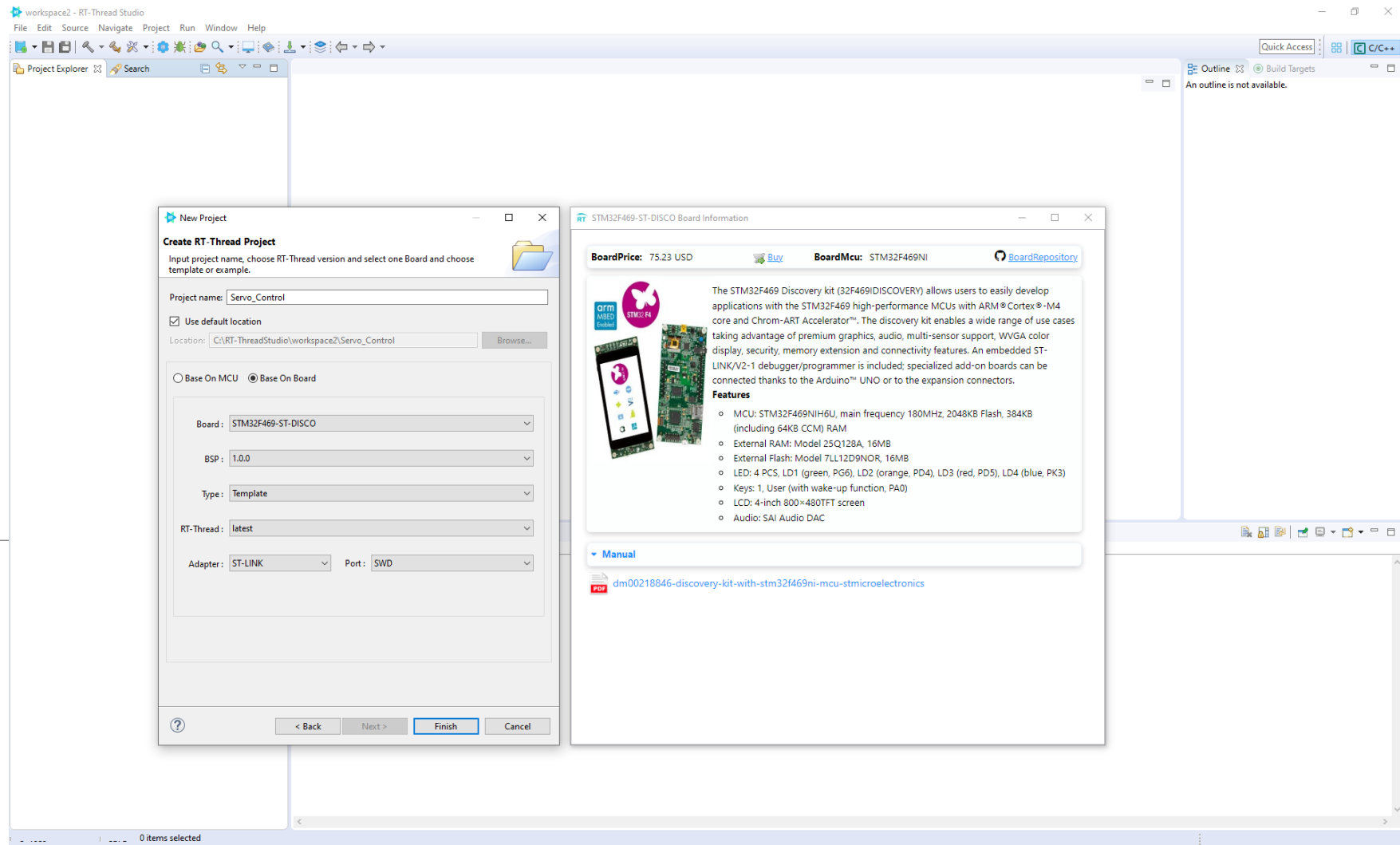
#### 4. Verify or install the packages for the development board.



## 5. Create an RT-Thread project.



## 6. Assign a name to the project and choose the development board to use.



## 7. Enable PWM device drivers and device timers

workspace2 - Servo\_Control/Kconfig - RT-Thread Studio

File Edit Source Navigate Project Run Window Help

Project Explorer Search

**Servo\_Control [Active - Debug]**

- RT-Thread Settings
- Board Information
- Binaries
- Includes
- applications
  - lcd\_init.c
  - main.c
  - SConscript
- board
  - CubeMX\_Config
  - linker\_scripts
  - board.c
  - board.h
  - Kconfig
  - SConscript
- Debug
- figures
- libraries
  - HAL\_Drivers
  - STM32F4xx\_HAL
    - Kconfig
- packages
- rt-thread [latest]
- rtconfig.h
- README.md

main.c stm32f4xx\_hal\_msp.c board.h stm32f4xx\_hal\_conf.h drv\_pwm.c kservice.c \*RT-Thread Settings

Kernel Components Packages Hardware

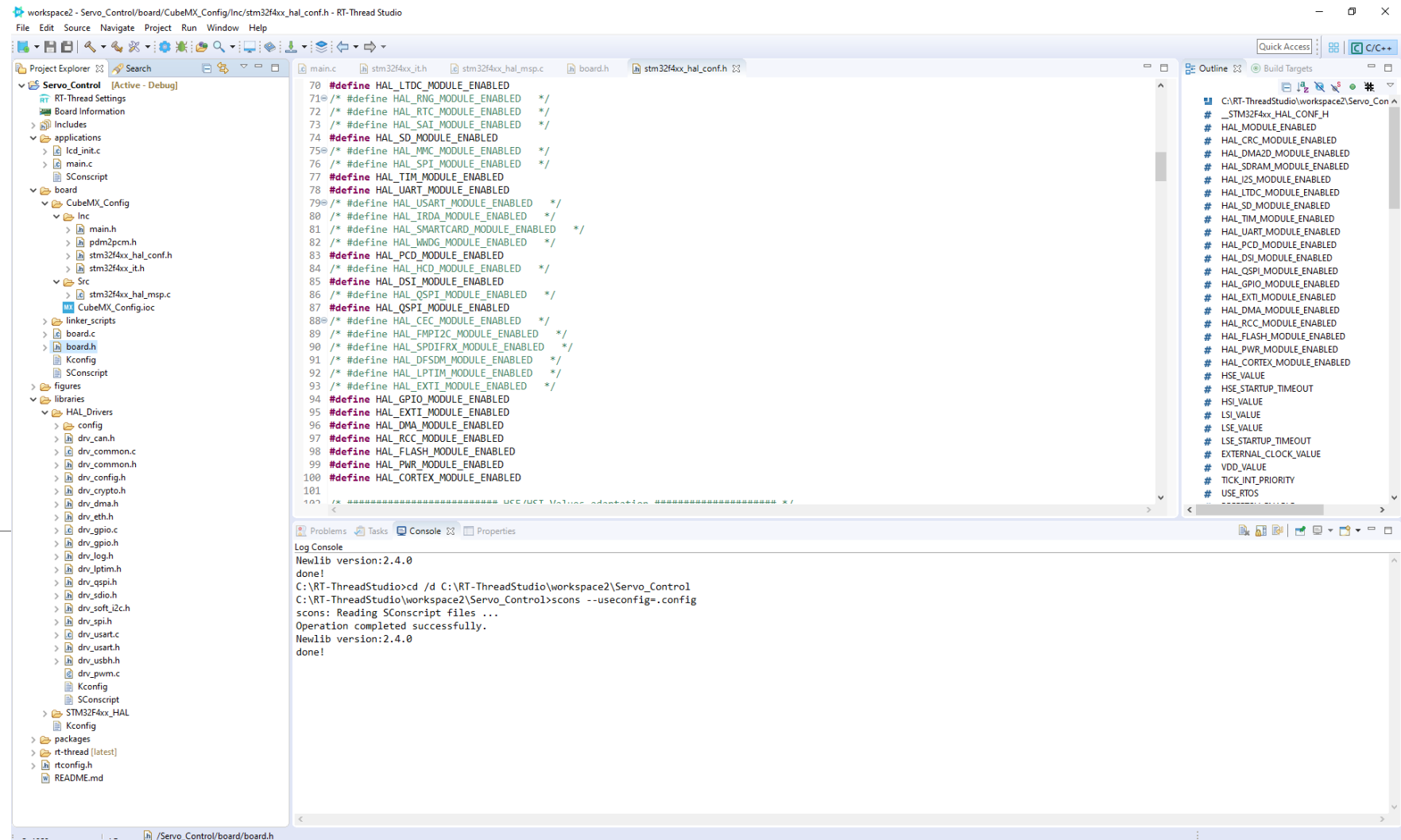
Property	Value
▶ Using device drivers IPL	<input checked="" type="checkbox"/>
▶ Using serial device drivers	<input checked="" type="checkbox"/>
Using CAN device drivers	<input type="checkbox"/>
Using hardware timer device drivers	<input checked="" type="checkbox"/>
Enable CPU time for high resolution clock counter	<input type="checkbox"/>
Using I2C device drivers	<input type="checkbox"/>
Using ethernet phy device drivers	<input type="checkbox"/>
Using generic GPIO device drivers	<input checked="" type="checkbox"/>
Using ADC device drivers	<input type="checkbox"/>
Using DAC device drivers	<input type="checkbox"/>
Using PWM device drivers	<input checked="" type="checkbox"/>
Using MTD Nor Flash device drivers	<input checked="" type="checkbox"/>
Using MTD Nand Flash device drivers	<input type="checkbox"/>
Using Power Management device drivers	<input type="checkbox"/>
Using RTC device drivers	<input type="checkbox"/>
▶ Using SD/MMC device drivers	<input checked="" type="checkbox"/>
Using SPI Bus/Device device drivers	<input type="checkbox"/>
Using Watch Dog device drivers	<input type="checkbox"/>

>>

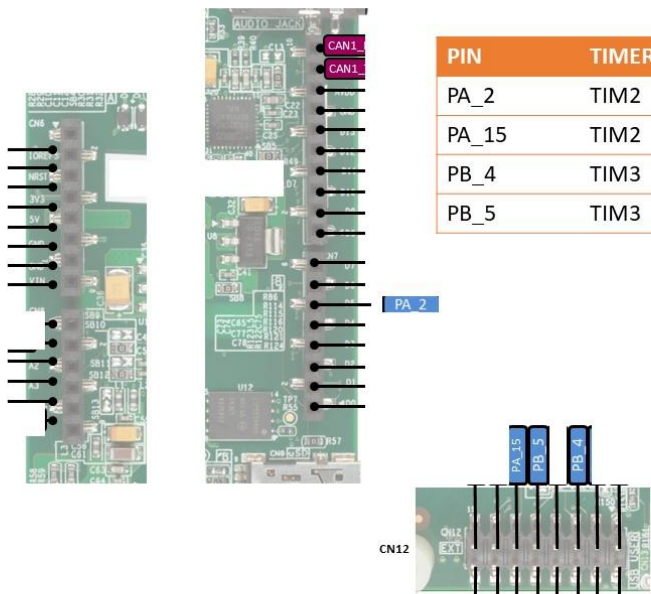
[RT\_USING\_HWTIMER]



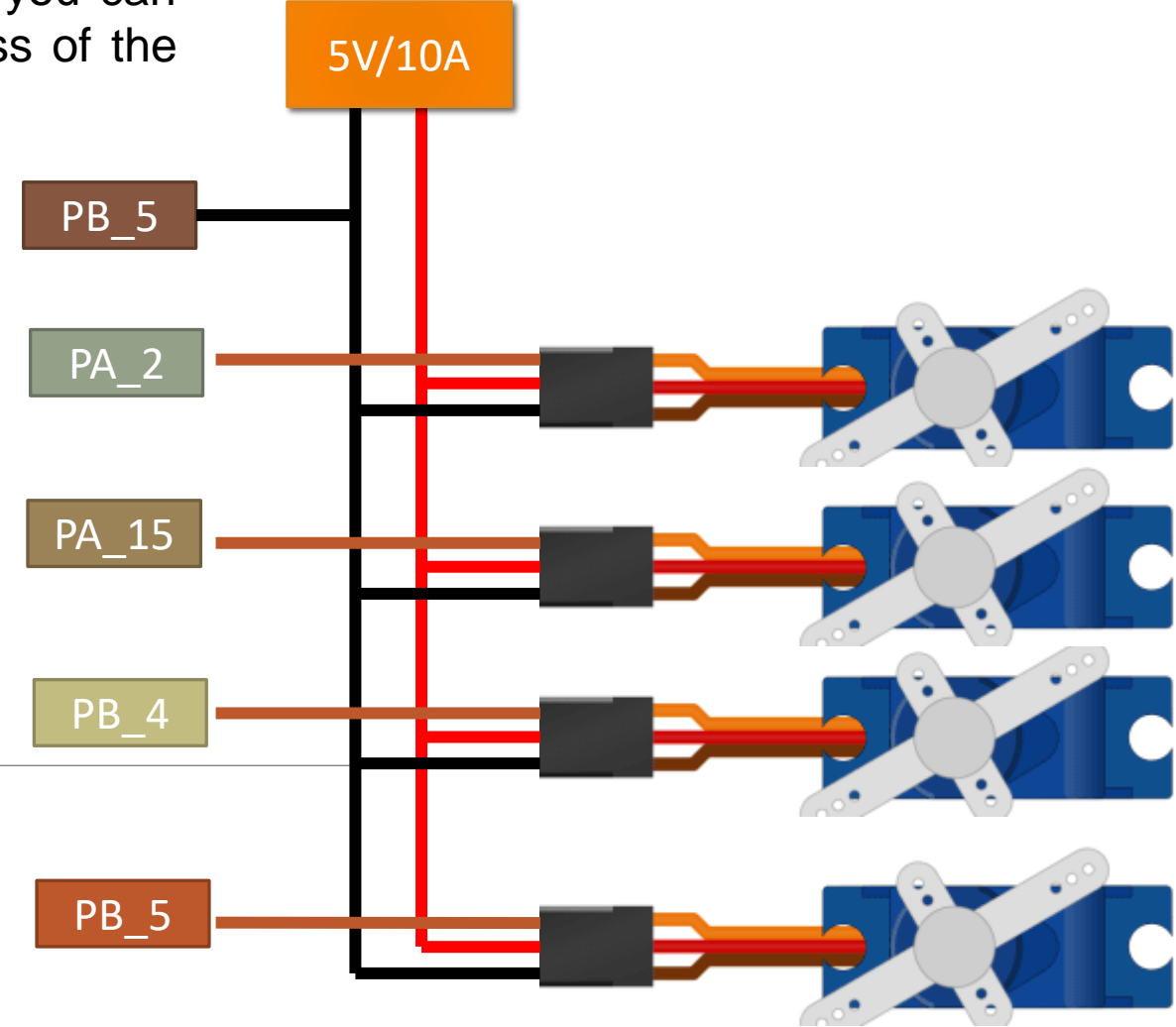
8. Check if the hardware timers and PWM have been enabled on the development board, so we go to the board



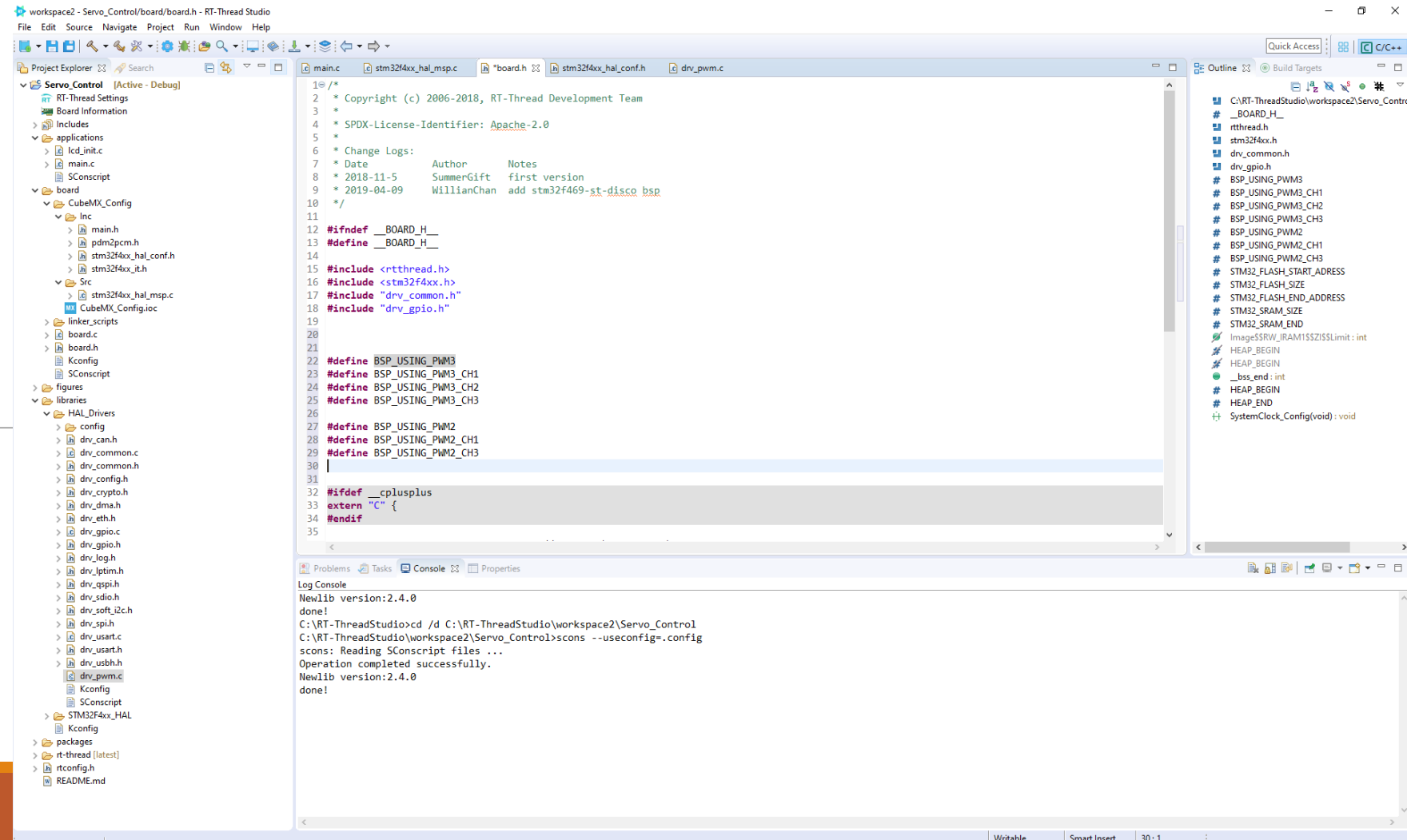
9. Check the documentation of the pins with which you can make the PWM output. Therefore obtain the address of the timers and the corresponding channel..



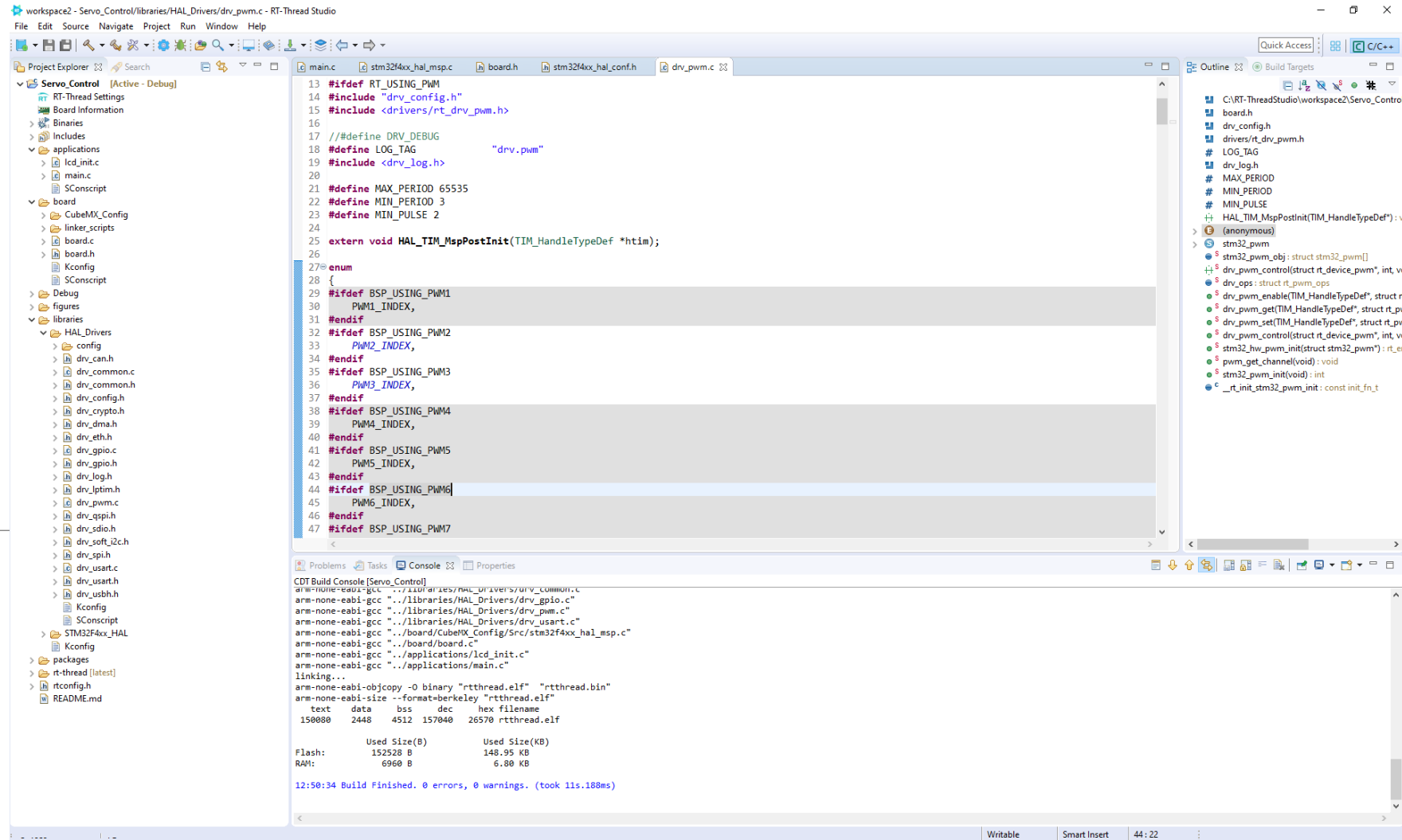
PIN	TIMER	CHANNEL	FINGER
PA_2	TIM2	CH3	Middle
PA_15	TIM2	CH1	Thumb
PB_4	TIM3	CH1	Index
PB_5	TIM3	CH2	Ring / Little



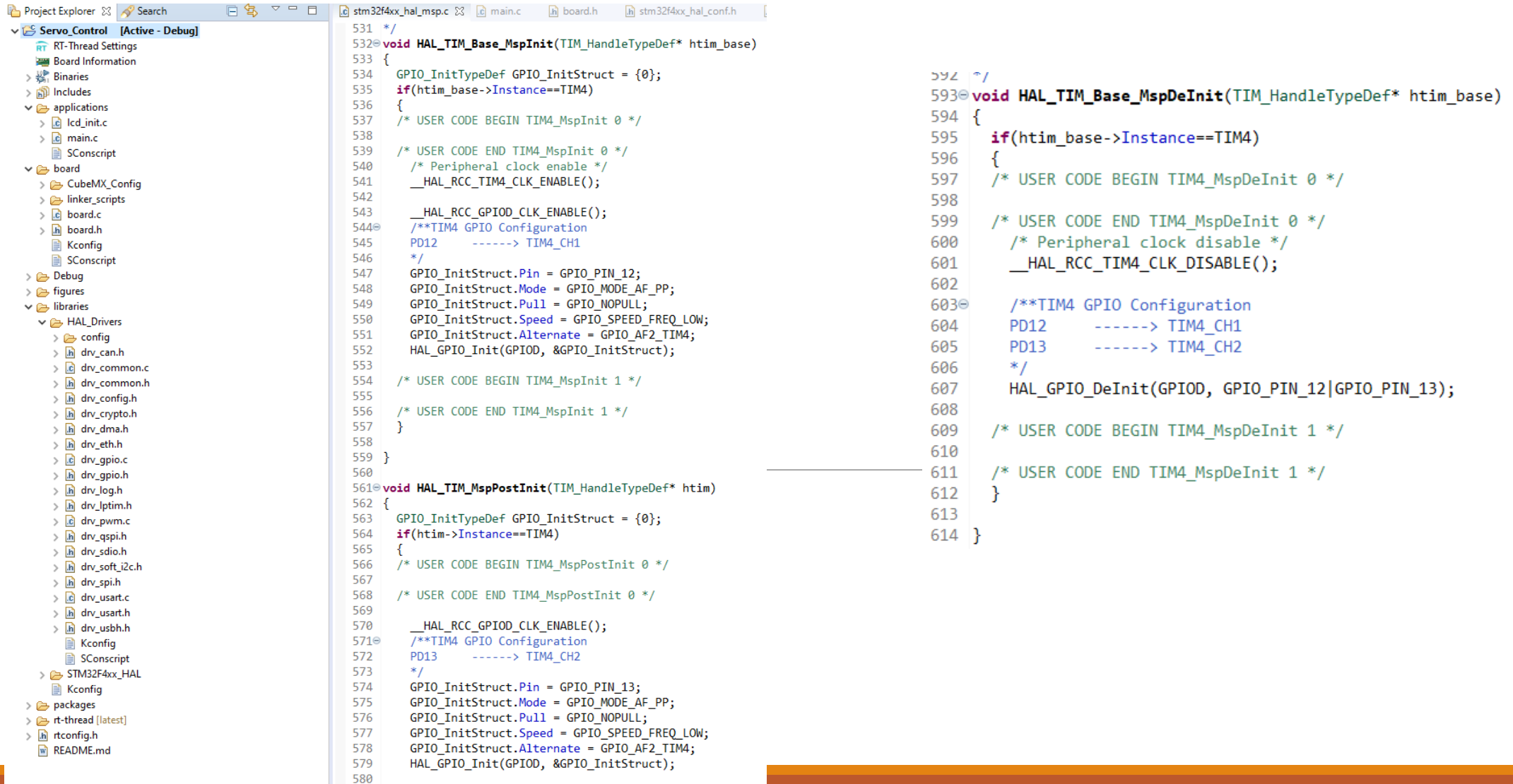
10. To activate the timers and the respective channels that are going to be used, we go to the board folder, and enter the board.h header file. In which we will include the definitions of PWM outputs 2 and 3, with their respective channels.



11. We check the PWM driver file and we can see that the specified PWM outputs have been activated.



12. We enter the MSP file of the microcontroller, where the PWM output pins are configured. By default we have the programming configured for timer 4 with another output pin.



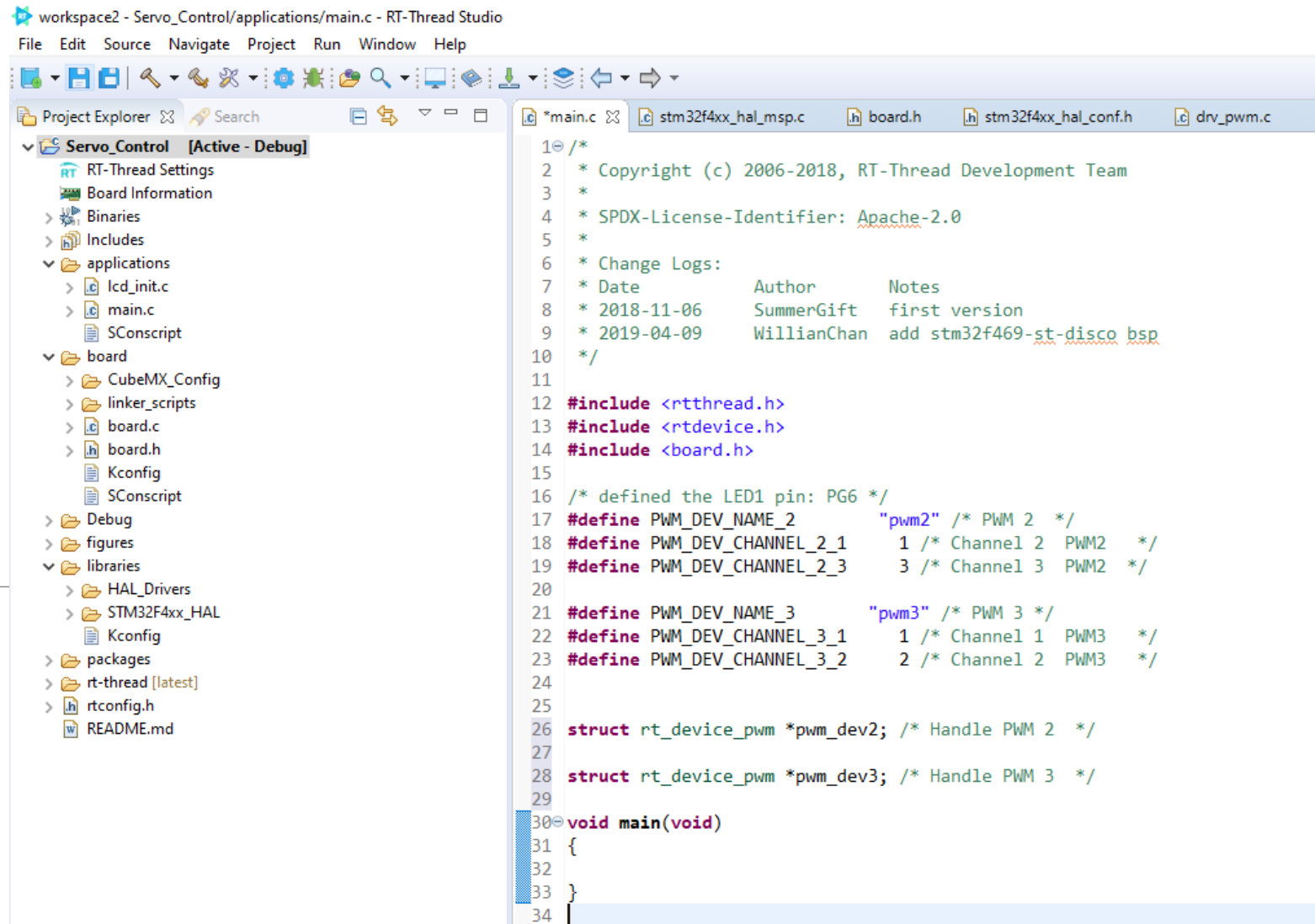
```
531 */
532 void HAL_TIM_Base_MspInit(TIM_HandleTypeDef* htim_base)
533 {
534     GPIO_InitTypeDef GPIO_InitStruct = {0};
535     if(htim_base->Instance==TIM4)
536     {
537         /* USER CODE BEGIN TIM4_MspInit 0 */
538
539         /* USER CODE END TIM4_MspInit 0 */
540         /* Peripheral clock enable */
541         __HAL_RCC_TIM4_CLK_ENABLE();
542
543         __HAL_RCC_GPIOD_CLK_ENABLE();
544         /**TIM4 GPIO Configuration
545          PD12      -> TIM4_CH1
546          */
547         GPIO_InitStruct.Pin = GPIO_PIN_12;
548         GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
549         GPIO_InitStruct.Pull = GPIO_NOPULL;
550         GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
551         GPIO_InitStruct.Alternate = GPIO_AF2_TIM4;
552         HAL_GPIO_Init(GPIOD, &GPIO_InitStruct);
553
554         /* USER CODE BEGIN TIM4_MspInit 1 */
555
556         /* USER CODE END TIM4_MspInit 1 */
557     }
558 }
559
560 void HAL_TIM_MspPostInit(TIM_HandleTypeDef* htim)
561 {
562     GPIO_InitTypeDef GPIO_InitStruct = {0};
563     if(htim->Instance==TIM4)
564     {
565         /* USER CODE BEGIN TIM4_MspPostInit 0 */
566
567         /* USER CODE END TIM4_MspPostInit 0 */
568
569         __HAL_RCC_GPIOD_CLK_ENABLE();
570         /**TIM4 GPIO Configuration
571          PD13      -> TIM4_CH2
572          */
573         GPIO_InitStruct.Pin = GPIO_PIN_13;
574         GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
575         GPIO_InitStruct.Pull = GPIO_NOPULL;
576         GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
577         GPIO_InitStruct.Alternate = GPIO_AF2_TIM4;
578         HAL_GPIO_Init(GPIOD, &GPIO_InitStruct);
579
580
581
582
583
584
585
586
587
588
589
590
591
592 */
593 void HAL_TIM_Base_MspDeInit(TIM_HandleTypeDef* htim_base)
594 {
595     if(htim_base->Instance==TIM4)
596     {
597         /* USER CODE BEGIN TIM4_MspDeInit 0 */
598
599         /* USER CODE END TIM4_MspDeInit 0 */
600         /* Peripheral clock disable */
601         __HAL_RCC_TIM4_CLK_DISABLE();
602
603         /**TIM4 GPIO Configuration
604          PD12      -> TIM4_CH1
605          PD13      -> TIM4_CH2
606          */
607         HAL_GPIO_DeInit(GPIOD, GPIO_PIN_12|GPIO_PIN_13);
608
609         /* USER CODE BEGIN TIM4_MspDeInit 1 */
610
611         /* USER CODE END TIM4_MspDeInit 1 */
612     }
613 }
614 }
```

13. Therefore, the configuration of timers 2 and 3 is carried out, activating and deactivating the clock configuration in their respective functions. Also in the initialization configuration of the pins. The function identifies the type of timer that is being used, for which the corresponding pins are initialized in their respective channels. These pins are PA15, PA2, PB4 and PB5.

```
532 void HAL_TIM_Base_MspInit(TIM_HandleTypeDef* htim_base)
533 {
534     GPIO_InitTypeDef GPIO_InitStruct = {0};
535     if(htim_base->Instance==TIM2)
536     {
537         /* Peripheral clock enable */
538         __HAL_RCC_TIM2_CLK_ENABLE();
539     }
540     else if(htim_base->Instance==TIM3)
541     {
542         /* Peripheral clock enable */
543         __HAL_RCC_TIM3_CLK_ENABLE();
544     }
545 }
546
547 void HAL_TIM_Base_MspDeInit(TIM_HandleTypeDef* htim_base)
548 {
549     if(htim_base->Instance==TIM2)
550     {
551         /* Peripheral clock disable */
552         __HAL_RCC_TIM2_CLK_DISABLE();
553     }
554     else if(htim_base->Instance==TIM3)
555     {
556         /* Peripheral clock disable */
557         __HAL_RCC_TIM3_CLK_DISABLE();
558     }
559 }
```

```
552 void HAL_TIM_MspPostInit(TIM_HandleTypeDef* htim)
553 {
554     GPIO_InitTypeDef GPIO_InitStruct = {0};
555     if(htim->Instance==TIM2)
556     {
557         __HAL_RCC_GPIOA_CLK_ENABLE();
558         /*TIM2 GPIO Configuration
559         PA15      -----> TIM2_CH1
560         PA2       -----> TIM2_CH3
561
562         */
563         GPIO_InitStruct.Pin = GPIO_PIN_15|GPIO_PIN_2;
564         GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
565         GPIO_InitStruct.Pull = GPIO_NOPULL;
566         GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
567         GPIO_InitStruct.Alternate = GPIO_AF1_TIM2;
568         HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
569     }
570     else if(htim->Instance==TIM3)
571     {
572         __HAL_RCC_GPIOB_CLK_ENABLE();
573         /*TIM3 GPIO Configuration
574         PB4       -----> TIM3_CH1
575         PB5       -----> TIM3_CH2
576
577         */
578         GPIO_InitStruct.Pin = GPIO_PIN_4|GPIO_PIN_5;
579         GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
580         GPIO_InitStruct.Pull = GPIO_NOPULL;
581         GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
582         GPIO_InitStruct.Alternate = GPIO_AF2_TIM3;
583         HAL_GPIO_Init(GPIOB, &GPIO_InitStruct);
584     }
585 }
```

14. In the main file, the corresponding Macros are defined, the PWM that will be used and the respective channels. PWM device structures are created to perform initialization.



The screenshot displays the RT-Thread Studio IDE interface. The left sidebar shows the Project Explorer with the 'Servo\_Control' project selected and the 'main.c' file highlighted. The main editor window shows the code for 'main.c', which includes copyright information, license details, and macro definitions for PWM devices and channels. The code is as follows:

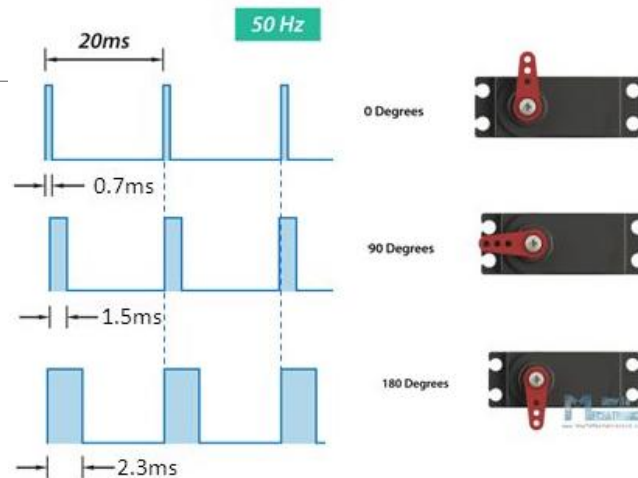
```
1  /*
2  * Copyright (c) 2006-2018, RT-Thread Development Team
3  *
4  * SPDX-License-Identifier: Apache-2.0
5  *
6  * Change Logs:
7  * Date       Author       Notes
8  * 2018-11-06   SummerGift   first version
9  * 2019-04-09   WillianChan   add stm32f469-st-disco bsp
10 */
11
12 #include <rtthread.h>
13 #include <rtdevice.h>
14 #include <board.h>
15
16 /* defined the LED1 pin: PG6 */
17 #define PWM_DEV_NAME_2      "pwm2" /* PWM 2 */
18 #define PWM_DEV_CHANNEL_2_1 1 /* Channel 2 PWM2 */
19 #define PWM_DEV_CHANNEL_2_3 3 /* Channel 3 PWM2 */
20
21 #define PWM_DEV_NAME_3      "pwm3" /* PWM 3 */
22 #define PWM_DEV_CHANNEL_3_1 1 /* Channel 1 PWM3 */
23 #define PWM_DEV_CHANNEL_3_2 2 /* Channel 2 PWM3 */
24
25
26 struct rt_device_pwm *pwm_dev2; /* Handle PWM 2 */
27
28 struct rt_device_pwm *pwm_dev3; /* Handle PWM 3 */
29
30 void main(void)
31 {
32
33 }
34
```



15. In the main function, uint32 variables corresponding to the period of the PWM signal in units of ns are defined, and the servo positions are based on the servo motor specification.

In this case the servomotor works at a frequency of 50Hz. Therefore, the value of the period is as the frequency is obtained, in the same way the value of the positions are according to the necessary pulse width.

```
30 void main(void)
31 {
32     rt_uint32_t period, pos1, pos2;
33
34     period = 20000000; /* Period for servo MG90S */
35
36     pos1 = 700000; /* PWM Pulse width degree value , single position by nanosecond ns */
37
38     pos2 = 2000000; /* PWM Pulse width degree value , single position by nanosecond ns */
39
40 }
41
```



$$frequency = \frac{1}{period} = \frac{1}{20000000ns} = 50Hz$$



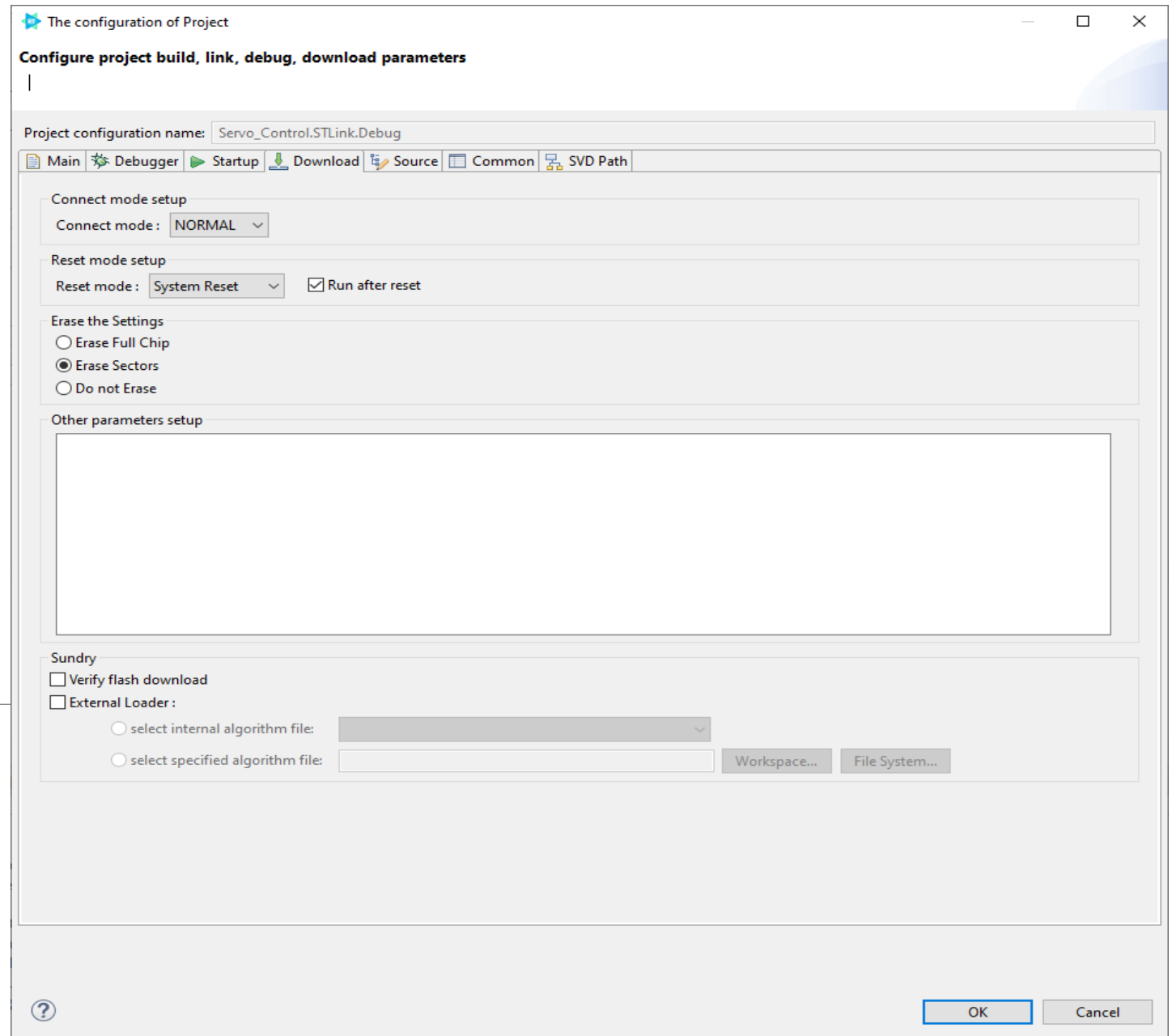
16. The search function is used to find device identifiers according to the name of the PWM device, which were defined in the macros  
For each of the PWM channels, its values are set, the device to be managed, the respective channel, the period and the pulse.  
In addition, the activation of the channels of the devices is carried out.

```
30 void main(void)
31 {
32     rt_uint32_t period, pos1, pos2;
33
34     period = 20000000; /* Period for servo MG90S */
35
36     pos1 = 700000; /* PWM Pulse blunt wide degree value , single position by nanosecond ns */
37
38     pos2 = 2000000; /* PWM Pulse blunt wide degree value , single position by nanosecond ns */
39
40     pwm_dev2 = (struct rt_device_pwm *)rt_device_find(PWM_DEV_NAME_2);
41
42     pwm_dev3 = (struct rt_device_pwm *)rt_device_find(PWM_DEV_NAME_3);
43
44     /* Set up PWM */
45
46     rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_1, period, pos2);
47     rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_3, period, pos1);
48     rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_1, period, pos2);
49     rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_2, period, pos1);
50
51     /* Enable Channel */
52     rt_pwm_enable(pwm_dev2, PWM_DEV_CHANNEL_2_1);
53     rt_pwm_enable(pwm_dev2, PWM_DEV_CHANNEL_2_3);
54     rt_pwm_enable(pwm_dev3, PWM_DEV_CHANNEL_3_1);
55     rt_pwm_enable(pwm_dev3, PWM_DEV_CHANNEL_3_2);
56 }
57
```

17. An infinite loop is established, in which the position of the fingers is changed in order to open and close the hands. This change is made every 5 seconds, and the status of the fingers is printed on the terminal. Some of the outputs are inverted, since the servomotors have different positions.

```
58     while (1)
59     {
60
61         rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_1, period, pos2); /*Thumb*/
62         rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_3, period, pos1); /*Middle*/
63         rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_1, period, pos2); /* Index*/
64         rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_2, period, pos1); /*Ring / Little */
65
66
67         rt_kprintf("Open Fingers \n");
68
69         rt_thread_mdelay(5000);
70
71         rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_1, period, pos1); /*Thumb*/
72         rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_3, period, pos2); /*Middle*/
73         rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_1, period, pos1); /* Index*/
74         rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_2, period, pos2); /*Ring / Little */
75
76
77
78         rt_kprintf("Close Fingers \n");
79         rt_thread_mdelay(5000);
80
81     }
82 }
```

18. In the download configuration of the program, it is specified that a system reset is performed before uploading it to the board.



19. The program is loaded onto the board and we can see on the console that the program is running. If the terminal is opened, the outputs are displayed indicating the position of the fingers.

The screenshot displays the RT-Thread Studio IDE. The main window shows the `main.c` file with the following code:

```
38 pos2 = 2000000; /* PWM Pulse blunt wide degree value, single position by nanosecond ns */
39
40 pwm_dev2 = (struct rt_device_pwm *)rt_device_find(PWM_DEV_NAME_2);
41
42 pwm_dev3 = (struct rt_device_pwm *)rt_device_find(PWM_DEV_NAME_3);
43
44 /* Set up PWM */
45
46 rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_1, period, pos2);
47 rt_pwm_set(pwm_dev2, PWM_DEV_CHANNEL_2_3, period, pos1);
48 rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_1, period, pos2);
49 rt_pwm_set(pwm_dev3, PWM_DEV_CHANNEL_3_2, period, pos1);
50
51 /* Enable Channel */
52 rt_pwm_enable(pwm_dev2, PWM_DEV_CHANNEL_2_1);
53 rt_pwm_enable(pwm_dev2, PWM_DEV_CHANNEL_2_3);
54 rt_pwm_enable(pwm_dev3, PWM_DEV_CHANNEL_3_1);
55 rt_pwm_enable(pwm_dev3, PWM_DEV_CHANNEL_3_2);
56
```

The console window shows the following output:

```
ST-LINK SN : 066CFF505567494867245644
ST-LINK FW : V2140M27
Board : 32F469IDISCOVERY
Voltage : 3.24V
SMD freq : 4000 KHz
Connect mode: Normal
Reset mode : Software reset
Device ID : 0x434
Revision ID : Rev A
Device name : STM32F469xx/F467xx
Flash size : 2 MBytes
Device type : MCU
Device CPU : Cortex-M4
Memory Programming ...
Opening and parsing file: rtthread.elf
File : rtthread.elf
Size : 152868 Bytes
Address : 0x08000000
Erasing memory corresponding to segment 0:
Erasing internal memory sectors [0 5]
Download in Progress:
Progress: 100%
File download complete
Time elapsed during download operation: 00:00:03.937
MCU Reset
Software reset is performed
RUNNING Program ...
Address : 0x8000000
Application is running
Start operation achieved successfully
completed, elapse time :4091ms.
```

The terminal window shows the following output:

```
\ | /
- RT - Thread Operating System
/ | \ 5.0.0 build Oct 20 2022 13:51:50
2006 - 2022 Copyright by RT-Thread team
Open Fingers
msh />Close Fingers
Open Fingers
Close Fingers
Open Fingers
```