Instituto Tecnológico de Costa Rica
Área Académica de Ingeniería en Computadores

Proyecto de Diseño en Ingeniería en Computadores

Design Document v1

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February 26, 2018

Table 1: Revision History

Date	Version	Description	Author
02 March 2018	1.0	Design Document for Design of (ASIPs) for Approximate Computing	Daniel Moya

1 Introduction

1.1 Purpose

The primary purpose of this document is to present a detailed description of the design elements of an ASIP.

1.2 Scope

This project is going to be implemented...

Future users will be able to...

This project will be implemented between ...

1.3 Context

constraints

Table 2: Definitions

Term	Definition
ASIP	Application Specific Instruction Set Processor. This means that, although the processor can execute a wide range of applications, it is optimized for a specific one, in which it can execute with improved performance (for instance, energy consumption or execution time would be lower) compared to a General Purpose Proccesor (GPP).
GPP	General Purpose Proccesor. In general, they show better flexibility than ASIPs because all the programs are executed in general-purpose components, but since they are not optimized, they show less resource efficiency.
ASIC	Application Specific Integrated Circuit. In general, they show better performance results than ASIPs, nevertheless, they are less flexible when executing anything other than the specific application they are meant to.
ITCR	Instituto Tecnológico de Costa Rica. Place from where this project is being developed.

1.4 Summary

References

2 Glossary

3 Composition

4 Logical

5 Dependency

6 Information

7 Patterns

8 Interfaces

8.1 User interface

9 Structure