

INSTITUTO TECNOLÓGICO DE COSTA RICA
ÁREA ACADÉMICA DE INGENIERÍA EN COMPUTADORES
PROYECTO DE DISEÑO EN INGENIERÍA EN COMPUTADORES



Progress report #1 for the project Design of (ASIPs) for Approximate Computing

Chair for Embedded System (CES)
Karlsruhe Institute of Technology (KIT)
Period: 19/02/2018 (week 2) - 23/02/2018 (week 4)

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1 Performed activities

1. **Get to know the software platform:** Several laboratory scripts have been followed to get to know the software tools from which the ASIPs are going to be build. However, some server errors have slowed down the process which implies a little change in the schedule, as explained in section 2. This laboratory scripts consist of several exercises and questions (an answer sheet is available for comparison) to get a student through all the necessary knowledge for building ASIPs, from the basics of an assembly program to an audio application which needs to be optimized.
2. **Find appropriate error-tolerant applications:**
3. **Redact Project Plan document:** The project plan document was revised and corrected according to the professor's observations.
4. **Redact Requirements document:** The requirements document was redacted and send to the professor for his possible pre-review.
5. **Redact Design document:** The design document is currently being redacted and it is expected that it could be delivered to the professor on friday to obtain a possible pre-review.

2 Change of scope/activities

The activity ID 01 "Get to know the software platform" was affected by server errors like permissions and general configuration of the environment. The corresponding laboratory sessions made for this task are not completely finished, nevertheless, it is expected to work on the sessions on parallel to the activity ID 02, because they do not depend on each other, also, the main concepts have been already learned from the currently done laboratory sessions.

3 Gained value analysis

Table 1: Revision History

Activity ID	Activity	Budget	%Planned Value	PV	AC	%Completed work	EV	CPI	SPI	Initial planned date	Ending date	Initial real date	Real ending
01	Get to know the software platform	100,000	50%	PV	AC	%Trabajo completado	EV	CPI	SPI	Fecha inicio planeada	Finalización planeada	Fecha inicio real	Finalización real
04	Redact Project Plan document	100,000	50%	PV	AC	%Trabajo completado	EV	CPI	SPI	Fecha inicio planeada	Finalización planeada	Fecha inicio real	Finalización real
05	Redact Requirements document	100,000	50%	PV	AC	%Trabajo completado	EV	CPI	SPI	Fecha inicio planeada	Finalización planeada	Fecha inicio real	Finalización real
06	Redact Design document	100,000	50%	PV	AC	%Trabajo completado	EV	CPI	SPI	Fecha inicio planeada	Finalización planeada	Fecha inicio real	Finalización real

References