

Date	Description of Work	Hours
5-Nov	Conitnued work on 2 layer board layout	1
10-Nov	Updated Pull up resistors/schematic and schematic review of Continue work on 2 Layer PCB layout. Identified issues with	0.5
14-Nov	routing/layout with current configuration (Discused in Added 4 Layer PCB, Mounting Holes, Rearranged layout to	0.5
17-Nov	match closer to dev board, routed power section, changed	3.5
Total:		5.5