

Date	Description of Work	Hours
24-Oct	Started Footprint creation for P1 and other IC's	1
26-Oct	Pad design for most footprints	1.5
30-Oct	Initial Footprint creation completed/ Design Rules Check	1
1-Nov	Connector Placement on PCB. Set up design rules for placement	0.5
3-Nov	Fixed outstanding schematic issues from intial review	0.75
4-Nov	Audio, Power, Charger Circuitry placed on PCB. Updated connector placement	1.5
Total:		6.25