

Risc V Reference Card

Core Instruction Formats

31	25	24	20	19	15	14	11	7	6	0	
funct7		rs2		rs1		funct3		rd		opcode	R-type
	imm[11:0]			rs1		funct3		rd		opcode	I-type
	imm[11:5]	rs2		rs1		imm[4:0]		rd		opcode	S-type
	imm[12:10:5]	rs2		rs1		imm[4:1 11]		rd		opcode	B-type
			imm[31:12]					rd		opcode	U-type
			imm[20:10:1 11 19:12]					rd		opcode	J-type

Bibliography

- [1] C. A. R. Hoare. Communicating sequential processes. *Communications of the ACM*, 21(8):666–677, 1978. ISSN 1557-7317.
- [2] B. Vinter and K. Skovhede. Synchronous message exchange for hardware designs. *Communicating Process Architectures*, pages 201–212, 2014.