Memoria DRAM 4k x 4 Bits , 4k = $2^2.2^{10}$ => 12 bits de endereços ou A11 a A0

A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	conteudo
0	0	0	0	0	0	0	0	0	0	0	0	11
0	0	0										11
0	0	0	1	1	1	1	1	1	1	1	1	11
0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0										0
0	0	0	1	1	1	1	1	1	1	1	1	0
0	0	1	0	0	0	0	0	0	0	0	0	8
0	0	1										8
0	0	1	1	1	1	1	1	1	1	1	1	8
0	1	0	0	0	0	0	0	0	0	0	0	10
0	1	0										10
0	1	0	1	1	1	1	1	1	1	1	1	10
0	1	1	0	0	0	0	0	0	0	0	0	7
0	1	1										7
0	1	1	1	1	1	1	1	1	1	1	1	7
1	0	0	0	0	0	0	0	0	0	0	0	8
1	0	0										8
1	0	0	1	1	1	1	1	1	1	1	1	8
1	1	0	0	0	0	0	0	0	0	0	0	9
1	1	0										9
1	1	0	1	1	1	1	1	1	1	1	1	9
1	1	1	0	0	0	0	0	0	0	0	0	3
1	1	1										3
1	1	1	1	1	1	1	1	1	1	1	1	3

RAS CAS