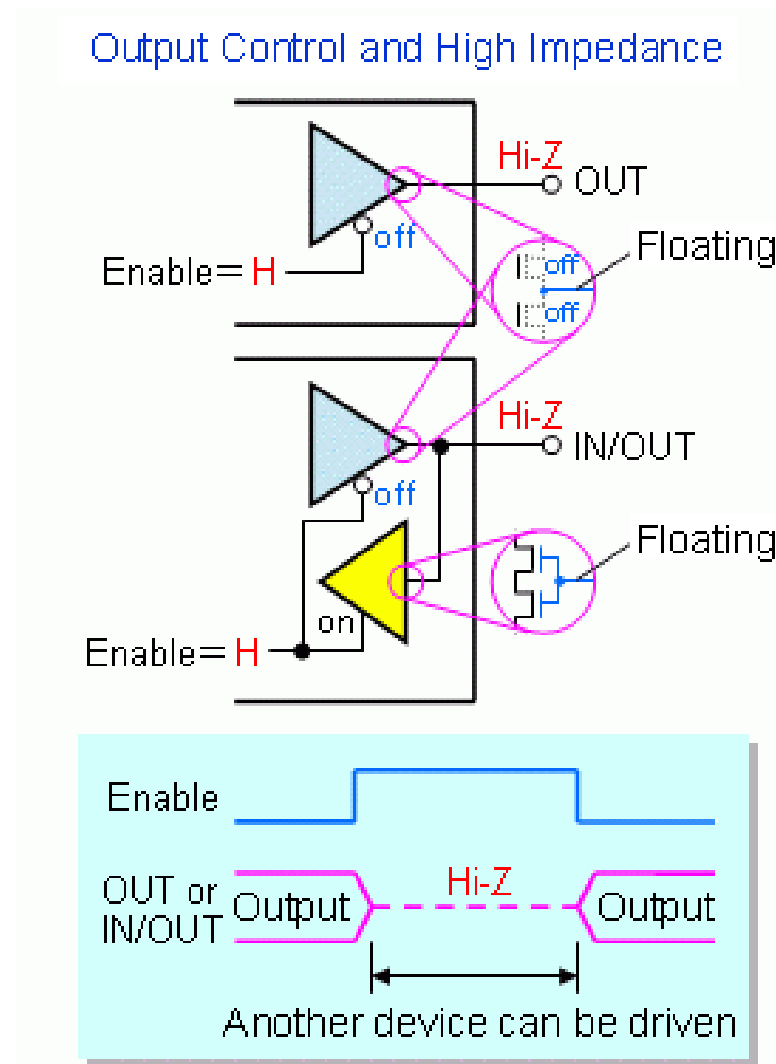
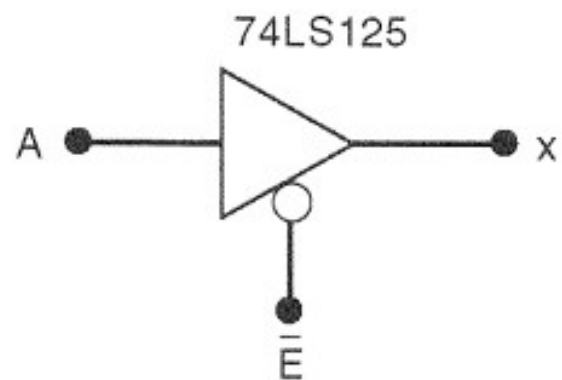


Circuito de alta Impedancia / Buffer tri-state

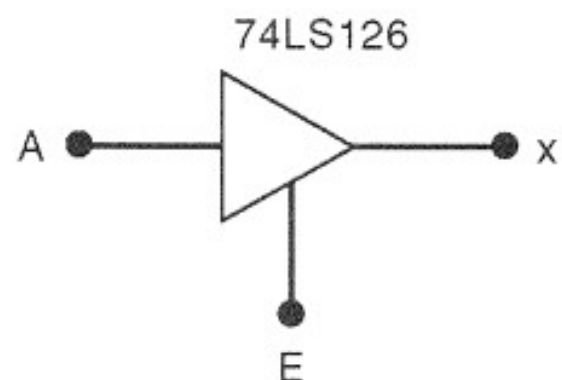




\bar{E}	x
0	A
1	Alta Impedância

2

(a)



E	x
0	Alta Impedância
1	A

2

(b)