CALIFORNIA STATE POLYTECHNIC UNIVERSITY, POMONA COLLEGE OF ENGINEERING

ECE 3301L Spring 2024 Session 1 Microcontroller Lab

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LAB2 Basic Input/Output using Microcontroller parallel ports

To perform the lab below, you need to download the spec of the processor PIC18F4620 that we are using in the lab. Go to the following link:

http://ww1.microchip.com/downloads/en/devicedoc/39626e.pdf

Remember that this processor is an upgrade to the processor PIC18f4321. It has the same peripheral hardware but its internal ROM/RAMs are larger.

Download the datasheet and save somewhere (like Desktop) that you can easily have access to it.

MATERIALS:

Provided by instructor:

• 2 RGB LEDs

Student must get the following:

- (4) 1K or higher resistors
- (4) regular LEDs (any color)
- 1 bank of 4 minimum DIP Switches

PART 1)

Write a program that will input from a bank of 4 switches (DIP switches) and output each corresponding input to an equivalent LED. There would be a total of 4 LEDs. The input switches are connected from PORT A bits 0-3 while the output LEDs are tied to PORT B bits 0-3.

Hint:

a) Use the downloaded datasheet of the PIC18F4620; go to chapter 9 'I/O Ports'. Study the definitions of the **TRISx** registers where 'x' represents the PORT name.

Set the right value of each bit in the TRIS register to setup whether the corresponding pin is an input or output.

A '1' will make the pin an input while a '0' makes it an output. Reading something from a switch to a pin of a port will make that pin an input. Controlling a LED from a pin of a port will make that pin an output. Based on those definitions, you should be able to determine what port should be setup as an input port or an output port. From there, you should be able to figure the value to be placed into the TRISx registers. In this case, you should deal with the registers TRISA and TRISB.

- b) If you use PORTA or PORTB for the inputs and/or outputs, you need to study the definitions of the register **ADCON1** (see the chapter 19 '10-bit Analog-to-Digital Converter A/D Module' in the datasheet) and its PCFG3:PCFG0 bits. You need to make sure that the pins from ports A and B are setup in <u>digital mode and not analog meaning all the pins must be digital.</u> We cover the purpose of this register on a later lab. For the purpose of this lab, we will load this register with the value of 0x0F.
- c) Write an endless loop to read the input switches and output the values to the LEDs. Based on the schematics shown on 'LAB2_Schematics.pdf', the four inputs should be port A bits 0 through 3 while the four LEDs should be connected to port B bits 0 through 3.

```
Example:
```

```
void main()
char in;
                                     // Use variable 'in' as char
       TRISA = 0x??;
                                     // fill out the ?? with the proper values
                                     // fill out the ?? with the proper values
       TRISB = 0x??;
       ADCON1 = 0x0F;
                                     //
       while (1)
               in = PORTA;
                                      // read data from PORTA and save it
                                      // into 'in'
                                     // Mask out the unused upper four bits
               in = in \& 0x0F;
                                     // while preserving the lower 4-bits
                                     // Output the data to PORTB
               PORTB = in:
       }
}
```

PART 2)

Connect the provided Common-Cathode RGB LED at D1 to the pins RC0 through RC2. Pin RC0 will be used to turn on the RED color of LED D1, RC1 is for the GREEN and RC2 is for the BLUE

Write an endless loop where the lowest three pins of the DIP Switch SW1 (pins connected to port A bits 0 through 2) are read and then these three bits are outputs to the port C whereas the following color will be generated on the LED D1:

Inputs (SW1)			O	Outputs (D1)			
RA2	RA1	RA0	RC2	RC1	RC0	Color	Color Value Reference
0	0	0	0	0	0	No light	0
0	0	1	0	0	1	Red	1
0	1	0	0	1	0	Green	2
0	1	1	0	1	1	Yellow	3
1	0	0	1	0	0	Blue	4
1	0	1	1	0	1	Purple	5
1	1	0	1	1	0	Cyan	6
1	1	1	1	1	1	White	7

Note: . Don't forget to add the TRISC command for this port C in your code.

Show the operation of this part by changing the switch settings of SW1 and check that the color of the RGB LED D1 matches with the combination of the switch. Make sure that the most significant bit of SW1 is on the left side of that switch and the LSB is on the right side.

PART 3)

Write an endless loop that will continuously generate the list of colors shown on Part 2) with about 1 second delay between each color. **There is no input to read**.

Hint:

Write the subroutine Delay_One_Sec() below:

```
#define delay 17000

void Delay_One_Sec()
{
    for (int I=0; I < delay; I++);
}</pre>
```

Once this delay routine is written, use it to generate the time delay. You would need an overall endless loop that will output the 8 different colors spaced by 1 second delay. To achieve this task, you will need to add a FOR loop with an 8-count value (count from 0 to 7). Use the counter value as the color of the output to be generated. In the FOR loop, after you have output the color to the port C, add the Delay_One_Sec() routine to wait 1 second. Failure to have this delay will cause the colors to be generated at a very high rate so that your eyes cannot differentiate the changing of the color resulting into the effect that the LED will always on with an apparent WHITE color.

In addition, on the schematics, bit 0 of PORT C is shown to be connected to channel 0 of the logic analyzer (see signal RC0). We are going to use that signal to measure the time duration of the delay routine Delay_One_Sec() provided above. The variable 'delay' in that routine was assigned with an arbitrary value of 17000. That would provide about 1 second delay. To be more accurate, we will use the logic analyzer to change that value in order to have the exact duration of 1 second.

Run the analyzer software 'Logic 2' when Part 3 is running. Capture all the channels on the analyzer. Pay attention to the channel 0. Stop the measurement and then use two markers, one placed at one rising edge of channel 0 while the other one at the negative edge. Measure the time difference between the two markers. If the time is not 1 second, change the value of the 'delay' with a new approximate value, recompile the program, download the new one to the board and rerun the program. Execute another analyzer capture and perform again the measurement. Repeat the process until a recorded time is 1 second with +/- 25 msec tolerance.

PART 4)

Now, this part will add one additional RGB LEDs D2. D2 has its three pins connected to PORTD bits 0 through 3. Here is the sequence of colors for the LEDs D1 and D2.

Step # Color @RGB LED D1 Color @RGB LED D2

0	Purple	Blue
1	White	Purple
2	Yellow	White
3	Cyan	No Light (off)
4	Red	Red
5	Green	Yellow
6	Blue	Cyan
7	No light (off)	Green

Due to the random assignments of the colors for both D1 and D2, it would be best to create arrays of values for each Step value and use the FOR loop to output the color value for each D1 and D2 on each Step.

To create an array of value, for each step, find the assigned color. Associate that color with the color value (provided on the chart on PART 2). Gather all the eight values and create then the array:

```
char array1[8] = { value1, value2, \dots};
```

whereas value1 and value2 are the color values

After both arrays are created for D1 and D2, use the index of the FOR loop as the offset of the array to retrieve the value of the colors for the steps. Output the values to the associated PORTs

PART 5)

Now we will use the logic analyzer probe to check our implementation. Connect the 6 channels 0 through 5 of the analyzer to the 6 pins that are the sources for the two RGB LEDs D1 and D2. See schematics.

Run Part 4). Capture the timing of the analyzer's channels. Make sure to adjust the capture window large enough so that all the eight steps of Part 4) are recorded.

Once the capture is complete, using a marker to slide over each step and check the logic state of each channel and create the following table:

```
Step # Ch2 Ch1 Ch0 Ch5 Ch4 Ch3
0 x x x x x x x x
1
```

'x' is the logic value observed.

Note that the sequence of the channels are placed with the higher number being on the left while the lower number is on the right and there are two groups, one for each RGB LED. This is to easily enumerate the digital value for the color.

Once the table is completed, translate the binary value into decimal value. Then, translate the decimal values into color values. Finally, verify that the recorded values match with the table indicated on Part 4)

General reminder:

Make sure that for this lab create a general folder marked as 'lab2'. From there, create sub-folders one for each part. Since we are going to have five parts, then we should have five sub-folders marked for example lab2p1, lab2p2, ... lab2p5.