StartBytes_Transmitter HDL Module Documentation

StartBytes_Transmitter module transmits four zero bytes via SPI to initiate the transmission correctly.

Signals

Signal	Direction	Width (bits)	Description
CLK	IN	1	Clock signal
NRST	IN	1	Synchronous reset. Active low
TX_COUNTER_MAX	IN	16	How many bytes to transmit. Assigned to 4
TX_COUNTER	OUT	16	Not used
NEXT_BYTE	IN	8	The next byte to transmit. Assigned to 0