



Certificate no: UC-b15e3b67-6561-4229-be3d-62ecd1bf1564

Certificate url: ude.my/UC-b15e3b67-6561-4229-be3d-62ecd1bf1564

Reference Number: 0004

CERTIFICATE OF COMPLETION

System Design using Verilog

Instructors **Dr. Yogesh Misra**

Danish Agnihotri

Date **Jan. 4, 2024**

Length **30.5 total hours**