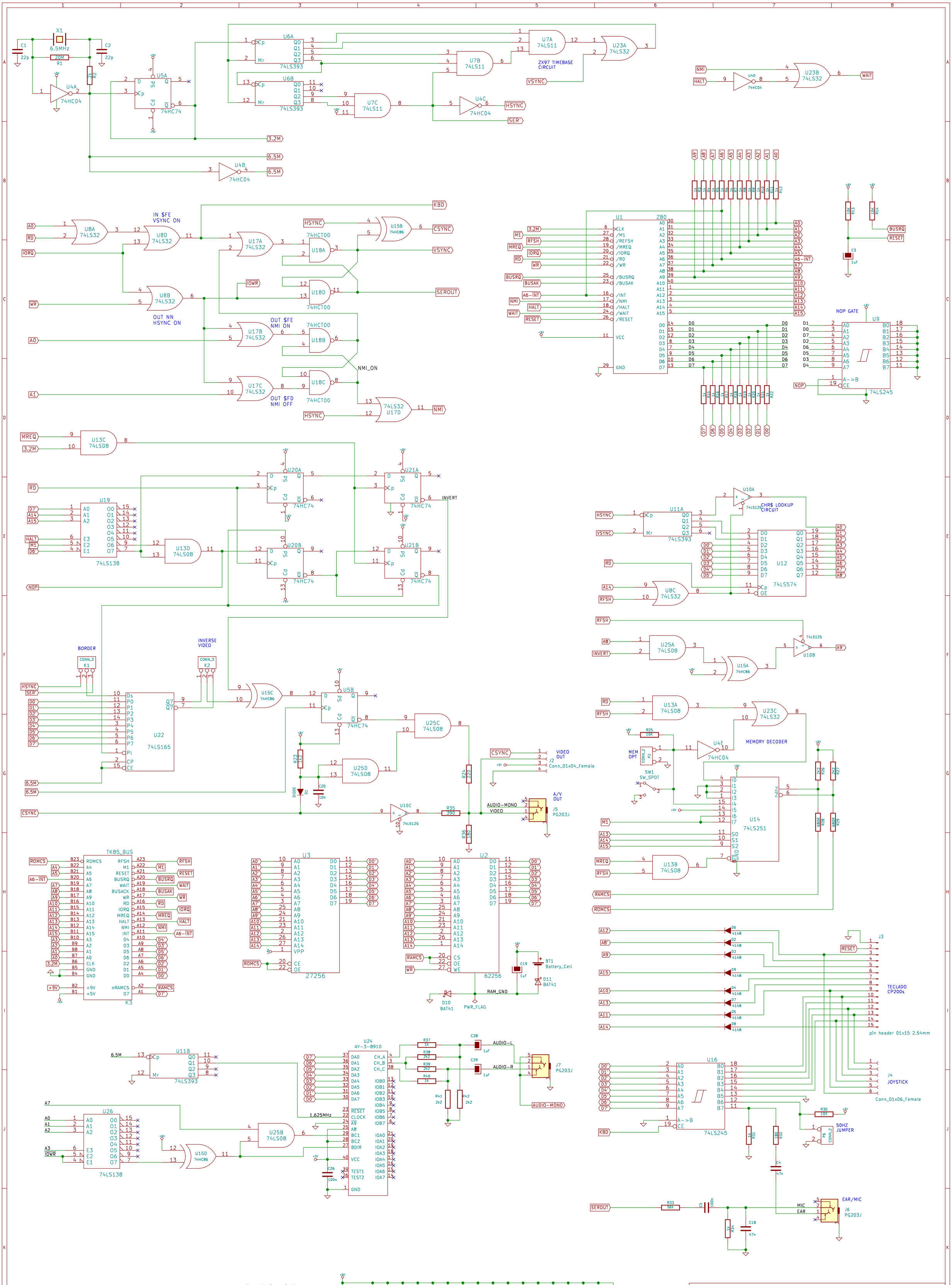
X1
6.5MHz
C1
22p
C2
22p
R1
20M
R2
2k
U4A
74HC04
U5A
74HC74
U6A
74LS393
U6B
74LS393
U7A
74LS11
U7B
74LS11
U7C
74LS11
U23A
74LS32
ZX97 TIMEBASE CIRCUIT
VSYNC
U4C
74HC04
HSYNC
SER'
3.2M
6.5M
U4B
74HC04
NMI
HALT
U4D
74HC04
U23B
74LS32
WAIT
U8A
74LS32
U8D
74LS32
IN $FE
VSYNC ON
U8B
74LS32
OUT NN
HSYNC ON
U17A
74LS32
U17B
74LS32
OUT $FE
NMI ON
U17C
74LS32
OUT $FD
NMI OFF
IOWR
74HCT00
U18A
U18B
U18C
U18D
U15B
74HC86
KBD
CSYNC
SEROUT
NMI_ON
74LS32
U17D
U13C
74LS08
U19
74LS138
U13D
74LS08
U20A
U20B
U21A
U21B
74HC74
INVERT
U1
Z80
U9
NOP GATE
74LS245
RESET
BUSRQ
U10A
74LS125
CHR$ LOOKUP CIRCUIT
U11A
74LS393
U12
74LS574
U8C
74LS32
U25A
74LS08
U15A
74HC86
U10B
U13A
74LS08
U23C
74LS32
MEMORY DECODER
U4E
74HC04
SW1
SW_SPDT
U14
74LS251
U13B
74LS08
BORDER
K1
INVERSE VIDEO
K2
U22
74LS165
U15C
74HC86
U5B
U25C
74LS08
U25D
74LS08
U10C
R35
390
R36
180
R24
220
R23
1k2
C20
10n
DIODE
D1
VIDEO OUT
J2
Conn_01x04_Female
A/V OUT
J5
PG203J
AUDIO-MONO
VIDEO
TK85_BUS
K3
U3
27256
U2
62256
BT1
Battery_Cell
D11
BAT41
D10
BAT41
PWR_FLAG
RAM_GND
J3
TECLADO CP200s
pin header 01x15 2.54mm
U11B
74LS393
U24
AY-3-8910
1.625MHz
U16
74LS245
J4
JOYSTICK
Conn_01x06_Female
50HZ JUMPER
U26
74LS138
U25B
74LS08
U15D
74HC86
AUDIO-L
AUDIO-R
J7
PG203J
AUDIO-MONO
EAR/MIC
J6
PG203J
MIC
EAR
FONTE INTERNA CP200s
J1
+10V (n. reg)
+9V
+12V
-5V
GND
+5V
Conn_01x05_Male
Redrawn by Danjovic
Sheet: /
File: CP297s.sch
Title: ZX 97 lite
Size: A2
Date: 16 jun 2013
Rev:
KiCad E.D.A. kicad 6.0.0-rc1-unknown-7c77b9266ubuntu14.04.1
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