Vdd

bias_m9_m10

M9   M10

l=0.36u w=6.84u   l=0.36u w=6.84u

CMOS18P   CMOS18P

bias_m3_m4

M3   M4

M1   M2

Vp   Vn

l=0.18u w=23.04u   l=0.18u w=23.04u

CMOS18N   CMOS18N

l=0.60u w=7.56u   l=0.60u w=7.56u

CMOS18P   CMOS18P

Vo

bias_nmos

M5   M6   C1

l=0.36u w=1.44u   l=0.36u w=1.44u

0.1p

CMOS18N   CMOS18N

M12   M7   M8

bias_nmos

l=0.36u w=2.88u   l=0.36u w=1.44u   l=0.36u w=1.44u

CMOS18N   CMOS18N   CMOS18N

Vss

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