ES215 - COA - Assignment -4 Name: Daniel Criftson E Roll No: 20110051). Let's assume the no. of instructions to be "N". . Baseline is a pipeline with larger value of N Therefore, [CP] = 1 (> N+4) 1+(F) = (o (of N >> 4) · Speedup = C Beenix no 1 C and clock rate are going to be the same in both the cases). CPI baseline CPI pipeline a) i) 30% RAW dependenny and do% branch dependency: → We know that CPI = \(\le CPI \) nistration instruction instruction No dependency CPI pipelnie = (1-0.3-0.2) * 1 + (0.3) (1+ stall RAW dependency)
+ (0.2) (1+ stall branch dependency) We know that Et stall RAW dependency = 3 stall brank dependency = 2

· · · CPI = (0.5+1) + (0.3+4) + (0.2+3)
pipelnie CPI pipelnie = 2.3

oo Speedup = 1 0.4348

ii) If we used a branch predictor with 80% accuracy.

· This means that the 20% (manuate) of 20% brand dependent vinstautions au stalled by 2.

And as we did before, 30%. RAW dependent anistautions

are stalled by 3.

O CP1

pipelme

pipelme

| CP1 pipelnie = 1.98

.. Speedup = 0.5051 Di) 40 % branch dependency:

=> CP1 pipelnie => C1-0.4) +1 + (0.4) (1+ stall branch dependent)

CPI pipeline = 1.8

· Speedup = 1 = 0.5556

i) If we used a branch predictor with 80% acumany:

This means that 20% (mannay) of 40% branch dependent mistrutions are stalled by 2.

00 CPI pipelme => (1-0.4) * 1 + (0.4) (1+ (0.2)(2))

j 1.16.

° Speedup => 1 => 0.8621

2) Criven: 20% => branch ristructions. 85% > Filled delay slots. 'CPI base = 1.5 __ no delay shot used. base = (0.8) (CPI) + (0.2) (CPI branch) = 11.5 = (0.8)(CPI non-branch) + (0.2)(CPI branch) -10 For each delay slot filled, CPI reduces by 1. · praction of branch instructions where delay slot is filled (0.2)(0.85) > 4g · fraction of branch instructions where delay shots are not filled (=) (0.2) (0.15) =) 0.03 00 CP1 = (0.8)(CP1 non-branch) + 0.17 (CP1 -1)

+ 0.03 (CP1 branch)

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 $\frac{1.5 - CPI}{CPI = 1.33}$