

1. Description

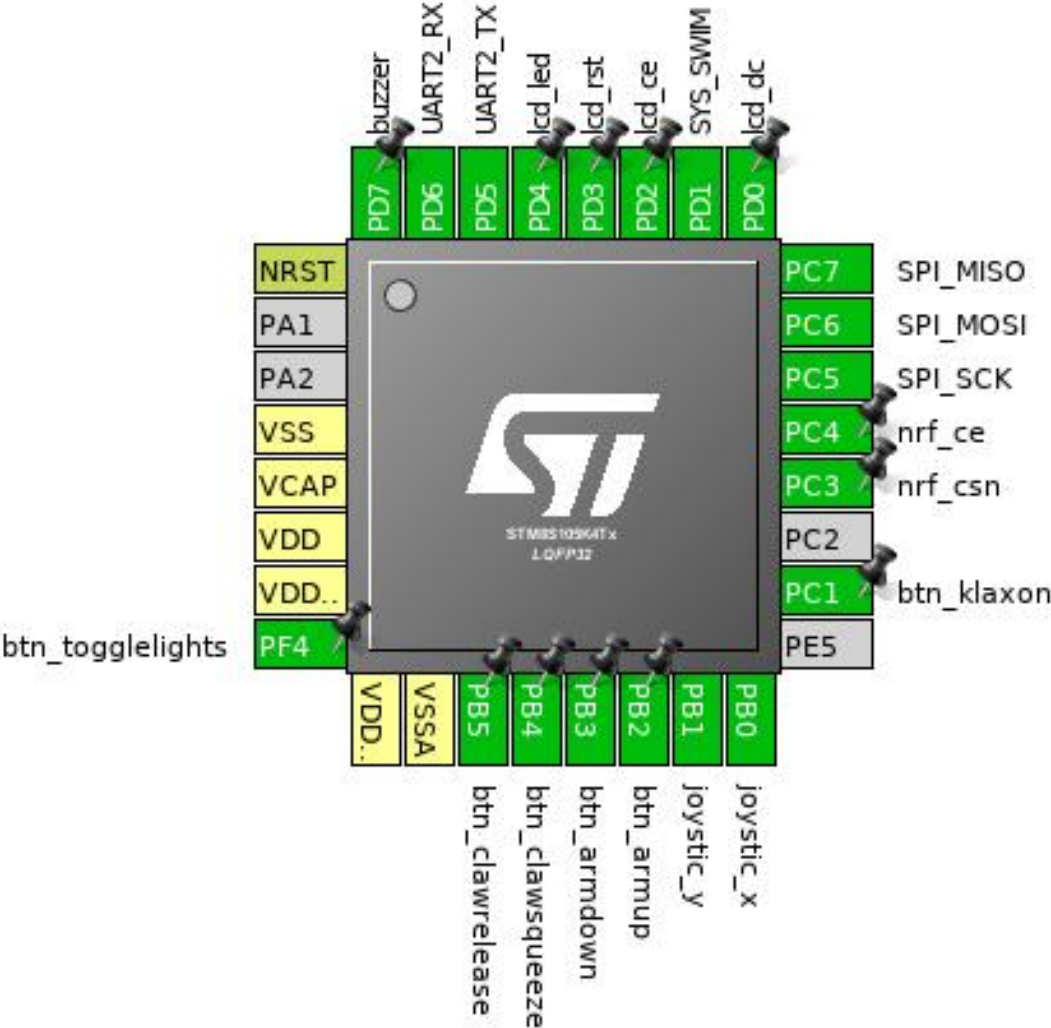
1.1. Project

Project Name	DUMRON_RC
Board Name	No information
Generated with:	STM8CubeMX 1.4.0
Date	08/01/2019

1.2. MCU

MCU Series	STM8S
MCU Line	STM8S103/105
MCU name	STM8S105K4Tx
MCU Package	LQFP32
MCU Pin number	32

2. Pinout Configuration

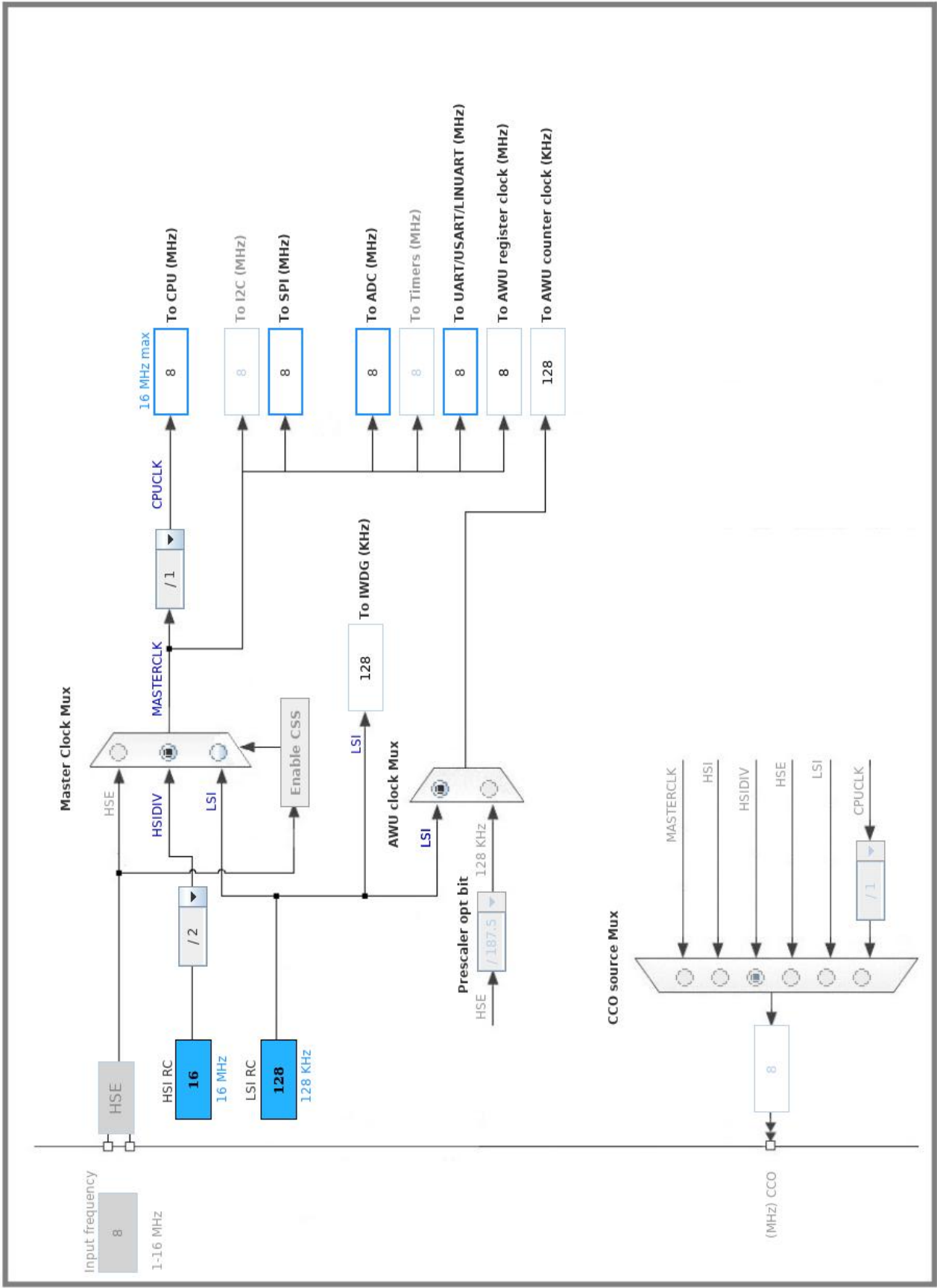


3. Pins Configuration

Pin Number LQFP32	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	NRST	Reset		
4	VSS	Power		
5	VCAP	Power		
6	VDD	Power		
7	VDDIO	Power		
8	PF4 *	I/O	GPIO_Input	btn_togglelights
9	VDDA	Power		
10	VSSA	Power		
11	PB5 *	I/O	GPIO_Input	btn_clawrelease
12	PB4 *	I/O	GPIO_Input	btn_clawsqueeze
13	PB3 *	I/O	GPIO_Input	btn_armdown
14	PB2 *	I/O	GPIO_Input	btn_armup
15	PB1	I/O	ADC_IN1	joystic_y
16	PB0	I/O	ADC_IN0	joystic_x
18	PC1 *	I/O	GPIO_Input	btn_klaxon
20	PC3 *	I/O	GPIO_Output	nrf_csn
21	PC4 *	I/O	GPIO_Output	nrf_ce
22	PC5	I/O	SPI_SCK	
23	PC6	I/O	SPI_MOSI	
24	PC7	I/O	SPI_MISO	
25	PD0 *	I/O	GPIO_Output	lcd_dc
26	PD1	I/O	SYS_SWIM	
27	PD2 *	I/O	GPIO_Output	lcd_ce
28	PD3 *	I/O	GPIO_Output	lcd_rst
29	PD4 *	I/O	GPIO_Output	lcd_led
30	PD5	I/O	UART2_TX	
31	PD6	I/O	UART2_RX	
32	PD7 *	I/O	GPIO_Output	buzzer

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

Series	STM8S
Line	STM8S103/105
MCU	STM8S105K4Tx
Datasheet	14771_Rev15

5.2. Parameter Selection

Temperature	25
Vdd	null