Difference Amplifier or Subtractor APJAKTU: 2006-07, 2007-08, 2009-10

- The circuit diagram of subtractor is shown in the Fig. 9.18.1.
- the inputs and output let us use Superposition principle.
- Let V_{o1} be the output, with input V_1 acting alone, assuming V_2 to be zero. And V_{o2} be the output, with input V_2 acting, assuming V_1 to be zero.
- With V₂ zero, the circuit acts as an inverting amplifier as shown in the Fig. 9.18.2.
- · Hence we can write,

$$V_{o1} = -\frac{R_f}{R_1} V_1$$
 ... (9.18.1)

- While with V_1 as zero with V_2 acting, the circuit reduces to as shown in the Fig. 9.18.3.
- This circuit acts as a noninverting amplifier which amplifies the voltage at node B by the factor $\left(1 + \frac{R_f}{R_1}\right)$.
- Let potential of node B be V_B.
- Applying voltage divider rule to the input V₂ loop,

$$V_B = \frac{R_f}{R_2 + R_f} V_2$$
 ... (9.18.2)

$$V_{o2} = \left[1 + \frac{R_f}{R_1}\right] V_B \qquad ... (9.18.3)$$

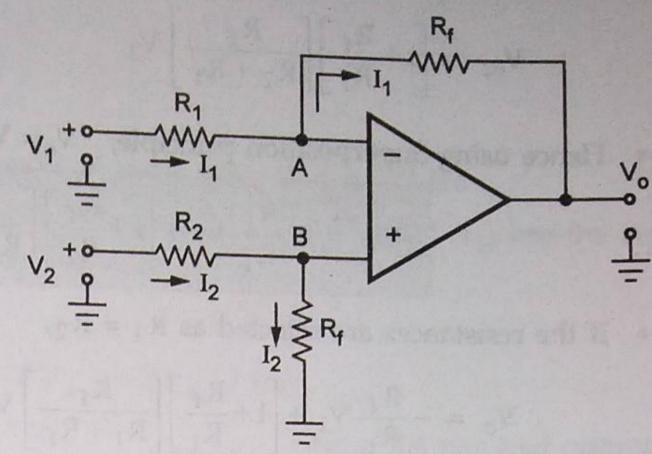


Fig. 9.18.1 Subtractor circuit

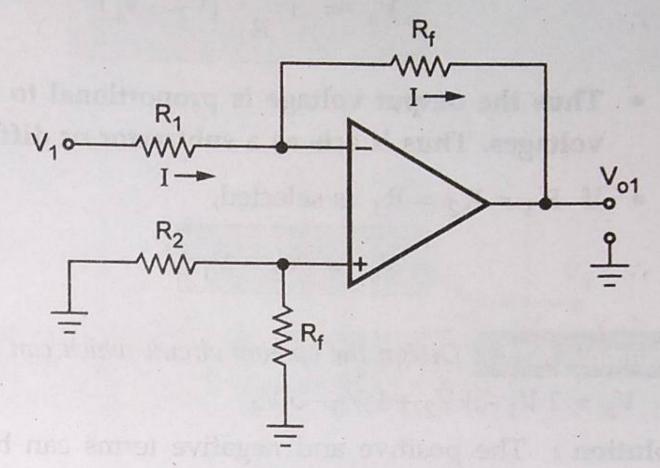


Fig. 9.18.2 V_1 acting, $V_2 = 0$

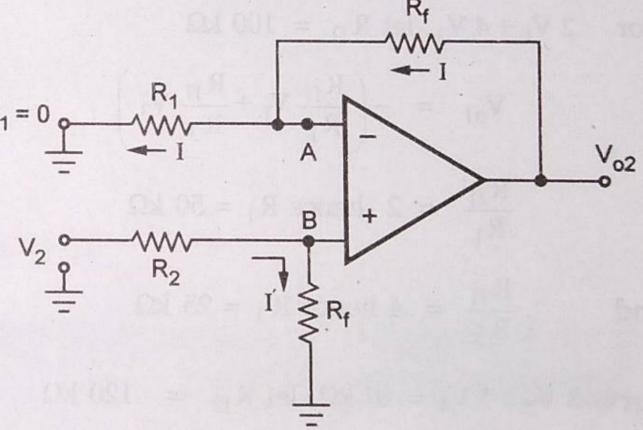


Fig. 9.18.3 V_2 acting, $V_1 = 0$

• Substituting V_B from (9.18.2) in (9.18.3) we get,

$$V_{02} = \left[1 + \frac{R_f}{R_1}\right] \left[\frac{R_f}{R_2 + R_f}\right] V_2$$
 ... (9.18.4)

• Hence using Superposition principle, $V_0 = V_{01} + V_{02}$

$$V_{o} = -\frac{R_{f}}{R_{1}} V_{1} + \left[1 + \frac{R_{f}}{R_{1}}\right] \left[\frac{R_{f}}{R_{2} + R_{f}}\right] V_{2} \qquad ... (9.18.5)$$

• If the resistances are selected as $R_1 = R_2$,

$$V_{o} = -\frac{R_{f}}{R_{1}} V_{1} + \left[1 + \frac{R_{f}}{R_{1}}\right] \left[\frac{R_{f}}{R_{1} + R_{f}}\right] V_{2} = -\frac{R_{f}}{R_{1}} V_{1} + \frac{R_{f}}{R_{1}} V_{2}$$

$$V_{o} = +\frac{R_{f}}{R_{1}} (V_{2} - V_{1}) \qquad ... (9.18.6)$$

- Thus the output voltage is proportional to the difference between the two input voltages. Thus it acts as a subtractor or difference amplifier.
- If $R_1 = R_2 = R_f$ is selected,

$$V_0 = V_2 - V_1$$
 ... (9.18.7)

Fig. 9.18.4

Example 9.132 Find the output voltage of the following op-amp circuit shown in Fig. 9.18.5.

APJAKTU: 2009-10, Marks 10

Scanned by TapScanner