



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058, India

(Autonomous College Affiliated to University of Mumbai)

Mid Semester Examination

March 2019

Max.Marks: 20

Class: S.E.

Course Code: EL43

Name of the Course: Computer Organization and Architecture

Duration: 60 Min

Semester: IV

Branch: Electronics

SYNOPTIC

Q-1) A)

Flow chart of booth's Algorithm - (1 M)

Algorithm steps (correct) - (4 M)

B) Flow chart of Non-restoring division algorithm - (1 M)

Algorithm with correct steps (4 M)

Q-2) Three factors

- Microprocessor speed (Explanation) - (2 M)
- performance Balance (Explanation) - (1.5 M)
- Improvements in chip organization and architecture (Explanation) - (1.5 M)

Q-3) A) Five points for differentiate
1 mark to each (5 M)

B) Diagram of ARM architecture (2 M)
Explanation in short (3 M)

- Q-4) Diagram of 1 bit memory cell - (1M)
Read operation - (1M)
write operation - (1M)
Diagram for 64KB chip - (1M)
Calculation (Answer 28) - (1M)