



Bharatiya Vidya Bhavan's
SARDAR PATEL INSTITUTE OF TECHNOLOGY
 (Autonomous Institute Affiliated to University of Mumbai)
 Munshi Nagar, Andheri (W), Mumbai – 400 058.

Re-Examination	
August 2021	
Max. Marks: 60	Duration: 2 Hrs.
Class: T.E	Semester: VI
Course Code: CE61	Branch: Computer Engineering
Name of the Course: System Programming and Compiler Construction	
Instruction: <ol style="list-style-type: none"> 1) All questions are compulsory. 2) Draw neat diagrams. 3) Assume suitable data if necessary but justify the same. 	

Q. No.	Question	Max. Marks	COs																																										
Q. 1 (a)	<p>Given below is a program PROG that has been written using the SIC/XE machine.</p> <table border="1"> <tr><td></td><td>LDT</td><td>NUM</td></tr> <tr><td></td><td>LDA</td><td>ALPHA</td></tr> <tr><td></td><td>ADDR</td><td>T, A</td></tr> <tr><td></td><td>SUB</td><td>#8</td></tr> <tr><td></td><td>STA</td><td>SUM1</td></tr> <tr><td></td><td>LDA</td><td>GAMMA</td></tr> <tr><td></td><td>ADDR</td><td>T, A</td></tr> <tr><td></td><td>SUB</td><td>#12</td></tr> <tr><td></td><td>STA</td><td>SUM2</td></tr> <tr><td>ALPHA</td><td>RESW</td><td>1</td></tr> <tr><td>SUM1</td><td>RESW</td><td>1</td></tr> <tr><td>GAMMA</td><td>RESW</td><td>1</td></tr> <tr><td>SUM2</td><td>RESW</td><td>1</td></tr> <tr><td>NUM</td><td>RESW</td><td>1</td></tr> </table> <p>i) Write the mathematical expressions equivalent to the above program code</p> <p>ii) Assuming that it is to be loaded in memory from 0058H, generate the corresponding addresses for each instruction and write the corresponding machine code. The operation codes for the instructions are LDT=74, LDA=00, ADDR=90, SUB=1C, STA=0C and the addresses for registers A and T are 0 and 5 respectively.</p>		LDT	NUM		LDA	ALPHA		ADDR	T, A		SUB	#8		STA	SUM1		LDA	GAMMA		ADDR	T, A		SUB	#12		STA	SUM2	ALPHA	RESW	1	SUM1	RESW	1	GAMMA	RESW	1	SUM2	RESW	1	NUM	RESW	1	08	CO2
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Q. 1 (b)	Exemplify the various data structures involved in SIC/XE macro processor with a small sample of code.	07	CO2																																										
Q. 2 (a)	<p>Prove that the given grammar is LL(1) grammar</p> <p>$S \rightarrow aBDh$</p> <p>$B \rightarrow cC$</p> <p>$C \rightarrow bc/\epsilon$</p> <p>$D \rightarrow EF$</p> <p>$E \rightarrow g/\epsilon$</p> <p>$F \rightarrow f/\epsilon$</p>	08	CO3																																										



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Q. 2 (b)	Construct CLR Parsing table for the grammar $S \rightarrow L=R/R$ $L \rightarrow *R/id$ $R \rightarrow L.$ OR Test whether the grammar is LL (1) or not and construct a predictive parsing table for following grammar. $S \rightarrow iEtSA \mid a$, $A \rightarrow eS \mid \epsilon$, $E \rightarrow b$	07	CO3
Q. 3 (a)	What are the common conflicts that can be encountered in shift reduce parsers? Explain.	08	CO3
Q. 3 (b)	Write the translation scheme to generate intermediate code for assignment statements with array references.	07	CO4
Q. 4 (a)	Translate the given expression into Quadruples, triples, and indirect triples. $(i+j)*(k+l)+(i*j/k)*j+60$ OR What is a leader of basic block? Write and explain the algorithm used to find leaders. Draw flow graph for matrix multiplication.	08	CO4
Q. 4 (b)	What is the significance of symbol table at runtime and compile time? Discuss it in detail.	07	CO5