

# Datenbusse

## Main Bus

MAIN\_BUS\_0  
MAIN\_BUS\_1  
MAIN\_BUS\_2  
MAIN\_BUS\_3  
MAIN\_BUS\_4  
MAIN\_BUS\_5  
MAIN\_BUS\_6  
MAIN\_BUS\_7

## Memory BUS

MEMORY\_BUS\_0  
MEMORY\_BUS\_1  
MEMORY\_BUS\_2  
MEMORY\_BUS\_3  
MEMORY\_BUS\_4  
MEMORY\_BUS\_5  
MEMORY\_BUS\_6  
MEMORY\_BUS\_7

## Xfer Bus

XFER\_BUS\_0  
XFER\_BUS\_1  
XFER\_BUS\_2  
XFER\_BUS\_3  
XFER\_BUS\_4  
XFER\_BUS\_5  
XFER\_BUS\_6  
XFER\_BUS\_7  
XFER\_BUS\_8  
XFER\_BUS\_9  
XFER\_BUS\_10  
XFER\_BUS\_11  
XFER\_BUS\_12  
XFER\_BUS\_13  
XFER\_BUS\_14  
XFER\_BUS\_15  
XFER\_BUS\_16

## Address Bus

ADDRESS\_BUS\_0  
ADDRESS\_BUS\_1  
ADDRESS\_BUS\_2  
ADDRESS\_BUS\_3  
ADDRESS\_BUS\_4  
ADDRESS\_BUS\_5  
ADDRESS\_BUS\_6  
ADDRESS\_BUS\_7  
ADDRESS\_BUS\_8  
ADDRESS\_BUS\_9  
ADDRESS\_BUS\_10  
ADDRESS\_BUS\_11  
ADDRESS\_BUS\_12  
ADDRESS\_BUS\_13  
ADDRESS\_BUS\_14  
ADDRESS\_BUS\_15  
ADDRESS\_BUS\_16

## ALU Right Bus

ALU\_RIGHT\_BUS\_0  
ALU\_RIGHT\_BUS\_1  
ALU\_RIGHT\_BUS\_2  
ALU\_RIGHT\_BUS\_3  
ALU\_RIGHT\_BUS\_4  
ALU\_RIGHT\_BUS\_5  
ALU\_RIGHT\_BUS\_6  
ALU\_RIGHT\_BUS\_7

## ALU Left Bus

ALU\_LEFT\_BUS\_0  
ALU\_LEFT\_BUS\_1  
ALU\_LEFT\_BUS\_2  
ALU\_LEFT\_BUS\_3  
ALU\_LEFT\_BUS\_4  
ALU\_LEFT\_BUS\_5  
ALU\_LEFT\_BUS\_6  
ALU\_LEFT\_BUS\_7