

3.3. Set-associative mapping

$M_2(0), M_2(1) \dots M_2(j) \dots M_2(2^m - 1)$
 $M_1(0), M_1(1) \dots M_1(i) \dots M_1(2^m - 1)$, $m < n$

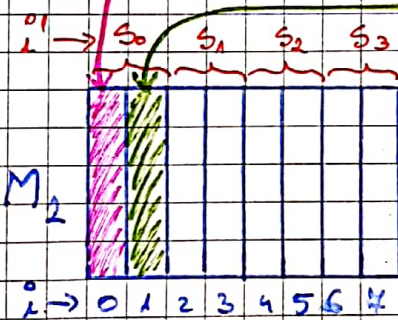
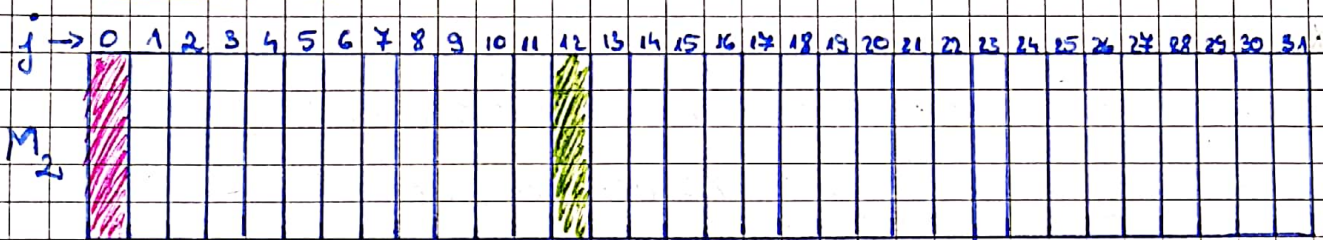
blocuri din memoria principală

k-way set-associative mapping
 $k = 2$

$M_1'(0), M_1'(1), \dots M_1'(i') \dots M_1'(2^{m-\Delta} - 1)$

Maparea se face: $i' = j \bmod 2^{m-\Delta}$, $m' = m - \Delta$

Example 1: $m = 3$; $n = 5$; $\Delta = 1 \Rightarrow M_2 : 32 \text{ blocuri}$



$i' = j \bmod 2^{m-\Delta}$, în cazul nostru

$$i' = j \bmod 4$$

// Pe urmă mapăm la un set oriunde în set

ex: pt. 12: $\rightarrow 011 \boxed{00}$ $m-\Delta = 2$
 tag indexul adresă bit



Example 2: 2^{24} words - main memory (MM)

1 word = 1 B

1 block = 4 words

Adresa: 2^{24} words = 2^{24} B = $\underbrace{2^4}_{16} \cdot 2^{20}$ B = 16 MiB
 ↳ mega

Address	M_2				Block number
0	0	1	2	3	0
4	4	5	6	7	1
8					2
12					3
\vdots					\vdots
4092					1023
4096					1024
4100					1025
\vdots					\vdots
8188					2047
8192					2048
\vdots					\vdots
12284					3071
12288					3072
12292					3073
\vdots					\vdots
$2^{22} - 4$					$2^{22} - 1$

⊙ Address Code (de la ex. trecută)

8192 → 230
4100 → 2715
12292 → 170

cu codificările ⊗
(cu 2 pagini mai înainte)

Cache: 1k-block

Cache size = 2^{10} blocks

Cache - 2 way Set Association (SA)

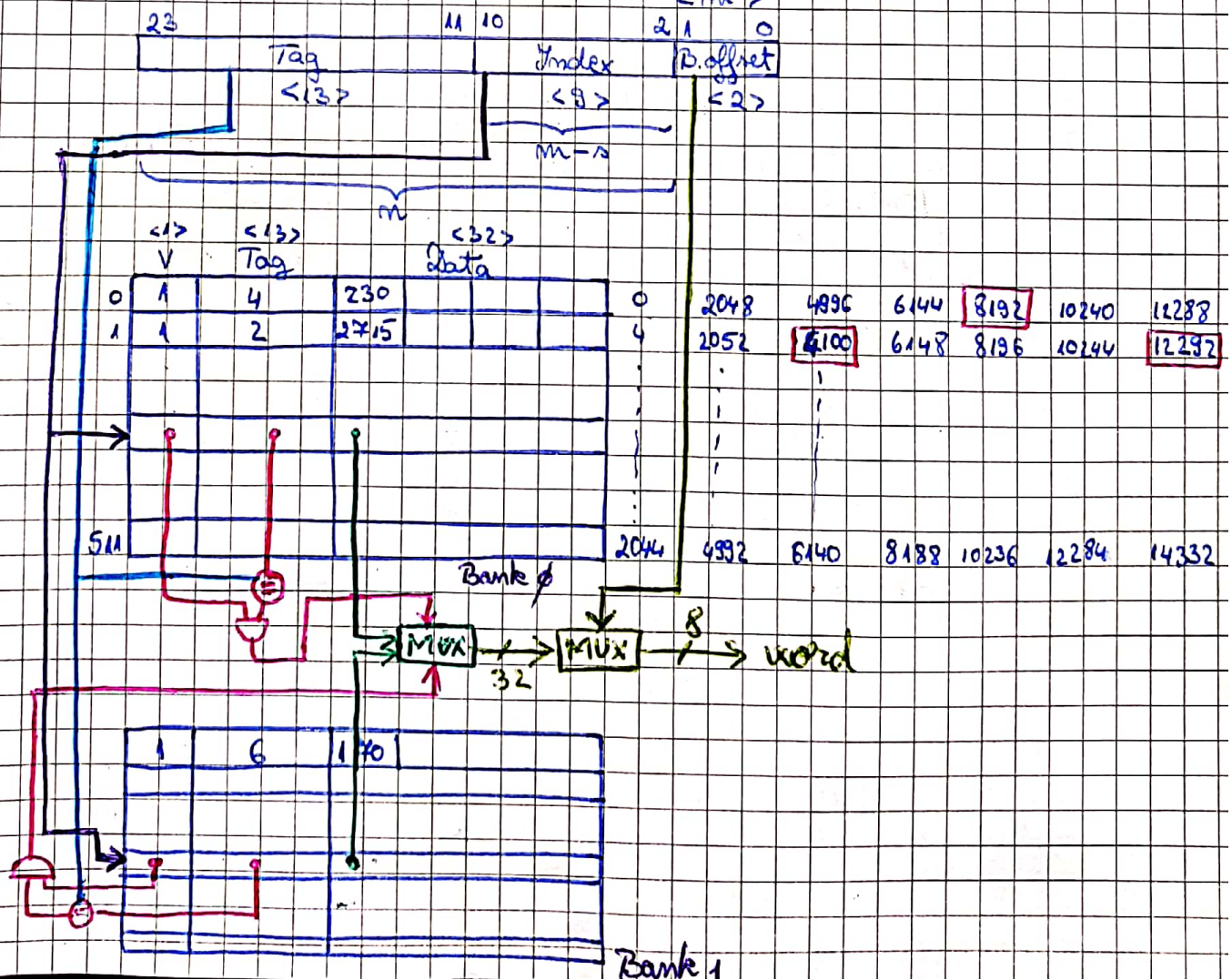
Index values = $\frac{2^{10}}{2} = 2^9$
set associativity

DM Address
(Direct Mapping)

2^{20} k-way SA Add.

Tag	Index	B.offset	W.offset
$\langle n-m \rangle$	$\langle m \rangle$	$\langle p \rangle$	$\langle r \rangle$

Tag	Index	B.offset	W.offset
$\langle m-m+p \rangle$	$\langle m-p \rangle$	$\langle p \rangle$	$\langle r \rangle$
	$\langle m' \rangle$		



Example 3 : 4-way SA

8-sets

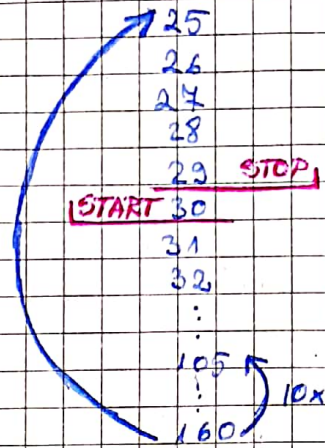
1 block = 4 words

Cache initially empty

1 word = 1 byte

MM size = 2^{16} words

Instruction cache : $\frac{2^{16}}{2^2} = 2^{14}$ blocks



Address word format?

15	5	4	2	1	0
Tag		Index		B off	
(11)		(3)		(2)	

Saltul la instructiune se face de 10 ori. Care este Hit rate?

Răzându-ne pe tabelul de pe pagina următoare, obținem.

	V	Tag	Sete
0	1	1	32-35
1	1	1	36-39
2	1	1	40-43
3	1	1	44-47
4	1	1	48-51
5	1	1	52-55
6	1	X0	56-59 24-27
7	1	X	28-31
		4	156-159

Block 0

	V	Tag	Sete
0	1	3	96-99
1	1	3	100-103
2	1	3	104-107
3	1	3	108-111
4	1	3	112-115
5	1	3	116-119
6	1	3	120-123
7	1	2	92-95

Block 2

	V	Tag	Sete
0	1	2	64-67
1	1	2	68-71
2	1	2	72-75
3	1	2	76-79
4	1	2	80-83
5	1	2	84-87
6	1	2	88-91
7	1	X	60-63
		0	28-31

Block 1

	V	Tag	Sete
0	1	4	128-131
1	1	4	132-135
2	1	4	136-139
3	1	4	140-143
4	1	4	144-147
5	1	4	148-151
6	1	4	152-155
7	1	3	124-127

Block 3

$$\begin{pmatrix} 1M \\ 1H \end{pmatrix} \begin{pmatrix} 1M \\ 3H \end{pmatrix} \times 32 \begin{pmatrix} 1M \\ 0H \end{pmatrix} \\ \begin{pmatrix} 0M \\ 560H \end{pmatrix} \cdot \begin{pmatrix} 1M \\ 2H \end{pmatrix} \begin{pmatrix} 1M \\ 1H \end{pmatrix}$$

$$\begin{pmatrix} 36M \\ 661H \end{pmatrix}$$

$$\Rightarrow \text{Hit rate} = \frac{661}{607} \approx 95\%$$

M₂

Address

Block Index

	0-3	0
	4-7	1
	8-11	2
	12-15	3
	16-19	4
	20-23	5
	24-27	6
→	28-31	7
⇒	32-35	8
	36-39	9
	40-43	10
	44-47	11
	48-51	12
	52-55	13
	56-59	14
→	60-63	15
→	64-67	16
	68-71	17
	72-75	18
	76-79	19
	80-83	20
	84-87	21
	88-91	22
	92-95	23
	96-99	24
	100-103	25
	104-107	26
	108-111	27
	112-115	28
	116-119	29
	120-123	30
	124-127	31
	128-131	32
	132-135	33
	136-139	34
	140-143	35
	144-147	36
	148-151	37
	152-155	38
→	156-159	39
	160-163	40