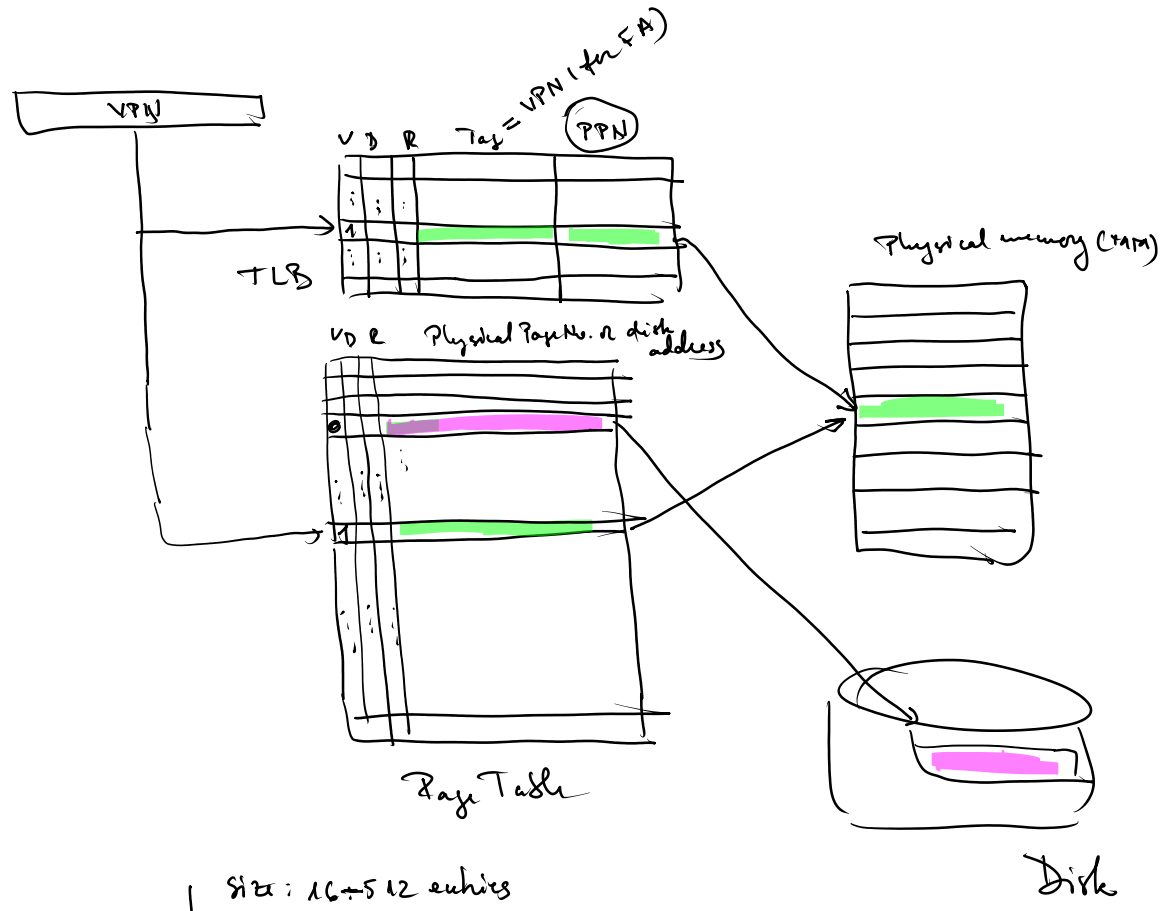


# TLB (Translation Lookaside Buffer)

Page Table in RAM => Performance overhead

Solution => Address cache (TLB) / Page Table Entry Cache

## TLB



## TLB Fast MATH

- Size: 16 - 512 entries
- Block size: 1 - 2 Page Table entries, FA
- Hit time: 1 c.c.
- Miss Penalty: 10s - 100s c.c.
- Miss Rate: 0.1% - 1%

