



## element<sub>14</sub>

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<u>TIP2955</u> <u>TIP3055</u>

ΕN

This Datasheet is presented by the manufacturer

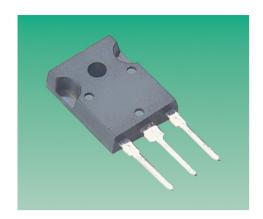
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Cette fiche technique est présentée par le fabricant

## **Complementary Power Transistors**

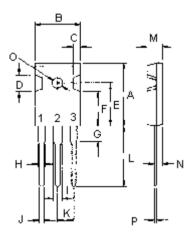




Complementary Silicon Power Transistors are designed for use in general purpose power amplifier and switching applications.

### Features:

- Power Dissipation- $P_D$  = 90W at  $T_C$  = 25°C.
- DC Current Gain  $h_{FE}$  = 20 ~ 100 at  $I_C$  = 4.0A.
- $V_{CE(sat)} = 1.1V$  (Maximum) at  $I_C = 4.0A$ ,  $I_B = 400mA$ .



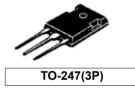
Pin 1. Base

- 2. Collector
- 3. Emitter

Dimensions	Minimum	Maximum		
Α	20.63	22.38		
В	15.38	16.20		
С	1.90	2.70		
D	5.10	6.10		
Е	14.81	15.22		
F	11.72	12.84		
G	4.20	4.50		
Н	1.82	2.46		
I	2.92	3.23		
J	0.89	1.53		
K	5.26	5.66		
L	18.50	21.50		
M	4.68	5.36		
N	2.40	2.80		
0	3.25	3.65		
Р	0.55	0.70		

NPN	PNP
TIP3055	TIP2955

15 Ampere Complementary Silicon Power Transistors 60 Volts 90 Watts



### **Maximum Ratings**

Characteristic	Symbol	Rating	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	60		
Collector-Emitter Voltage	V <sub>CER</sub>	70	V	
Collector-Base Voltage	V <sub>CBO</sub>	100	V	
Emitter-Base Voltage	V <sub>EBO</sub>	7.0		
Collector Current-Continuous	I <sub>C</sub>	15	А	
Base Current	I <sub>B</sub>	7.0		
Total Power Dissipation at T <sub>C</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	90 0.72	W W/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C	



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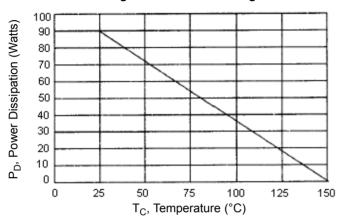
## **Complementary Power Transistors**



### **Thermal Characteristics**

Characteristic	Symbol	Maximum	Unit
Thermal Resistance Junction to Case	Rθjc	1.39	°C/W

Figure - 1 Power Derating



## Electrical Characteristics (T<sub>c</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Minimum	Maximum	Unit	
OFF Characteristics					
Collector-Emitter Sustaining Voltage (1) $(I_C = 30\text{mA}, I_B = 0)$	V <sub>CEO(SUS)</sub>	60	-	V	
Collector Cut off Current ( $V_{CE} = 70V$ , $R_{BE} = 100\Omega$ )	I <sub>CER</sub>	-	1.0		
Collector Cut off Current (V <sub>CE</sub> = 30V, I <sub>B</sub> = 0)	I <sub>CEO</sub>	-	0.7	- mA	
Collector Cut off Current (V <sub>CE</sub> = 100V, V <sub>BE(off)</sub> = 1.5V)	I <sub>CEV</sub>	-	5.0		
Emitter Cut off Current (V <sub>EB</sub> = 7.0V, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	5.0		
ON Characteristics (1)					
DC Current Gain $(I_C = 4.0A, V_{CE} = 4.0V)$ $(I_C = 10A, V_{CE} = 4.0V)$	h <sub>FE</sub>	20 5.0	100	-	
Collector-Emitter Saturation Voltage ( $I_C = 4.0A$ , $I_B = 0.4A$ ) ( $I_C = 10A$ , $I_B = 3.3A$ )	V <sub>CE(sat)</sub>	-	1.1 3.0	V	
Base-Emitter On Voltage (I <sub>C</sub> = 4.0A, V <sub>CE</sub> = 4.0V)	V <sub>BE(on)</sub>	-	1.8		
Dynamic Characteristics	•				
Current Gain Bandwidth Product ( $I_C = 500$ mA, $V_{CE} = 10$ V, $f = 1.0$ MHz)	f <sub>T</sub>	2.5	-	MHz	
Small-Signal Current Gain ( $I_C = 1.0A$ , $V_{CE} = 4V$ , $f = 1kHz$ )	h <sub>fe</sub>	15	-	-	

<sup>(1)</sup> Pulse Test: Pulse Width = 300µs, Duty Cycle ≤2.0%



<sup>(2)</sup>  $f_T = |h_{fe}| \cdot f_{test}$ 

## **Complementary Power Transistors**



Figure - 2 DC Current Gain

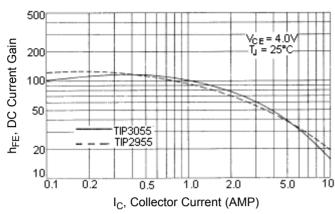
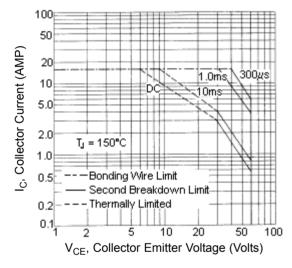


Figure - 3 Active Region Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate  $I_{C-}V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure - 3 is based on  $T_C$  = 150°C;  $T_{J(PK)}$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated for temperature.

### **Specifications**

I <sub>C(av)</sub> maximum (A)	V <sub>CEO</sub> maximum (V)	h <sub>FE</sub> minimum at I <sub>C</sub> = 4A	P <sub>tot</sub> at 25°C (W)	Package	Туре	Part Number
15	60 20	20	90	TO-247	PNP	TIP2955
15			20	90	10-247	NPN



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## **Complementary Power Transistors**



Notes:

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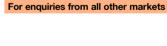
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