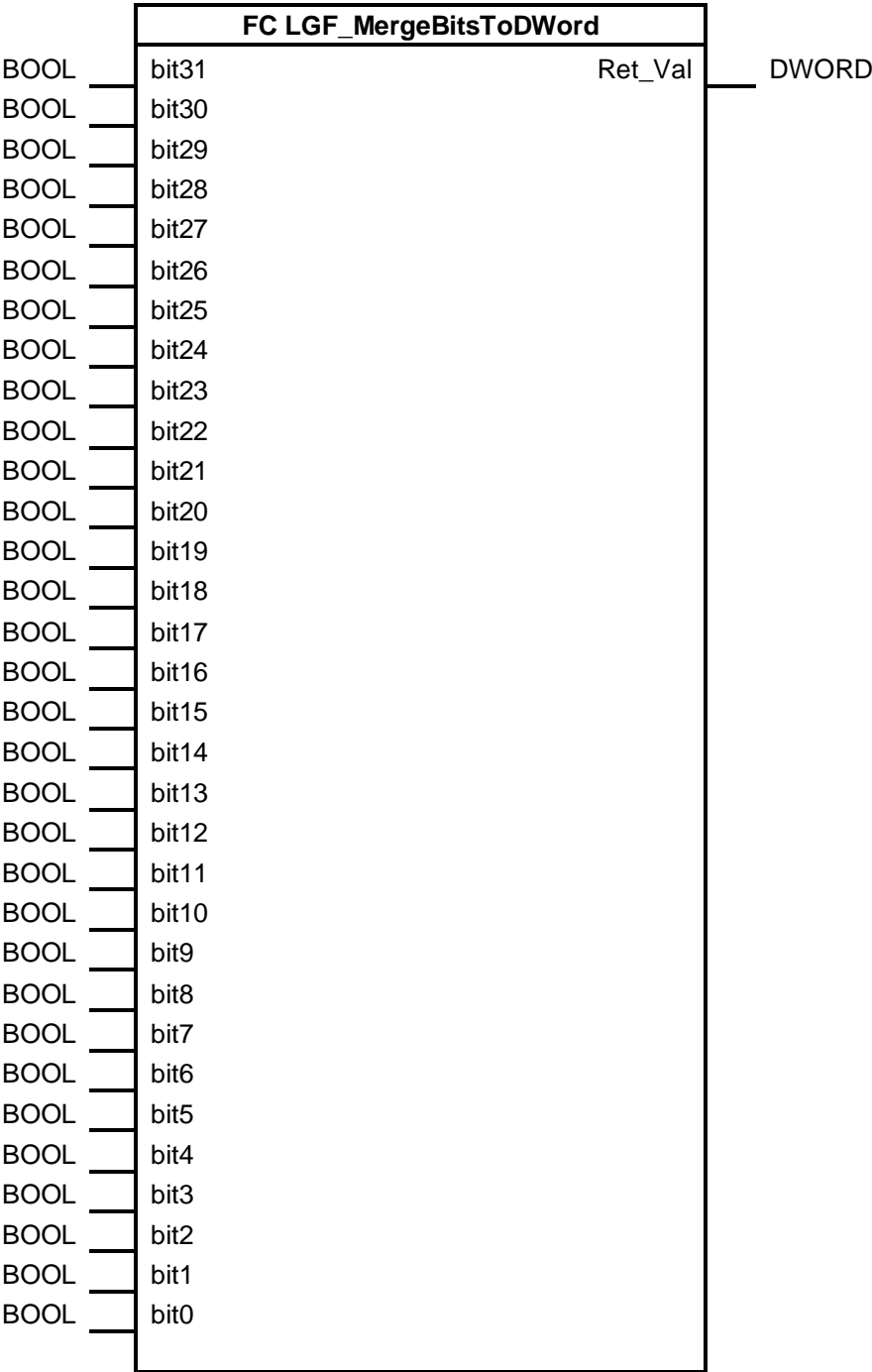


LGF_MergeBitsToDWord

Short description

This block converts 32 Boolean variables into a DWord variable.

Block



Input parameters

Parameters	Data type	Description
bit31 ... bit0	BOOL	Bit variables, whose bits are combined to form a bit sequence

Output parameters

Parameters	Data type	Description
Ret_Val	DWORD	Composite bit sequence stored in a variable

Further information on libraries in TIA Portal:

- Topic page libraries
<https://support.industry.siemens.com/cs/ww/en/view/109738702>
- Guideline on Library Handling
<https://support.industry.siemens.com/cs/ww/en/view/109747503>
- Programming Guideline for S7-1200/1500 in chapter "Libraries"
<https://support.industry.siemens.com/cs/ww/en/view/81318674>
- Programming Styleguide
<https://support.industry.siemens.com/cs/ww/en/view/81318674>