${\bf LGF_MergeBitsToDWord}$

Short description

This block converts 32 Boolean variables into a DWord variable.

Block

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BOOL	bit31	FC LGF_MergeBitsToDWor		DWORD
BOOL BOOL	bit30		Ret_Val	DWORD
BOOL	bit29			
BOOL	bit28			
	bit27			
BOOL				
BOOL	bit26			
BOOL	bit25			
BOOL	bit24			
BOOL	bit23			
BOOL	bit22			
BOOL	bit21			
BOOL	bit20			
BOOL	bit19			
BOOL	bit18			
BOOL	bit17			
BOOL	bit16			
BOOL	bit15			
BOOL	bit14			
BOOL	bit13			
BOOL	bit12			
BOOL	bit11			
BOOL	bit10			
BOOL	bit9			
BOOL	bit8			
BOOL	bit7			
BOOL	bit6			
BOOL	bit5			
BOOL	bit4			
BOOL	bit3			
BOOL	bit2			
BOOL	bit1			
BOOL	bit0			

Input parameters

Parameters	Data type	Description		
bit31	BOOL	Bit variables, whose bits are combined to form a bit sequence		
bit0				

Output parameters

Parameters	Data type	Description
Ret_Val	DWORD	Composite bit sequence stored in a variable

Further information on libraries in TIA Portal:

- Topic page libraries
 https://support.industry.siemens.com/cs/ww/en/view/109738702
- Guideline on Library Handling https://support.industry.siemens.com/cs/ww/en/view/109747503
- Programming Guideline for S7-1200/1500 in chapter "Libraries" https://support.industry.siemens.com/cs/ww/en/view/81318674
- Programming Styleguide
 https://support.industry.siemens.com/cs/ww/en/view/81318674