Started on	Monday, 16 October 2023, 8:08 AM
State	Finished
Completed on	Monday, 16 October 2023, 9:40 AM
Time taken	1 hour 32 mins
Marks	5.00/9.00
Grade	**5.56** out of 10.00 (**55.56**%)

Question 1

Incorrect

Mark 0.00 out of 1.00

Given the following MIPS sequence:

```
          addi $t1, $zero, 10
          add $t2, $zero, $zero
FindMean: addi $t1,$t1, -1
          addi $t2, $t2, 1
          beq $t1, $t2, Exit
          j FindMean
Exit:
```

Assume that the above sequence is executed by a 10 MHz MIPS processor. The processor can process one ins
execution time of the above sequence?

- ○ A.   All other answers are wrong
- ○ B.   2.1 µs
- ○ C.   2.0 µs
- ● D.   2.2 µs ✖

Câu trả lời của bạn không chính xác.

The correct answer is:

2.1 µs

Ask any coding question

Question 2

Correct

Mark 1.00 out of 1.00

Given the following MIPS sequence:

```
                addi $t1, $zero, 10
                add $t2, $zero, $zero
FindMean: addi $t1,$t1, -1
                addi $t2, $t2, 1
                beq $t1, $t2, Exit
                j FindMean
Exit:
```

What is value of the **$t1** register after the above sequence is executed?

- ○ A.   0
- ○ B.   10
- ◉ C.   5 ✔
- ○ D.   9

Câu trả lời của bạn là chính xác.

The correct answer is:
5

Question 3

Correct

Mark 1.00 out of 1.00

Given the following MIPS sequence:

```
                addi $t1, $zero, 10
                add $t2, $zero, $zero
FindMean: addi $t1,$t1, -1
                addi $t2, $t2, 1
                beq $t1, $t2, Exit
                j FindMean
Exit:
```

What is the value of the address field when the **beq** instruction is assembled into binary?

○ A. **0x0001**

○ B. **0x0002**

○ C. **0xFFFF**

◉ D. **0xFFFE** ✔

Câu trả lời của bạn là chính xác.

The correct answers are:

0x0001,

0xFFFE

Question 4

Incorrect

Mark 0.00 out of 1.00

Given the following MIPS sequence:

```
            addi $t1, $zero, 10
            add $t2, $zero, $zero
FindMean: addi $t1,$t1, -1
            addi $t2, $t2, 1
            beq $t1, $t2, Exit
            j FindMean
Exit:
```

What is the value of the address field when the **j** instruction is assembled into binary if the first instruction in **0xCAFE2020**?

  ○ A. All other answers are wrong

  ○ B. **0xAFE2020**

  ○ C. **46106632**

  ◉ D. **2BF8808** ✖

Câu trả lời của bạn không chính xác.

The correct answers are:

46106632,

0xAFE2020

Question 5

Correct

Mark 1.00 out of 1.00

A conditional branch instruction can change the value of **PC** to:

  ○ A. Address of any instructions in the memory

  ◉ B. Address of any instructions in the range of $\pm 2^{17}$ bytes from the current value of **PC** ✔

  ○ C. Address of any instructions in the range of $\pm 2^{15}$ instructions from the `main()`

  ○ D. Address of any instructions in the range of $\pm 2^{16}$ instructions from the instruction next to the condition branch instruction

Câu trả lời của bạn là chính xác.

The correct answer is:

Address of any instructions in the range of $\pm 2^{17}$ bytes from the current value of **PC**

Question 6

Incorrect

Mark 0.00 out of 1.00

Which is the machine instruction of the `lw` `$t0, -32($s1)` MIPS instruction?

- ⦿ A.   All other answers are wrong ✖
- ○ B.   **0x8E28FFE0**
- ○ C.   **0x8D110020**
- ○ D.   **0x8D11FFE0**
- ○ E.   **0x8E280020**

Câu trả lời của bạn không chính xác.

The correct answer is:
0x8E28FFE0

Question 7

Incorrect

Mark 0.00 out of 1.00

Assume that the **$s0** register stores value of 12345678. What is the value of the **$s2** register after the followir

`andi` `$s2, $s0, 2021`

- ⦿ A.   All other answers are wrong ✖
- ○ B.   There is an error
- ○ C.   **0x00000144**
- ○ D.   **0x00000660**

Câu trả lời của bạn không chính xác.

The correct answer is:
0x00000144

Question 8

Correct

Mark 1.00 out of 1.00

Which is NOT an operand for standard MIPS instructions?

- ◉ A.   Real numbers ✔
- ○ B.   Address of memory cells
- ○ C.   Registers
- ○ D.   Integer numbers

Câu trả lời của bạn là chính xác.

The correct answer is:
Real numbers

Question 9

Correct

Mark 1.00 out of 1.00

Biany code of the standard MIPS instruction **sll** `$t2, $s1, 10` is:

- ○ A.   **0x00115290**
- ○ B.   **0x0011527F**
- ◉ C.   **0x00115280** ✔
- ○ D.   **0x00115284**

Câu trả lời của bạn là chính xác.

The correct answer is:
0x00115280

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