

Started on Monday, 16 October 2023, 7:53 AM

State Finished

Completed on Monday, 16 October 2023, 8:03 AM

Time taken 9 mins 48 secs

Marks 5.00/9.00

Grade 5.56 out of 10.00 (55.56%)

Question 1

Correct

Mark 1.00 out of 1.00

Given the following expression (all variables in integer) $f = a + (b - (c + d))$. At least, how many MIPS instructions are required to evaluate this expression?

- ☒ A. 3 ✓
- ☐ B. 4
- ☐ C. 1
- ☐ D. 2

Câu trả lời của bạn là chính xác.

The correct answer is:

3

Question 2

Incorrect

Mark 0.00 out of 1.00

Choose a correct statement for the `sll` instruction:

- ☐ A. `sll` is encoded by I-format because there exists an immediate
- ☐ B. All other answers are wrong
- ☐ C. We can shift the source register to the left up to 32 bits
- ☒ D. The result is always equal to the value of the source register multiply by 2^n ✗

Câu trả lời của bạn không chính xác.

The correct answer is:

All other answers are wrong

Hi! Here some our recommendations
BLACKBOX:

Be as clear as possible

End the question in what k
answer to be, e.g: 'connec

or you can just

Here are some

Write a function that rec

How to delete docs fr

Connec

Ask any coding question

Blackbox AI Chat is in beta and Blackbox is not liable for the conte
acknowledge that you agree to agree to Blackbox's Terms and Pri

Question 3

Correct

Mark 1.00 out of 1.00

A register stores a value of **0xCAFECA21**; which instruction can we use to extract the last 16 bits?

- ☐ A. **lui**
- ☐ B. **or**
- ☐ C. **add**
- ☒ D. **andi** ✓

Câu trả lời của bạn là chính xác.

The correct answer is:

andi

Hi! Here some our recommendations
BLACKBOX:

Be as clear as possible

End the question in what k
answer to be, e.g: 'connec

or you can just

Here are some

Write a function that rec

How to delete docs fr

Conne

Question 4

Correct

Mark 1.00 out of 1.00

Given the values of the registers **\$s0** and **\$s1** are **0xCAFECA21** and **0x40000000**, respectively. What we can the address of **0x40000002** after executing **sw \$s0, 0(\$s1)**

- ☒ A. **0xCA** ✓
- ☐ B. **0xCAFECA**
- ☐ C. **0xCA21**
- ☐ D. **0xFE**

Câu trả lời của bạn là chính xác.

The correct answer is:

0xCA

Blackbox AI Chat is in beta and Blackbox is not liable for the conte
acknowledge that you agree to agree to Blackbox's Terms and Pri

Question 5

Correct

Mark 1.00 out of 1.00

To assign value of **0x12345678** to the **\$t0** register, which standard MIPS instructions should we execute?

- ☐ A. **lui** \$t0, 0x1234
lwi \$t0, 0x5678
- ☒ B. **lui** \$t0, 4660 ✓
addi \$t0, \$t0, 22136
- ☐ C. **addi** \$t0, \$zero, 0x12345678
- ☐ D. All other answers are wrong

Câu trả lời của bạn là chính xác.

The correct answer is:

lui \$t0, 4660

addi \$t0, \$t0, 22136

Hi! Here some our recommendations
BLACKBOX:

Be as clear as possible

End the question in what k
answer to be, e.g: 'connec

or you can just

Here are some

Write a function that rec

How to delete docs fr

Connec

Question 6

Incorrect

Mark 0.00 out of 1.00

Given the following MIPS instruction

lhu \$s0, \$s0 (\$s0)

Choose the correct statement:

- ☐ A. This is not a standard MIPS instruction
- ☒ B. The instruction causes a compiled error due to the same source and destination operands ✗
- ☐ C. The instruction causes a runtime error due to the same source and destination operands
- ☐ D. All other answers are wrong

Câu trả lời của bạn không chính xác.

The correct answer is:

This is not a standard MIPS instruction

Blackbox AI Chat is in beta and Blackbox is not liable for the conte
acknowledge that you agree to agree to Blackbox's Terms and Pri

Question 7

Incorrect

Mark 0.00 out of 1.00

Given the following MIPS sequence:

lw \$t0, 30(\$s3)

add \$s1, \$s2, \$t0

Assume that the **\$s3** register stores a base address of an integer array; choose the correct statement

- ☐ A. The sequence causes a runtime error
- ☐ B. All other answers are wrong
- ☐ C. The sequence causes a compile error
- ☒ D. The value at word address 30 + \$s3 in the memory is copied to the register \$t0 ✖

Câu trả lời của bạn không chính xác.

The correct answer is:

The sequence causes a runtime error

Hi! Here some our recommendations
BLACKBOX:

Be as clear as possible

End the question in what k
answer to be, e.g: 'connec

or you can just

Here are some

Write a function that rec

How to delete docs fr

Connec

BLACKBOX AI

Question 8

Incorrect

Mark 0.00 out of 1.00

Choose the correct statement in the context of standard MIPS instructions:

- ☒ A. **lui** moves an immediate to upper-half of a register ✖
- ☐ B. **lb** moves one byte from a register to memory
- ☐ C. All other answers are wrong
- ☐ D. **lhu** moves 16-bit data from memory to both the upper and lower half of a register
- ☐ E. **sb** does the sign-extension operation

Câu trả lời của bạn không chính xác.

The correct answer is:

All other answers are wrong

Blackbox AI Chat is in beta and Blackbox is not liable for the conte
acknowledge that you agree to agree to Blackbox's Terms and Pri

Question 9

Correct

Mark 1.00 out of 1.00

Which following standard MIPS instruction will assign value of 0 to the **\$s0** register?

- ☐ A. **ori** \$s0, \$s0, 0
- ☐ B. **add** \$s0, \$s0, \$zero
- ☐ C. All other answers are correct
- ☒ D. **lui** \$s0, 0 ✓

Câu trả lời của bạn là chính xác.

The correct answer is:

lui \$s0, 0

Hi! Here some our recommendations
BLACKBOX:

Be as clear as possible

End the question in what k
answer to be, e.g: 'connec

or you can just

Here are some

Write a function that rec

How to delete docs fr

Conner

BLACKBOX AI

BÁCH KHOA E-LEARNING



WEBSITE

HCMUT

MyBK

BKSI

CONTACT

📍 268 Ly Thuong Kiet Street Ward 14, District 10, Ho Chi Minh City, Vietnam

☎ (028) 38 651 670 - (028) 38 647 256 (Ext: 5258, 5234)

✉ elearning@hcmut.edu.vn

Blackbox AI Chat is in beta and Blackbox is not liable for the conte
acknowledge that you agree to agree to Blackbox's Terms and Pri

Copyright 2007-2022 BKEL - Power by Moodle